

ACTIVE DIGITAL TAPPED DELAY LINES

(C) DT TYPE

T-47-13

ACTIVE DUAL-IN-LINE PACKAGE

DT05A-□□□(5-TAP TYPE)

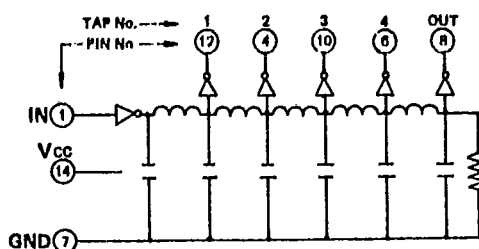
ELECTRICAL SPECIFICATIONS

Part No.	Delay Time T_d (ns)	Tap Delay (ns)				Rise Time T_r (ns) Max	TTL GATE IC
		Tap 1	Tap 2	Tap 3	Tap 4		
DT05A-340	340±5%	25	45	160	185	5	74S04
DT05A-400	400±5%	30	80	220	250	5	74S04
DT05A-440	440±5%	40	105	265	290	5	74S04
DT05A-515	515±5%	85	140	320	345	5	74S04

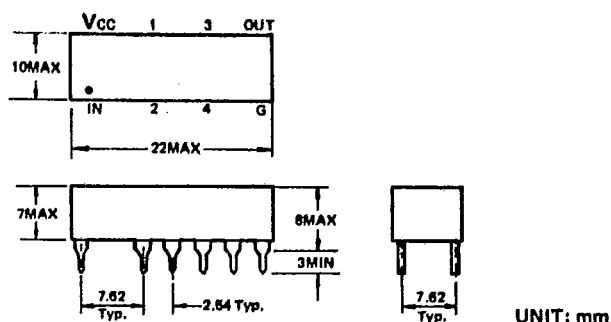
ELECTRICAL SPECIFICATIONS

Part No.	Delay Time T_d (ns)	Delay per Taps (ns)	Rise Time T_r (ns) Max	TTL GATE IC
DT05A-025	25±3	5±2.5	5	74S04
DT05A-050	50±4	10±3	5	74S04
DT05A-075	75±4	15±3	5	74S04
DT05A-100	100±5%	20±3	5	74S04
DT05A-150	150±5%	30±4	5	74S04
DT05A-200	200±5%	40±4	5	74S04
DT05A-250	250±5%	50±5	5	74S04
DT05A-300	300±5%	60±6	5	74S04
DT05A-500	500±5%	100±10	5	74S04

PIN CONNECTIONS



MECHANICAL DIMENSIONS

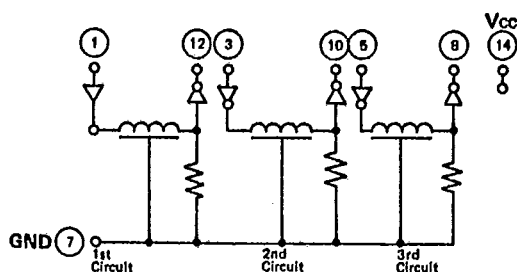


DT03B-□□□(3-TAP TYPE)

ELECTRICAL SPECIFICATIONS

Part No.	Delay Time T_d (ns)	Rise Time T_r (ns) Max	TTL GATE IC
DT03B-050	50±8	5	74S04
DT03B-100	100±7%	5	74S04
DT03B-150	150±7%	7	74S04
DT03B-200	200±7%	7	74S04

PIN CONNECTIONS



MECHANICAL DIMENSIONS

