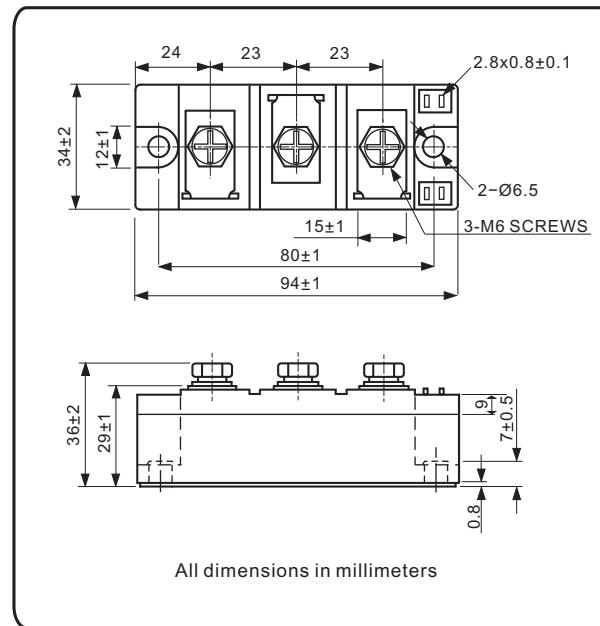


## Thyristor/Diode and Thyristor/Thyristor, 200A (New INT-A-PAK Power Modules)



### FEATURES

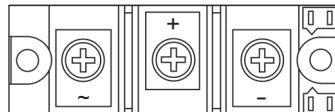
- High voltage
- Electrically isolated by DBC ceramic ( $\text{Al}_2\text{O}_3$ )
- 3500 V<sub>RMS</sub> isolating voltage
- Industrial standard package
- High surge capability
- Glass passivated chips
- Modules uses high voltage power thyristor/diodes in two basic configurations
- Simple mounting
- UL approved file E320098 
- Compliant to RoHS
- Designed and qualified for multiple level



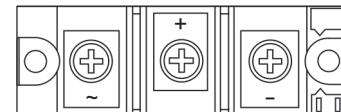
### APPLICATIONS

- DC motor control and drives
- Battery charges
- Welders
- Power converters
- Lighting control
- Heat and temperature control

NKT



NKH



### PRODUCT SUMMARY

I <sub>T(AV)</sub>	200 A
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### MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
I <sub>T(AV)</sub>	85 C	200	A
I <sub>T(RMS)</sub>	85 C	314	
I <sub>TSM</sub>	50 Hz	7200	A
	60 Hz	7560	
I <sup>2</sup> t	50 Hz	259	kA <sup>2</sup> s
	60 Hz	236	
I <sup>2</sup> √t		2592	kA <sup>2</sup> √s
V <sub>DRM</sub> / V <sub>RRM</sub>	Range	400 to 1600	V
T <sub>J</sub>	Range	-40 to 125	C

**ELECTRICAL SPECIFICATIONS**

<b>VOLTAGE RATINGS</b>				
<b>TYPE NUMBER</b>	<b>VOLTAGE CODE</b>	<b><math>V_{RRM}/V_{DRM}</math>, MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V</b>	<b><math>V_{RSM}/V_{DSM}</math>, MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V</b>	<b><math>I_{RRM}/I_{DRM}</math> AT 125 °C mA</b>
NKT200 NKH200	04	400	500	30
	08	800	900	
	12	1200	1300	
	14	1400	1500	
	16	1600	1700	

<b>FORWARD CONDUCTION</b>								
<b>PARAMETER</b>	<b>SYMBOL</b>	<b>TEST CONDITIONS</b>			<b>VALUES</b>	<b>UNITS</b>		
Maximum average on-state current at case temperature	$I_T(AV)$	180° conduction, half sine wave, 50Hz			200	A		
					85	°C		
Maximum RMS on-state current	$I_T(RMS)$	180° conduction, half sine wave, 50Hz, $T_C = 85^\circ C$			314	A		
Maximum peak, one-cycle, on-state non-repetitive surge current	$I_{TSM}$	$t = 10 \text{ ms}$	No voltage reapplied	Sine half wave, initial $T_J = T_J$ maximum	7200	A		
		$t = 8.3 \text{ ms}$			7560			
Maximum $I^2t$ for fusing	$I^2t$	$t = 10 \text{ ms}$	100% $V_{RRM}$ reapplied	Sine half wave, initial $T_J = T_J$ maximum	259	$\text{kA}^2\text{s}$		
		$t = 8.3 \text{ ms}$			236			
		$t = 10 \text{ ms}$			181			
		$t = 8.3 \text{ ms}$			165			
Maximum $I^2\sqrt{t}$ for fusing	$I^2\sqrt{t}$	$t = 0.1 \text{ ms to } 10 \text{ ms}$ , no voltage reapplied			2592	$\text{kA}^2\sqrt{\text{s}}$		
Maximum on-state voltage drop	$V_{TM}$	$I_{TM} = 480 \text{ A}$ , $T_J = 25^\circ C$ , 180° conduction			1.7	V		
Maximum forward voltage drop	$V_{FM}$	$I_{FM} = 480 \text{ A}$ , $T_J = 25^\circ C$ , 180° conduction			1.4			
Maximum holding current	$I_H$	Anode supply = 12 V initial $I_T = 30 \text{ A}$ , $T_J = 25^\circ C$			40~150	mA		
Maximum latching current	$I_L$	Anode supply = 12 V resistive load = 1 Ω Gate pulse: 10 V, 100 μs, $T_J = 25^\circ C$			400			

<b>BLOCKING</b>						
<b>PARAMETER</b>	<b>SYMBOL</b>	<b>TEST CONDITIONS</b>			<b>VALUES</b>	<b>UNITS</b>
Maximum peak reverse and off-state leakage current	$I_{RRM}$ , $I_{DRM}$	$T_J = 125^\circ C$			30	mA
RMS isolation Voltage	$V_{ISO}$	50 Hz, circuit to base, all terminals shorted			2500 (1min) 3500 (1s)	V
Critical rate of rise of off-state voltage	$dV/dt$	$T_J = T_J$ maximum, exponential to 67% rated $V_{DRM}$			500	$V/\mu\text{s}$

TRIGGERING						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum peak gate power	P <sub>GM</sub>	$t_p \leq 5 \text{ ms}$ , T <sub>J</sub> = T <sub>J</sub> maximum		10	W	
Maximum average gate power	P <sub>G(AV)</sub>	f = 50 Hz, T <sub>J</sub> = T <sub>J</sub> maximum		3		
Maximum peak gate current	I <sub>GM</sub>	$t_p \leq 5 \text{ ms}$ , T <sub>J</sub> = T <sub>J</sub> maximum		3	A	
Maximum peak negative gate voltage	- V <sub>GT</sub>			10	V	
Maximum required DC gate voltage to trigger	V <sub>GT</sub>	T <sub>J</sub> = 25°C	Anode supply = 12 V, resistive load; R <sub>a</sub> = 1Ω	0.7~1.8		
Maximum required DC gate current to trigger	I <sub>GT</sub>			30~150	mA	
Maximum gate voltage that will not trigger	V <sub>GD</sub>	T <sub>J</sub> = T <sub>J</sub> maximum, 66.7% V <sub>DRM</sub> applied		0.25	V	
Maximum gate current that will not trigger	I <sub>GD</sub>			10	mA	
Maximum rate of rise of turned-on current	dI/dt	T <sub>J</sub> = 25°C, I <sub>GM</sub> = 1.5A, t <sub>r</sub> ≤ 0.5 μs		150	A/μs	

THERMAL AND MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction operating temperature range	T <sub>J</sub>		- 40 to 125	C
Maximum storage temperature range	T <sub>Stg</sub>		- 40 to 150	
Maximum thermal resistance, junction to case per junction	R <sub>thJC</sub>	DC operation	0.14	C/W
Maximum thermal resistance, case to heatsink per module	R <sub>thCS</sub>	Mounting surface, smooth, flat and greased	0.025	
Mounting torque 10 % IAP to heatsink, M6 busbar to IAP, M6		A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow for the spread of the compound. Lubricated threads.	4 to 6	N.m
Approximate weight			220	g
			7.05	oz.
Case style				New INT-A-PAK

**ORDERING INFORMATION TABLE**

Device code	NKT	200	/	16	A
	(1)	(2)	(3)	(4)	

- [1] - Module type: NKT for (Thyristor + Thyristor) module  
NKH for (Thyristor + Diode) module
- [2] - Current rating: I<sub>T(AV)</sub>
- [3] - Voltage code x 100 = V<sub>RRM</sub>
- [4] - Assembly type,"A" for soldering type

## Nell High Power Products

Fig.1 On-state current vs. voltage characteristic

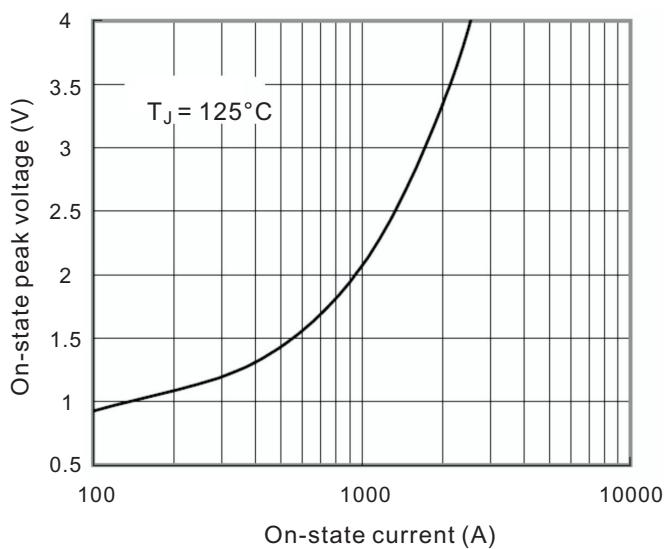


Fig.3 Power consumption vs. average current

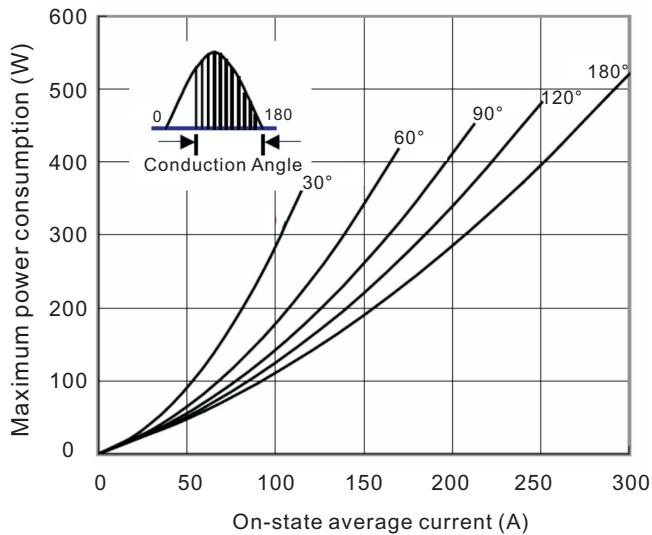


Fig.5 On-state surge current vs cycles

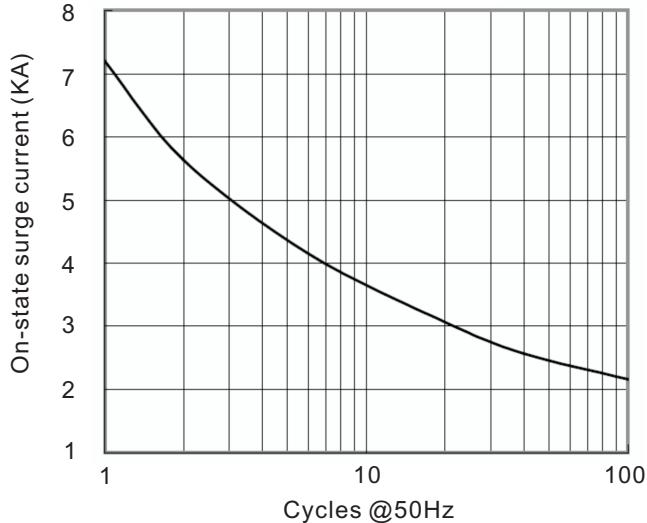


Fig.2 Transient thermal impedance(junction-case)

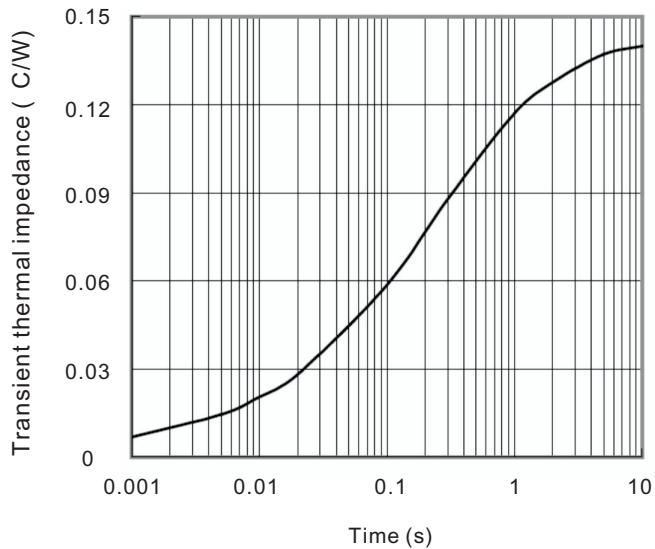


Fig.4 Case temperature vs. on-state average current

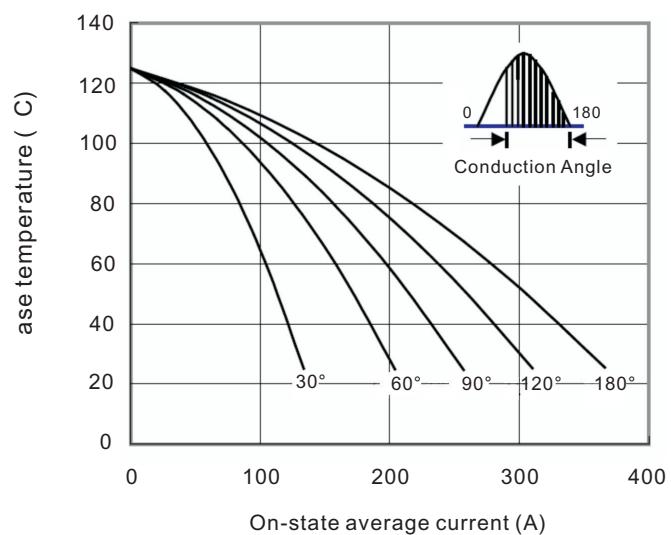


Fig.6  $I^2t$  characteristics

