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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RJK6002DPD

Silicon N Channel MOS FET High Speed Power Switching

REJ03G1483-0200 Rev.2.00 Dec 19, 2008

Features

- Low on-resistance
- Low leakage current
- High speed switching

Outline

RENESAS Package code: PRSS0004ZG-A
(Package name : MP-3A)

2, 4



- Gate
 Drain
- 3. Source
- 4. Drain

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	600	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	2	А
Drain peak current	I _{D (pulse)} Note1	4	А
Body-drain diode reverse drain current	I _{DR}	2	А
Body-drain diode reverse drain peak current	I _{DR (pulse)} Note1	4	А
Avalanche current	I _{AP} Note3	1	Α
Avalanche energy	E _{AR} Note3	0.05	mJ
Channel dissipation	Pch Note2	30	W
Channel to case thermal impedance	θch-c	4.17	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tc = 25°C
- 3. STch = 25° C, Tch $\leq 150^{\circ}$ C

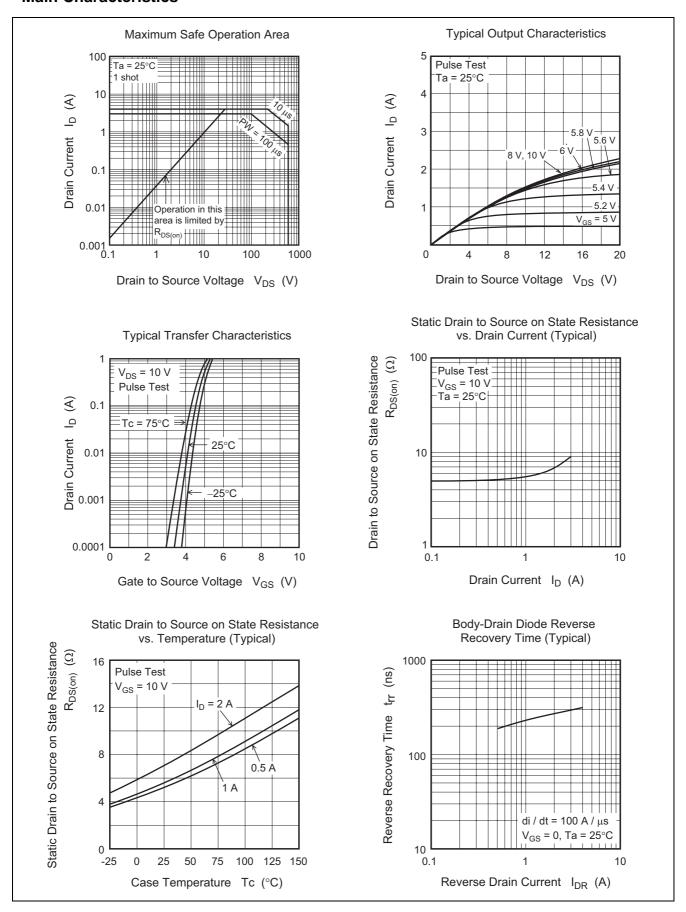
Electrical Characteristics

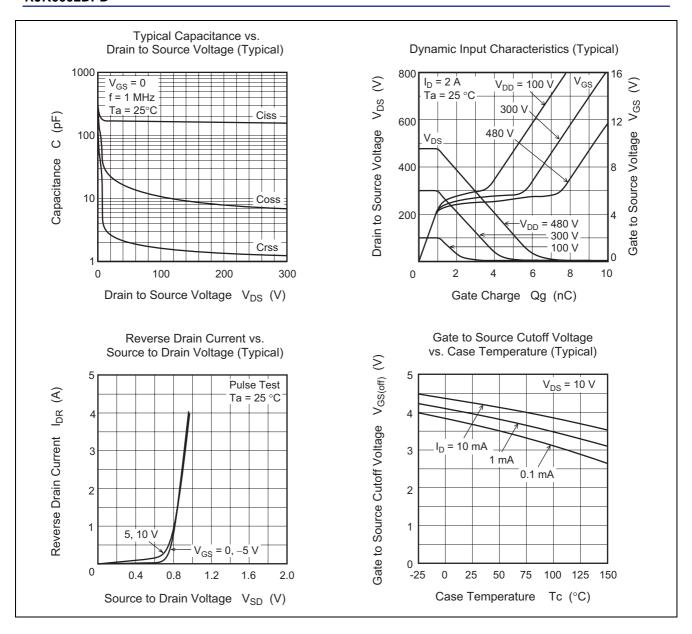
 $(Ta = 25^{\circ}C)$

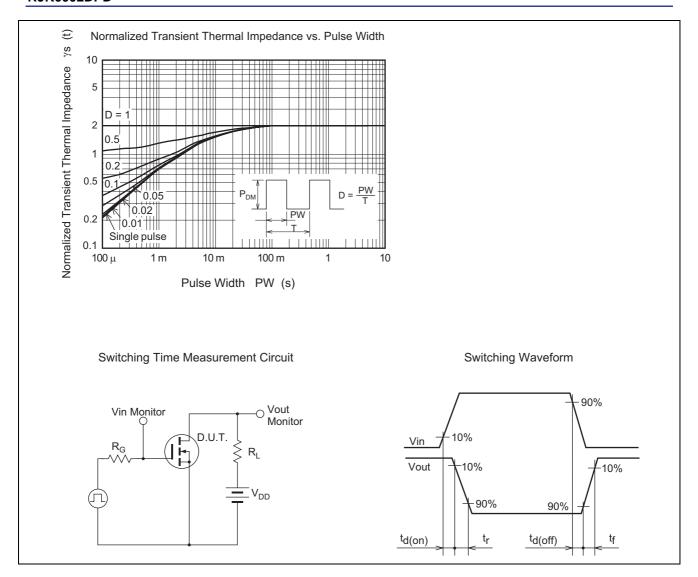
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 600 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	3.0	_	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS(on)}	1	5.7	6.8	Ω	$I_D = 1 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss		165		pF	V _{DS} = 25 V
Output capacitance	Coss	_	20	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	2.5	_	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	_	28	_	ns	I _D = 1 A
Rise time	t _r	_	17	_	ns	V _{GS} = 10 V
Turn-off delay time	$t_{d(off)}$	_	47	_	ns	$R_L = 300 \Omega$
Fall time	t _f	_	20	_	ns	$Rg = 10 \Omega$
Total gate charge	Qg	_	6.2	_	nC	V _{DD} = 480 V
Gate to source charge	Qgs	_	1.1	_	nC	V _{GS} = 10 V
Gate to drain charge	Qgd	_	3.6	_	nC	I _D = 2 A
Body-drain diode forward voltage	V_{DF}	_	0.87	1.45	V	$I_F = 2 A, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery time	t _{rr}	1	260		ns	$I_F = 2 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A/}\mu\text{s}$

Notes: 4. Pulse test

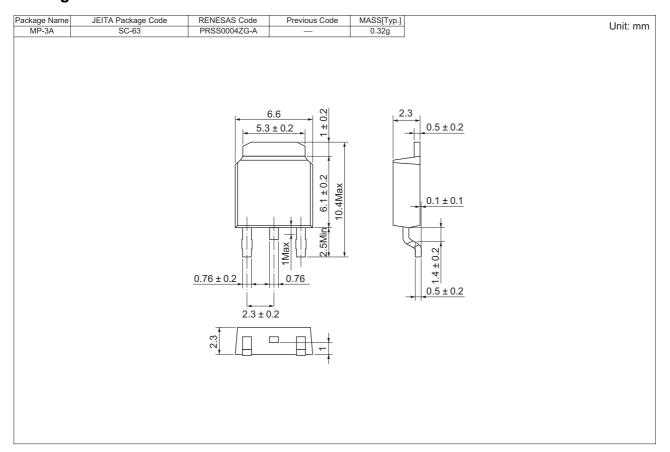
Main Characteristics







Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
RJK6002DPD-00-J2	3000 pcs	Taping

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