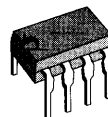


SYNC SEPARATOR AND VIDEO SIGNAL IDENTIFICATION

- AC COUPLED COMPOSITE VIDEO SIGNAL
- COMPOSITE SYNC OUTPUT
- EDGE TRIGGERED VERTICAL SYNC OUTPUT
- AUDIO MUTING OUTPUT



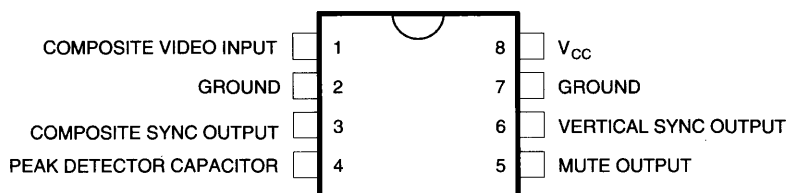
DIP8
(Plastic Package)

ORDER CODE : TDA8128

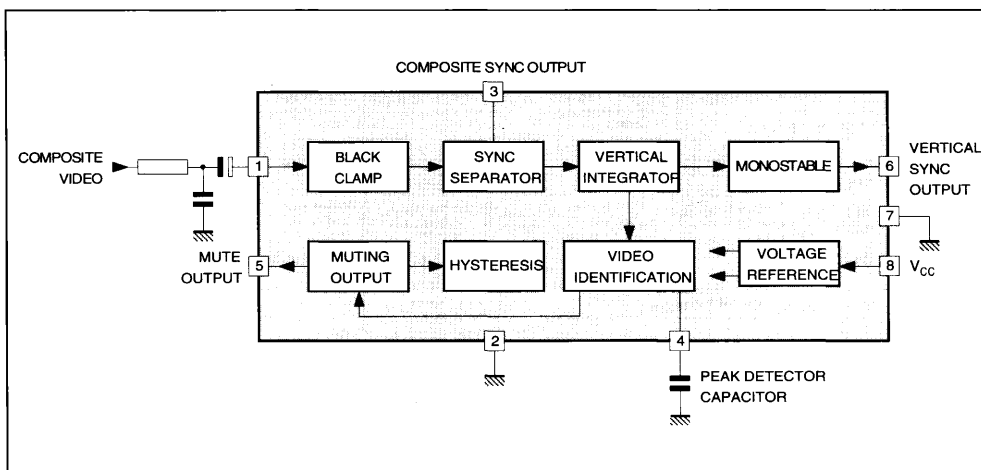
DESCRIPTION

The TDA8128 is a monolithic integrated circuit in DIP8 package. It provides composite sync, edge triggered vertical sync and audio muting signals.

PIN CONNECTIONS



BLOCK DIAGRAM



GENERAL DESCRIPTION

The TDA8128 extracts the composite and vertical sync signals from a video input signal with a negative going horizontal sync pulse.

By means of an internal monostable, the beginning of the vertical sync is triggered by the rising edge of the first serration in the vertical sync period.

The device also provides information when the input signal is not a true video signal or if its amplitude is below a certain limit.

The output stages are supplied by an internal reference voltage so that no external pull-up resistors are needed.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC}	Supply Voltage	15	V
	Sink Current (Pins 3, 5, 6)	20	mA
T _{oper}	Operating Ambient Temperature Range	0 to 70	°C
T _{stg}	Storage Temperature	-40, +150	°C

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THERMAL DATA

Symbol	Parameter	Value	Unit
R _{th (j-a)}	Junction-ambient Thermal Resistance Typ.	90	°C/W

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ELECTRICAL CHARACTERISTICS

V_{CC} = 12V, T_{amb} = 25°C,

Video Signal : standard PAL color bar generator (V_i = 2V_{PP}) (unless otherwise specified)

Symbol	Parameter	Pin	Test Conditions	Min.	Typ.	Max.	Unit
V _s	Supply Voltage	8		10.8	12	13.2	V
I _s	Supply Current	8			8	15	mA
V _{IN}	Input Signal Range	1			2	5	V _{PP}

COMPOSITE SYNC OUTPUT

WH	Min. Horizontal Input Sync Width	3	For stable output signals	4	4.7	5.5	µs
V _{1 min}	Min. Input Sync Pulse Amplitude	1	Stable output signal on Pin 3		330	400	mV _{PP}
V ₃	Pulse Amplitude	3		4.2	4.6	5.2	V
V _{3 sat}	Output Saturation Voltage	3	I _s = 1mA		100	200	mV
R3	Input Pull-up Resistor	3		3.5	5	6.5	kΩ
T3/1	Composite Sync Extract Delay	3/1	At 50% of sync pulse amplitude		1	2	µs

IDENTIFICATION OUTPUT

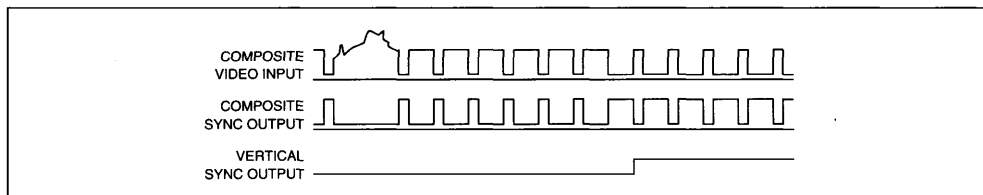
V _{1 ON}	Min. Input Composite Sync Pulse Amplitude	1	Identification output going from no ident to ident		320	370	mV _{PP}
V _{1 OFF}	Max. Input Composite Sync Pulse Amplitude	1	Identification output going from ident to no ident	150	210		mV _{PP}
HYS	Hysteresis	5	By attenuation of the input signal PIn 1		3.5		dB
V _{SH}	Identification Output Voltage	5	Video not identified	4.5	4.9	5.3	V
V _{SL}		5	Video identified, I _s = 1mA		80	200	mV
R5	Internal Pull-up Resistor	5		3.5	4.8	6.5	kΩ
TI	Identification Delay	5/1	Delay between the first incoming inverted line pulse and ident output signal • C4 = 1µF • C4 = 470nF		2 1	3	Frame Frame

8128-03.TBL

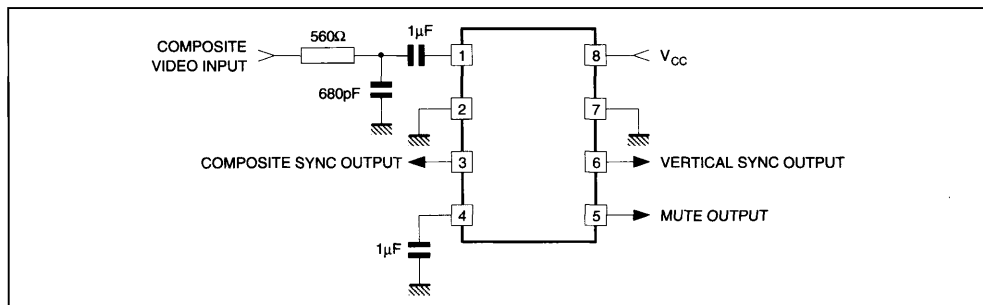
ELECTRICAL CHARACTERISTICS (continued) $V_{CC} = 12V$, $T_{amb} = 25^{\circ}C$,Video Signal : standard PAL color bar generator ($V_i = 2V_{PP}$) (unless otherwise specified)

Symbol	Parameter	Pin	Test Conditions	Min.	Typ.	Max.	Unit
VERTICAL PULSE OUTPUT							
$V_{1\ min}$	Min. Input Composite Sync Pulse Amplitude	1	Stable output pulse (Pin 6) Input pulse increasing		300	350	mV _{PP}
TFR	Vertical Pulse Width	6	Standard PAL color bar pattern ($2V_{PP}$)	100	250	400	μs
V_{6H}	Vertical Pulse Amplitude	6		4.5	4.9	5.3	V
$V_{6\ sat}$	Output Saturation Voltage	6	$I_6 = 1mA$		100	200	mV
R6	Internal Pull-up Resistor	6		3.5	5	6.5	k Ω
TDFR	Vertical Pulse Delay	6/1	Delay between the first incoming inverted line pulse (Pin 1) and vertical pulse at 50% amplitude		1	2	μs

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WAVEFORMS (Pin 7 grounded)

8128-03.EPS

TYPICAL APPLICATION

8128-04.EPS