





Features

- Dual data-rate 1.25Gbps/1.0625Gbps
- Up to 20km transmission on SMF
- 1310nm FP laser and PIN photodetector
- SFP MSA package with duplex LC connector
- Digital diagnostic monitor interface compliant with SFF-8472
- +3.3V single power supply
- Operating case temperature: -5 to +70°C
- RoHS compliant

Regulatory Compliance

Table	1 -	Regul	atory	Compl	iance
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Electrostatic Discharge	MIL-STD-883E	Class 1	
(ESD) to the Electrical Pins	Method 3015.7		
Electrostatic Discharge (ESD) to the	IEC 61000-4-2	Compliant with standards	
Duplex LC Receptacle	1EC 81000-4-2		
Electromagnetic	FCC Part 15 Class B	Compliant with standards	
Interference (EMI)	FCC Fait 15 Class B		
Lagor Evo Sofoty	FDA 21CFR 1040.10 and 1040.11	Compliant with Class I laser	
Laser Eye Safety	EN (IEC) 60825-1,2	product.	
Electrostatic Discharge	MIL-STD-883E	Class 1	
(ESD) to the Electrical Pins	Method 3015.7		

Absolute Maximum Ratings

Table 2 - Absolute Maximum Ratings

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Storage Temperature	Ts	-40	-	+85	°C	
Supply Voltage	V _{cc}	-0.5	-	+3.6	V	
Operating Relative Humidity	RH	+5	-	+95	%	



Recommended Operating Conditions

Table 3 – Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Operating Case Temperature	T _C	-5	-	+70	°C	
Power Supply Voltage	V _{CC}	3.13	3.3	3.47	V	
Power Supply Current	I _{cc}	-	-	300	mA	
Power Dissipation	PD	-	-	1	W	
Data Rate		1.0625	1.25		Gbps	

Optical Characteristics

Table 4 – Optical Characteristics

			ransmitter				
Par	rameter	Symbol	Min.	Typical	Max.	Unit	Notes
Centre Waveleng	ıth	λ _C	1270	1310	1355	nm	
Average Output F	Power	P _{0UT}	-8		-3	dBm	1
P _{0ut} @TX Disable	Asserted	P _{0UT}			-45	dBm	1
Spectral Width (RMS)	Δλ		2	4	nm	
Extinction Ratio		EX	9			dB	
Rise/Fall Time (2	0%~80%)	t _r /t _f			0.26	ns	2
Total Jitter	1.25G	–			0.431		3
Iotal Jitter	1.0625G	— T _J			0.43	- UI	
Deterministic	1.25G	D			0.2	UI	3
Jitter	1.0625G	DJ			0.21	01	3
Optical Eye Mask	<	IEEE	802.3ah and	ANSI Fibre Cl	hannel compl	iant	4
			Receiver				
Centre Waveleng	ıth	λ _C	1260		1570	nm	
Receiver Sensitiv	vity	P _{IN}			-22	dBm	5
Receiver Overloa	ad	P _{IN}	-3			dBm	5
Return Loss			12			dB	
LOS Assert		LOS _A	-35			dBm	
LOS Deassert		LOS _D			-23	dBm	
LOS Hysteresis			0.5		4	dB	
Total Jitter	1.25G	<u>т</u>			0.749	- UI	3
	1.0625G	— T _J			0.61	UI	3
Deterministic	1.25G	D			0.462		2
Jitter	1.0625G	DJ			0.36	UI	3

Notes:



- 1. The optical power is launched into SMF.
- 2. Unfiltered, measured with a PRBS 2⁷-1 test pattern @1.25Gbps
- 3. Meet the specified maximum output jitter requirements if the specified maximum input jitter is present.
- 4. Measured with a PRBS 2⁷-1 test pattern @1.25Gbps/1.0625Gbps.
- 5. Measured with a PRBS 2^7 -1 test pattern @1.25Gbps, worst-case extinction ratio, BER $\leq 1 \times 10^{-12}$.

Electrical Characteristics

Table 5 – Electrical Characteristics

Transmitter								
Parameter	Symbol	Min.	Typical	Max.	Unit	Notes		
Data Input Swing Differential	V _{IN}	500		2400	mV	1		
Input Differential Impedance	Z _{IN}	90	100	110	Ω			
Tx_DIS Disable	V _D	2.0		V _{cc}	V			
Tx_DIS Enable	V _{EN}	GND		GND+0.8	V			
TX_Fault (Fault)		2.0		Vcc+0.3	V			
TX_Fault (Normal)		0		0.8	V			
		Receiver						
Data Output Swing Differential	V _{OUT}	370		2000	mV	1		
Rx_LOS Fault	V _{LOS-Fault}	2.0		Vcc+0.3	V			
Rx_LOS Normal	V _{LOS-Normal}	GND		GND+0.8	V			

Notes:

1. Internally AC coupled

Recommended Host Board Power Supply Circuit

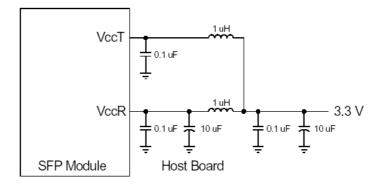


Figure 1, Recommended Host Board Power Supply Circuit



Recommended Interface Circuit

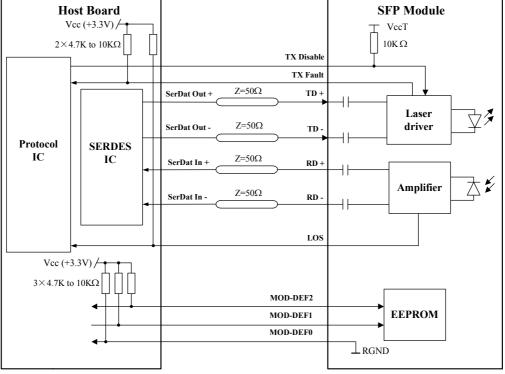


Figure 2, Recommended Interface Circuit

Pin Definitions

Figure 3 below shows the pin numbering of SFP electrical interface. The pin functions are described in Table 6 with some accompanying notes.

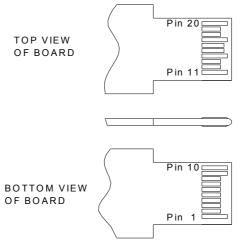


Figure 3, Pin View





Table 6 - Pin Function Definitions

Pin No.	Name	Function	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	
2	TX Fault	Transmitter Fault Indication	3	Note 1
3	TX Disable	Transmitter Disable	3	Note 2
4	MOD-DEF2	Module Definition 2	3	Note 3
5	MOD-DEF1	Module Definition 1	3	Note 3
6	MOD-DEF0	Module Definition 0	3	Note 3
7	Rate Select	Not Connected	3	
8	LOS	Loss of Signal	3	Note 4
9	VeeR	Receiver Ground	1	
10	VeeR	Receiver Ground	1	
11	VeeR	Receiver Ground	1	
12	RD-	Inv. Received Data Out	3	Note 5
13	RD+	Received Data Out	3	Note 5
14	VeeR	Receiver Ground	1	
15	VccR	Receiver Power	2	
16	VccT	Transmitter Power	2	
17	VeeT	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 6
19	TD-	Inv. Transmit Data In	3	Note 6
20	VeeT	Transmitter Ground	1	

Notes:

 TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.

2. TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a $4.7k \sim 10k\Omega$ resistor. Its states are:

Low (0~0.8V):	Transmitter on
(>0.8V, <2.0V):	Undefined
High (2.0~3.465V):	Transmitter Disabled
Open:	Transmitter Disabled

 MOD-DEF 0,1,2 are the module definition pins. They should be pulled up with a 4.7k~10kΩ resistor on the host board. The pull-up voltage shall be VccT or VccR.

 $\ensuremath{\mathsf{MOD}}\xspace{-}\ensuremath{\mathsf{DEF}}\xspace{0}$ is grounded by the module to indicate that the module is present

MOD-DEF 1 is the clock line of two wires serial interface for serial $\ensuremath{\mathsf{ID}}$

MOD-DEF 2 is the data line of two wires serial interface for serial ID

- 4. LOS is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; logic 1 indicates loss of signal. In the low state, the output will be pulled to less than 0.8V.
- 5. These are the differential receiver output. They are internally AC-coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES.



6. These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module.

EEPROM Information

The SFP MSA defines a 256-byte memory map in EEPROM describing the transceiver's capabilities, standard interfaces, manufacturer, and other information, which is accessible over a 2 wire serial interface at the 8-bit address 1010000X (A0h). The memory contents refer to Table 7.

Addr.	Field Size (Bytes)	Name of Field	Hex	Description
0	1	Identifier	03	SFP
1	1	Ext. Identifier	04	MOD4
2	1	Connector	07	LC
3—10	8	Transceiver	00 00 00 02 12 00 01 01	Transmitter Code
11	1	Encoding	01	8B10B
12	1	BR, nominal	0D	1.25Gbps
13	1	Reserved	00	
14	1	Length (9um)-km	14	20km
15	1	Length (9um)	C8	20km
16	1	Length (50um)	00	
17	1	Length (62.5um)	00	
18	1	Length (copper)	00	
19	1	Reserved	00	
20—35	16	Vendor name	53 4F 55 52 43 45 50 48 4F 54 4F 4E 49 43 53 20	"SOURCEPHOTONICS"(ASC II)
36	1	Reserved	00	
37—39	3	Vendor OUI	00 1F 22	
40—55	16	Vendor PN	53 50 47 42 45 4C 58 43 44 46 4D 20 20 20 20 20	"SPGBELXCDFM" (ASC II)
56—59	4	Vendor rev	xx xx xx xx	ASC II ("31 30 20 20" means 1.0 revision)
60-61	2	Wavelength	05 1E	1310nm
62	1	Reserved	00	
63	1	CC BASE	xx	Check sum of bytes 0 - 62
64—65	2	Options	00 1A	LOS, TX_FAULT and TX_DISABLE
66	1	BR, max	00	
67	1	BR, min	00	
68—83	16	Vendor SN	xx xx xx xx xx xx xx xx xx xx xx xx xx x	ASC II .

Table 7 - EEPROM Serial ID Memory Contents (A0h)



		Vendor date		Year (2 bytes), Month (2 bytes), Day (2
84—91	8	code	xx xx xx xx xx xx 20 20	bytes)
92	1	Diagnostic type	68	Diagnostics(Int.Cal)
		Enhanced		Diagnostics(Optional Alarm/warning flags, Soft TX_FAULT and Soft TX_LOS
93	1	option	В0	monitoring)
94	1	SFF-8472	02	Diagnostics(SFF-8472 Rev 9.4)
95	1	CC_EXT	xx	Check sum of bytes 64 - 94
96—255	160	Vendor specific		

Note: The "xx" byte should be filled in according to practical case. For more information, please refer to the related document of SFF-8472 Rev 9.5.

Monitoring Specification

The digital diagnostic monitoring interface also defines another 256-byte memory map in EEPROM, which makes use of the 8 bit address 1010001X (A2h). Please see Figure 4. For detail EEPROM information, please refer to the related document of SFF-8472 Rev 9.5. The monitoring specification of this product is described in Table 8.

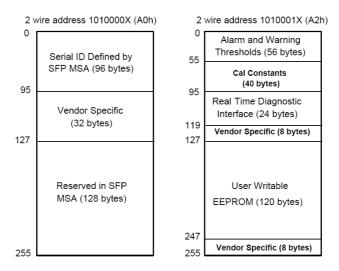




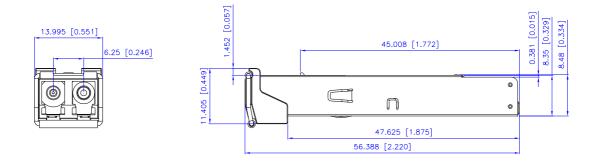
Table 8- Monitoring Specification

Parameter	Range	Accuracy	Calibration
Temperature	-10 to 80°C	±3°C	Internal
Voltage	3.0 to 3.6V	±3%	Internal
Bias Current	0 to 100mA	±10%	Internal
TX Power	-8 to -2 dBm	±3dB	Internal
RX Power	-23 to -2 dBm	±3dB	Internal



Mechanical Diagram





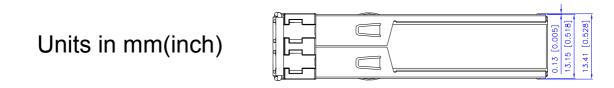


Figure 5, Mechanical Design Diagram of the SFP

Order Information

Table 9 – Order Information

Part No.	Application	Data Rate	Laser Source	Fiber Type
SP-GB-ELX-CDFM	GbE 20Km	1.25Gbps	1310nm FP	SMF

Warnings

Handling Precautions: This device is susceptible to damage as a result of electrostatic discharge (ESD). A static free environment is highly recommended. Follow guidelines according to proper ESD procedures.
Laser Safety: Radiation emitted by laser devices can be dangerous to human eyes. Avoid eye exposure to direct or indirect radiation.



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