

FLASH MEMORY CARD

68-pin PCMCIA Standard (256KB, 512KB, 1, 2 & 4 MBytes)

Pin Assignment

No.	Signal name	No.	Signal name	No.	Signal name
1	GND	24	A5	47	A18 (1)
2	D3	25	A4	48	A19 (2)
3	D4	26	A3	49	A20 (3)
4	D5	27	A2	50	A21 (4)
5	D6	28	A1	51	Vcc
6	D7	29	A0	52	Vpp2
7	CE1*	30	D0	53	NC
8	A10	31	D1	54	NC
9	OE*	32	D2	55	NC
10	A11	33	WP	56	NC
11	A9	34	GND	57	NC
12	A8	35	GND	58	NC
13	A13	36	CD1 (gnd)	59	NC
14	A14	37	D11	60	NC
15	WE*/PGM*	38	D12	61	NC
16	NC	39	D13	62	BVD2 (5)
17	Vcc	40	D14	63	BVD1 (5)
18	Vpp1	41	D15	64	D8
19	A16	42	CE2*	65	D9
20	A15	43	NC	66	D10
21	A12	44	NC	67	CD2 (gnd)
22	A7	45	NC	68	GND
23	A6	46	A17		

- Notes :**
- 1) Not connected for 256KB card.
 - 2) Not connected for 256KB and 512KB cards.
 - 3) Not connected for 256KB, 512KB and 1MB cards.
 - 4) Not connected for 256KB, 512KB, 1MB and 2MB cards.
 - 5) Pulled up to Vcc through resistors.
 - 6) "*" signifies active low signal.

Function Table

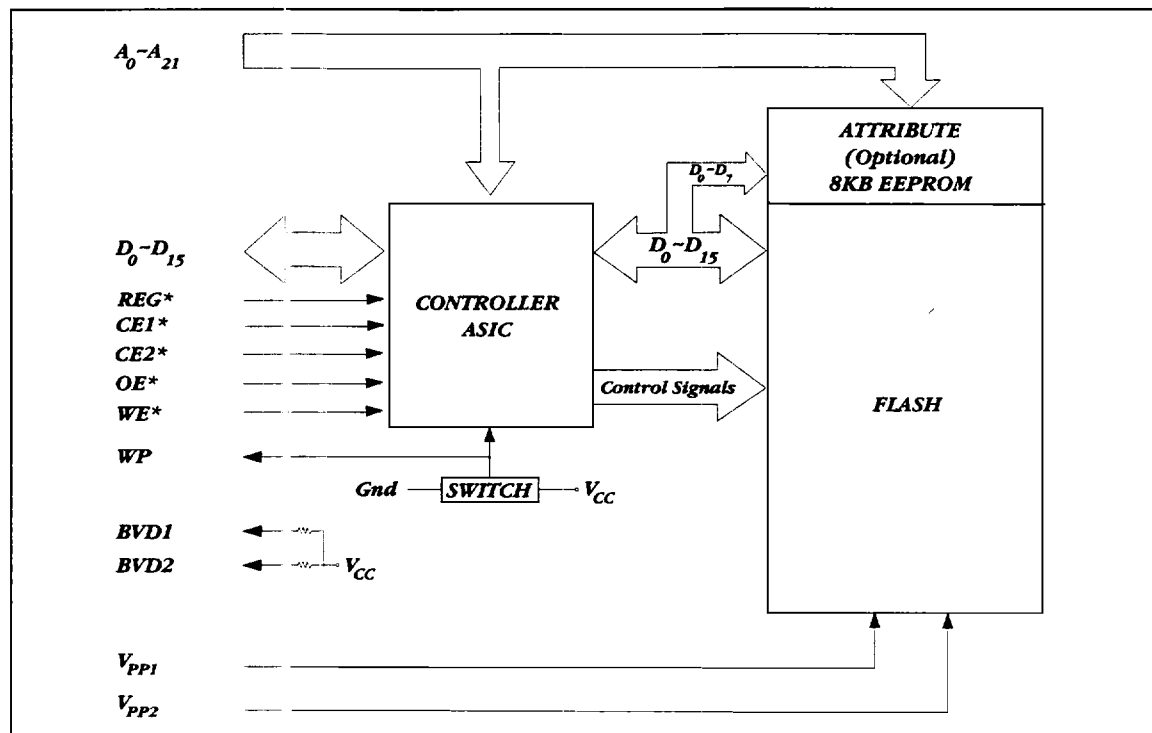
Function	CE2*	CE1*	A0	OE*	WE*	D15-D8	D7-D0
Standby	H	H	X	X	X	High-Z	High-Z
Read							
Byte Access (8 Bits)	H	L	L	L	H	High-Z	Lower Byte
	H	L	H	L	H	High-Z	Upper Byte
Word Access (16 Bits)	L	L	X	L	H	Upper Byte	Lower Byte
Odd-Byte Only Access	L	H	X	L	H	Upper Byte	High-Z
Write							
Byte Access (8 Bits)	H	L	L	H	L	Don't Care	Lower Byte
	H	L	H	H	L	Don't Care	Upper Byte
Word Access (16 Bits)	L	L	X	H	L	Upper Byte	Lower Byte
Odd-Byte Only Access	L	H	X	H	L	Upper Byte	Don't Care

- Notes :**
1. WP (Write Protect) Switch, when asserted, prevents the card from being written to or erased by the host.
 2. Vpp1 and Vpp2 have to be at 12V±5% during a write cycle

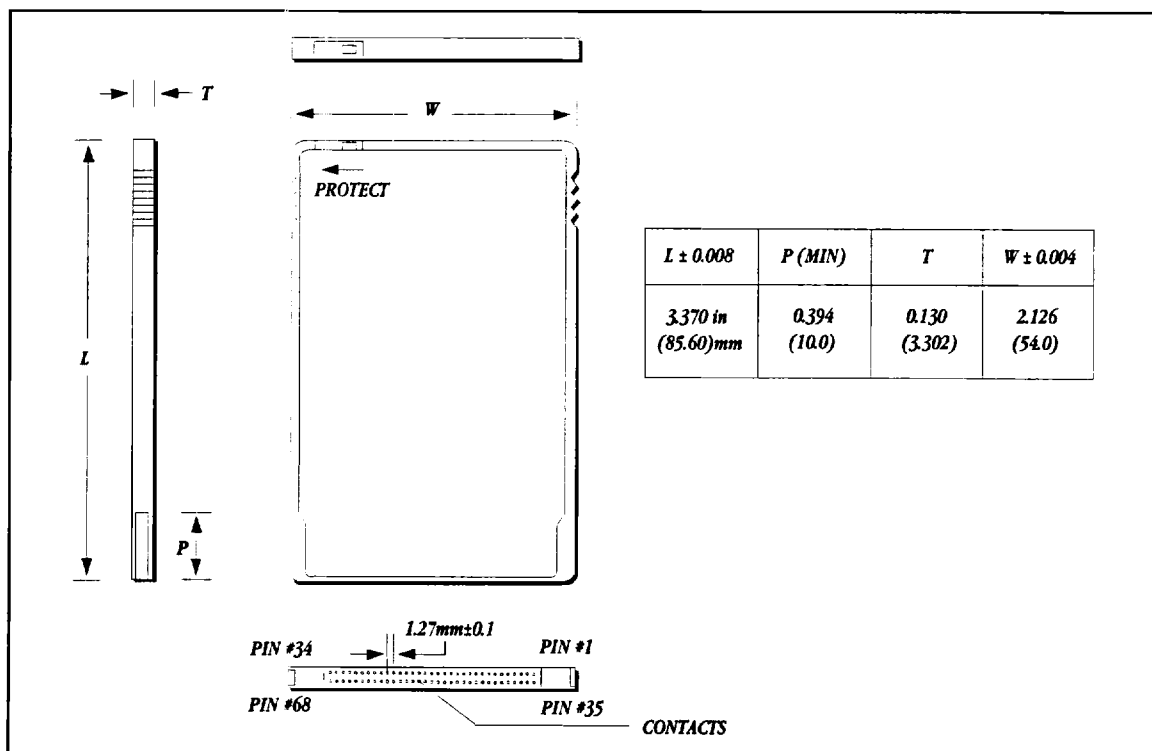
Primary Specifications

Specification	Density	256KByte	512KByte	1MByte	2MByte	4MByte
Operating Voltage		5V±5%				
Current Consumption		Active: 21mA (x16), 11mA (x8) / Standby: 400µA (Typical)				
Interface		Parallel I/O 68-pin PCMCIA (TTL/CMOS)				
Access Time		150/200/250 ns max.				
Operating Temperature Range		0 to 55°C				
Outer Dimensions		3.37" (L) x 2.126" (W) x 0.130" (T)				
Weight		35 gms (approx.)				

Block Diagram



Outer Dimensions



Ordering Information

