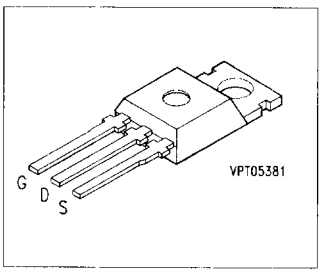
SIEMENS

SIPMOS® Power Transistors

BUZ 73 AL
BUZ 73 L

- N channel
- Enhancement mode
- Avalanche-rated
- Logic Level



Type	V_{DS}	I_D	T_C	$R_{DS\,(on)}$	Package [1]	Ordering Code
BUZ 73 AL	200 V	5.5 A	37 °C	0.4 Ω	TO-220 AB	C67078-S1328-A3
BUZ 73 L	200 V	7.0 A	28 °C	0.6 Ω	TO-220 AB	C67078-S1328-A2

Maximum Ratings

Parameter	Symbol	BUZ		Unit
		73 AL	73 L	
Continuous drain current	I_D	**5.5**	**7.0**	A
Pulsed drain current, T_C = 25 °C	$I_{D\,puls}$	**22**	**28**	
Avalanche current, limited by $T_{j\,max}$	I_{AR}	**7**		
Avalanche energy, periodic limited by $T_{j\,(max)}$	E_{AR}	**6.5**		mJ
Avalanche energy, single pulse I_D = 7 A, V_{DD} = 50 V, R_{GS} = 25 Ω L = 3.67 mH, T_j = 25 °C	E_{AS}	**120**		
Gate-source voltage	V_{GS}	± **10**		V
Gate-source peak voltage, aperiodic	V_{gs}	± **20**		
Power dissipation, T_C = 25 °C	P_{tot}	**40**		W
Operating and storage temperature range	T_j , T_{stg}	**– 55 ... + 150**		°C
Thermal resistance, chip-case	$R_{th\,JC}$	≤ **3.1**		K/W
DIN humidity category, DIN 40 040	–	**E**		–
IEC climatic category, DIN IEC 68-1	–	**55/150/56**		

1) See chapter Package Outlines.

Electrical Characteristics

at T_j = 25 °C, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Static characteristics					
Drain-source breakdown voltage V_{GS} = 0 V, I_D = 0.25 mA	$V_{(BR)\,DSS}$	200	–	–	V
Gate threshold voltage V_{GS} = V_{DS} , I_D = 1 mA	$V_{GS\,(th)}$	1.5	2.0	2.5	
Zero gate voltage drain current V_{DS} = 200 V, V_{GS} = 0 V T_j = 25 °C T_j = 125 °C	I_{DSS}	 – –	 0.1 10	 1.0 100	µA
Gate-source leakage current V_{GS} = 20 V, V_{DS} = 0 V	I_{GSS}	–	10	100	nA
Drain-source on-resistance V_{GS} = 5 V, I_D = 3.5 A BUZ 73 AL BUZ 73 L	$R_{DS\,(on)}$	 – –	 0.5 0.3	 0.6 0.4	Ω
Dynamic characteristics					
Forward transconductance $V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$, I_D = 3.5 A	g_{fs}	5.0	6.5	–	S
Input capacitance V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz	C_{iss}	–	650	850	pF
Output capacitance V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz	C_{oss}	–	120	200	
Reverse transfer capacitance V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz	C_{rss}	–	55	95	
Turn-on time t_{on} , ($t_{on} = t_{d\,(on)} + t_r$) V_{DD} = 30 V, V_{GS} = 5 V, I_D = 3 A, R_{GS} = 50 Ω	$t_{d\,(on)}$	–	15	20	ns
	t_r	–	60	90	
Turn-off time t_{off} , ($t_{off} = t_{d\,(off)} + t_f$) V_{DD} = 30 V, V_{GS} = 5 V, I_D = 3 A, R_{GS} = 50 Ω	$t_{d\,(off)}$	–	100	130	
	t_f	–	40	50	

Electrical Characteristics (cont'd)

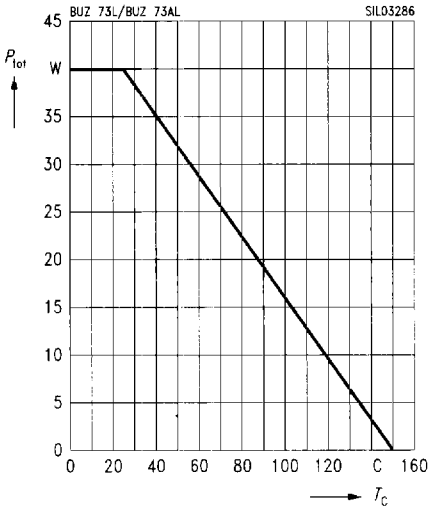
at T_j = 25 °C, unless otherwise specified.

Parameter		Symbol	Values			Unit
			min.	typ.	max.	
Reverse diode						
Continuous reverse drain current		I_S				A
T_C = 25 °C	BUZ 73 AL		–	–	5.5	
	BUZ 73 L		–	–	7.0	
Pulsed reverse drain current		I_{SM}				
T_C = 25 °C	BUZ 73 AL		–	–	22	
	BUZ 73 L		–	–	28	
Diode forward on-voltage I_S = 14 A, V_{GS} = 0 V		V_{SD}	–	1.1	1.7	V
Reverse recovery time V_R = 100 V, $I_F = I_S$, di_F/dt = 100 A/µs		t_{rr}	–	140	–	ns
Reverse recovery charge V_R = 100 V, $I_F = I_S$, di_F/dt = 100 A/µs		Q_{rr}	–	0.70	–	µC

Characteristics at T_j = 25 °C, unless otherwise specified.

Total power dissipation

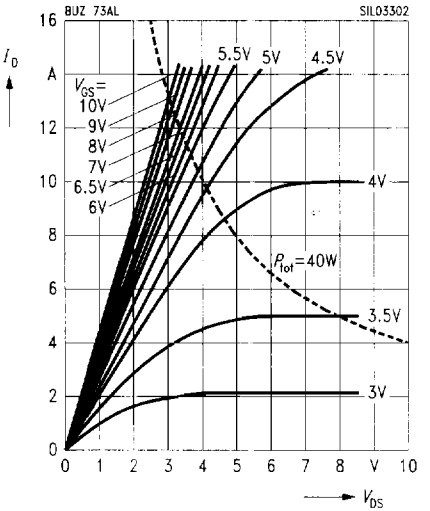
$P_{tot} = f(T_C)$



Typ. output characteristics

$I_D = f(V_{DS})$ **BUZ 73 AL**

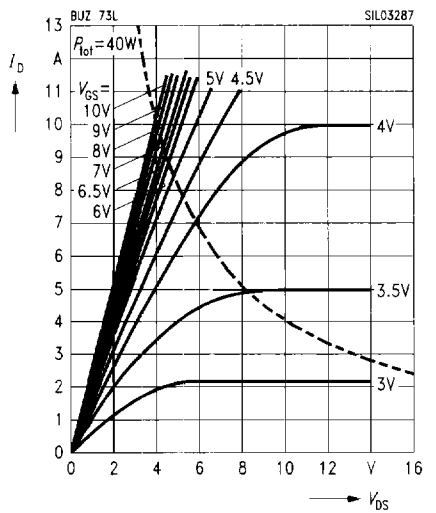
parameter: t_p = 80 µs



Typ. output characteristics

$I_D = f(V_{DS})$ **BUZ 73 L**

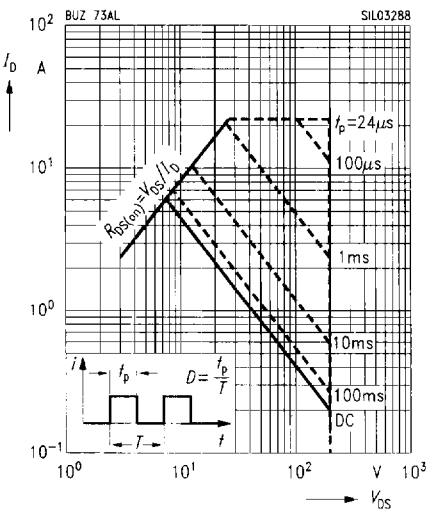
parameter: t_p = 80 µs



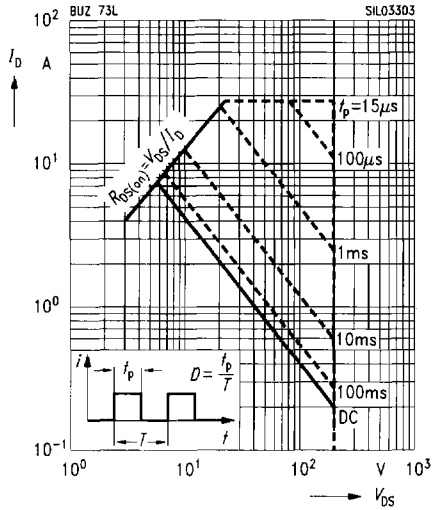
Safe operating area

$I_D = f(V_{DS})$ **BUZ 73 AL**

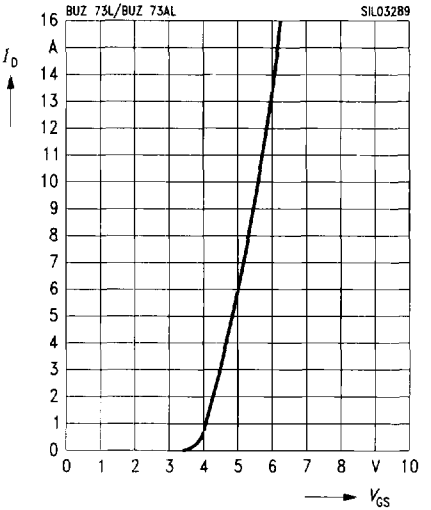
parameter: D = 0.01, T_C = 25 °C



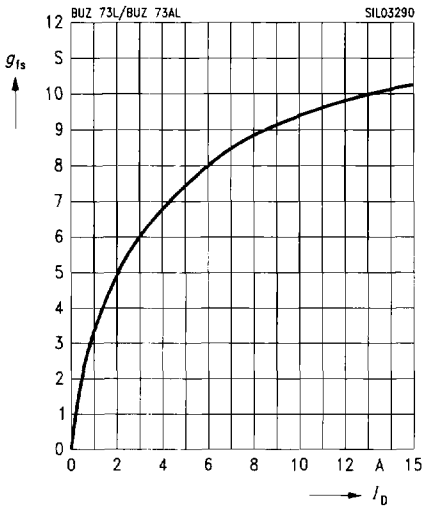
Safe operating area
$I_D = f(V_{DS})$ **BUZ 73 L**
parameter: D = 0.01, T_C = 25 °C



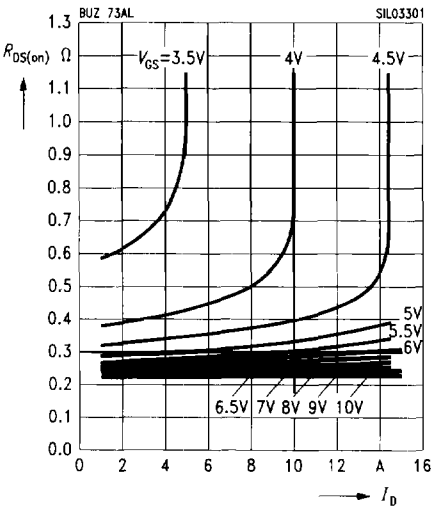
Typ. transfer characteristics
$I_D = f(V_{GS})$
parameter: t_p = 80 µs, V_{DS} = 25 V



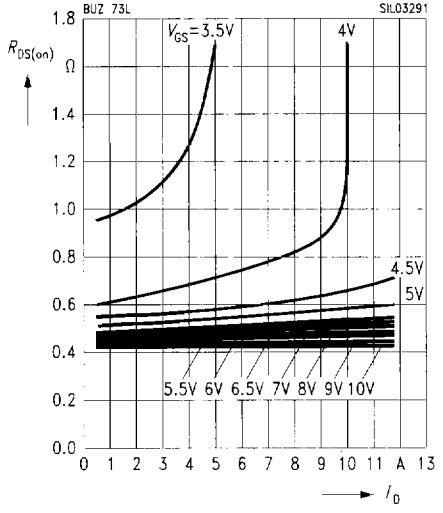
Typ.forward transconductance
$g_{fs} = f(I_D)$
parameter: t_p = 80 µs



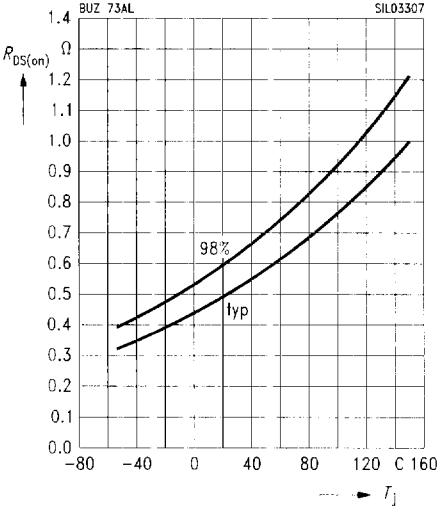
Typ. drain-source on-resistance
$R_{DS\,(on)} = f(I_D)$ **BUZ 73 AL**
parameter: V_{GS}



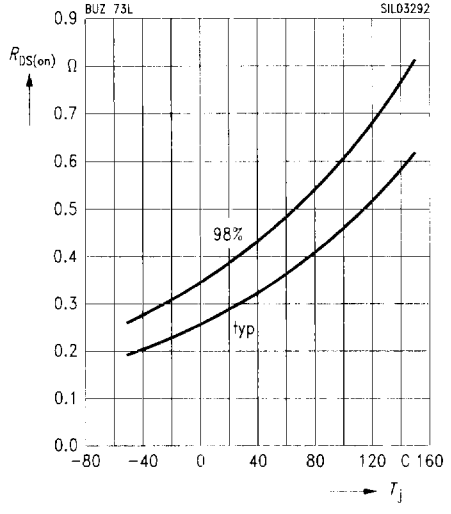
Typ. drain-source on-resistance
$R_{DS\,(on)} = f(I_D)$ **BUZ 73 L**
parameter: V_{GS}



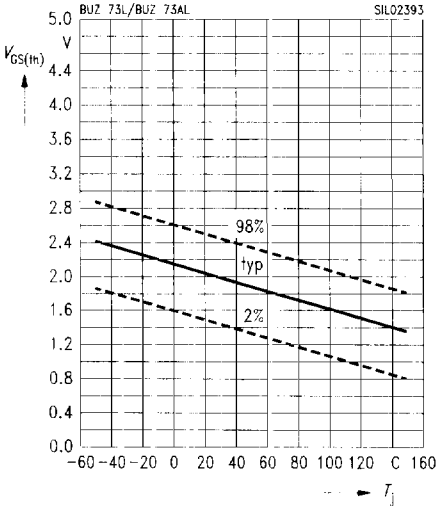
Drain-source on-resistance
$R_{DS\,(on)} = f(T_j)$ **BUZ 73 AL**
parameter: I_D = 4.5 A, V_{GS} = 5 V, (spread)



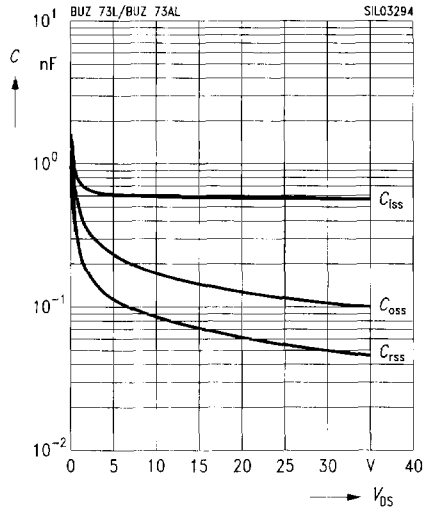
Drain-source on-resistance
$R_{DS\,(on)} = f(T_j)$ **BUZ 73 L**
parameter: I_D = 4.5 A, V_{GS} = 5 V, (spread)



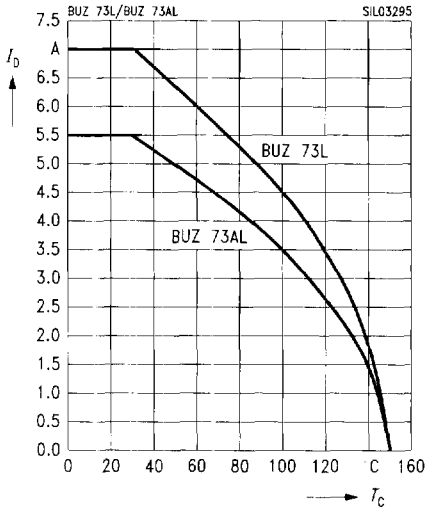
Gate threshold voltage
$V_{GS\,(th)} = f(T_j)$
parameter: $V_{GS} = V_{DS}$, I_D = 1 mA, (spread)



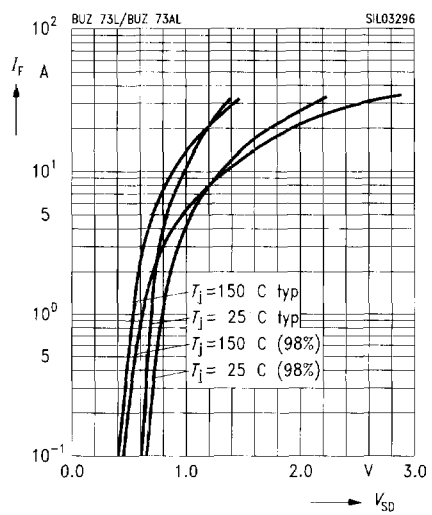
Typ. capacitances
$C = f(V_{DS})$
parameter: V_{GS} = 0 V, f = 1 MHz



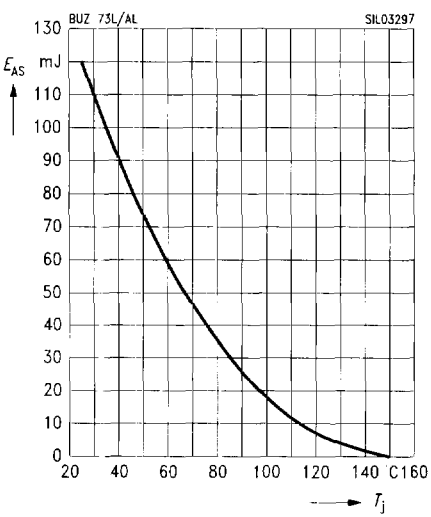
Drain current
$I_D = f(T_C)$
parameter: $V_{GS} \geq 5$ V



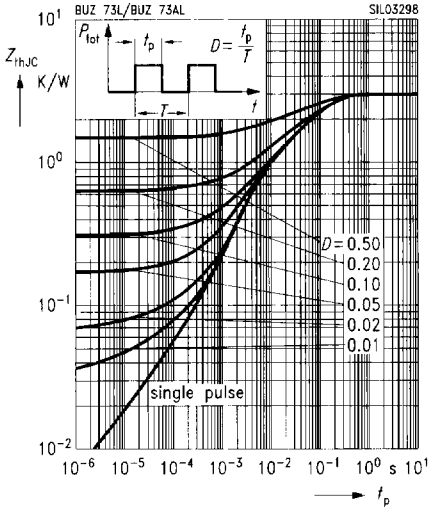
Forward characteristics of reverse diode
$I_F = f(V_{SD})$
parameter: T_j , t_p = 80 µs



Avalanche energy $E_{AS} = f(T_j)$
parameter: I_D = 7 A, V_{DD} = 50 V
R_{GS} = 25 Ω, L = 3.67 mH



Transient thermal impedance
$Z_{th\,JC} = f(t_p)$
parameter: $D = t_p / T$



Typ. gate charge
$V_{GS} = f(Q_{Gate})$
parameter: $I_{D\,puls}$ = 10.5 A