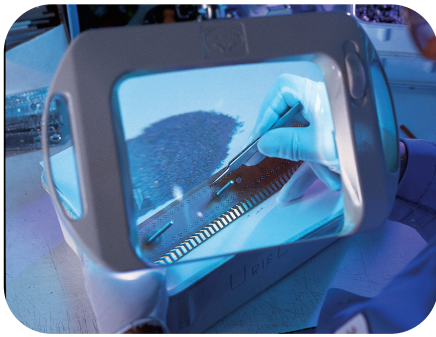
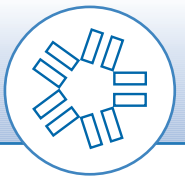
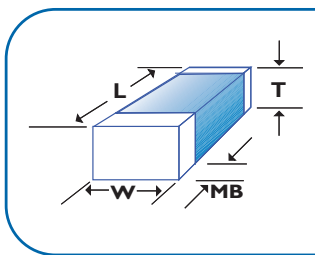


THIN PROFILE CAPACITORS



Popular EIA size chips are offered in very thin profile configuration with COG, X7R, Z5U and Y5V dielectric characteristics rated at 5 Vdc to 50 Vdc, for use as decoupling capacitors under other circuit elements, or for low profile RFID and “Smart Card” circuitry.



MAX CAP & VOLTAGE

MAX CAPACITANCE AT DISCRETE THICKNESS

3 digit code: two significant digits, followed by number of zeros
eg: 472= 4700 pF. R denotes decimal, eg. 2R7 = 2.7 pF

MAXIMUM THICKNESS								
SIZE	0805			1206			1210	
INCHES	.015	.018	.015	.018	.020	.015	.018	.020
MM	.381	.457	.381	.457	.508	.381	.457	.508
COG DIELECTRIC								
5V	102	152	222	332	392	392	682	682
16V	102	152	222	332	392	392	682	682
25V	821	122	182	272	272	332	472	562
50V	561	821	122	182	222	272	392	472
X7R DIELECTRIC								
5V	223	333	473	683	823	823	124	154
16V	223	333	473	683	683	683	104	124
25V	153	223	333	563	683	683	104	124
50V	153	223	333	563	683	683	104	124
Y5V/Z5U DIELECTRIC								
5V	184	274	274	474	564	564	824	105
16V	184	274	274	474	564	564	824	105
25V	124	224	224	394	394	474	684	824
50V	823	124	184	274	334	394	564	684

HOW TO ORDER

0805	B	103	K	250	N	X015	T
SIZE See Chart	DIELECTRIC N = COG B = X7R Y = Y5V Z = Z5U	CAPACITANCE Value in PicoFarads Two significant figures, followed by number of zeros: 103 = 10,000 pF	TOLERANCE J = +/- 5 % K = +/- 10 % COG, X7R ONLY M = +/- 20 % Z = +80% -20% P = +100% -0%	VOLTAGE-VDC Two significant figures, followed by number of zeros: 250 = 25V	TERMINATION N = Nickel Barrier (100% Tin) P = Palladium Silver Y = Nickel Barrier (90 Tin/10 lead)	THICKNESS OPTION X = Non-standard thickness. Specify in Mils if non-standard is required.	PACKING OPTION * T = Reeled *(Consult Factory)