

RJK0328DPB-01

Silicon N Channel Power MOS FET Power Switching

R07DS0264EJ0500 (Previous: REJ03G1637-0400) Rev.5.00 Mar 01, 2011

Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance

 $R_{DS(on)}$ = 1.6 m Ω typ. (at V_{GS} = 10 V)

- Pb-free
- Halogen-free

Outline

RENESAS Package code: PTZZ0005DA-A (Package name: LFPAK)

5
D
1, 2, 3 Source
4 Gate
5 Drain

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	60	А
Drain peak current	I _{D(pulse)} Note1	240	А
Body-drain diode reverse drain current	I _{DR}	60	А
Avalanche current	I _{AP} Note 2	30	А
Avalanche energy	E _{AR} Note 2	90	mJ
Channel dissipation	Pch Note3	65	W
Channel to case thermal resistance	θch-c Note3	1.93	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. $Tc = 25^{\circ}C$

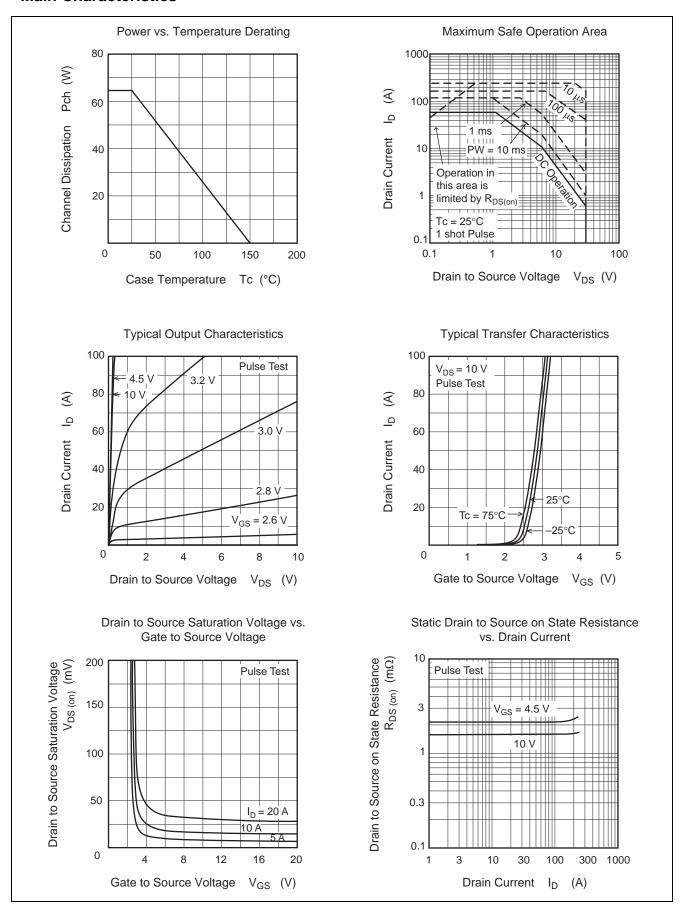
Electrical Characteristics

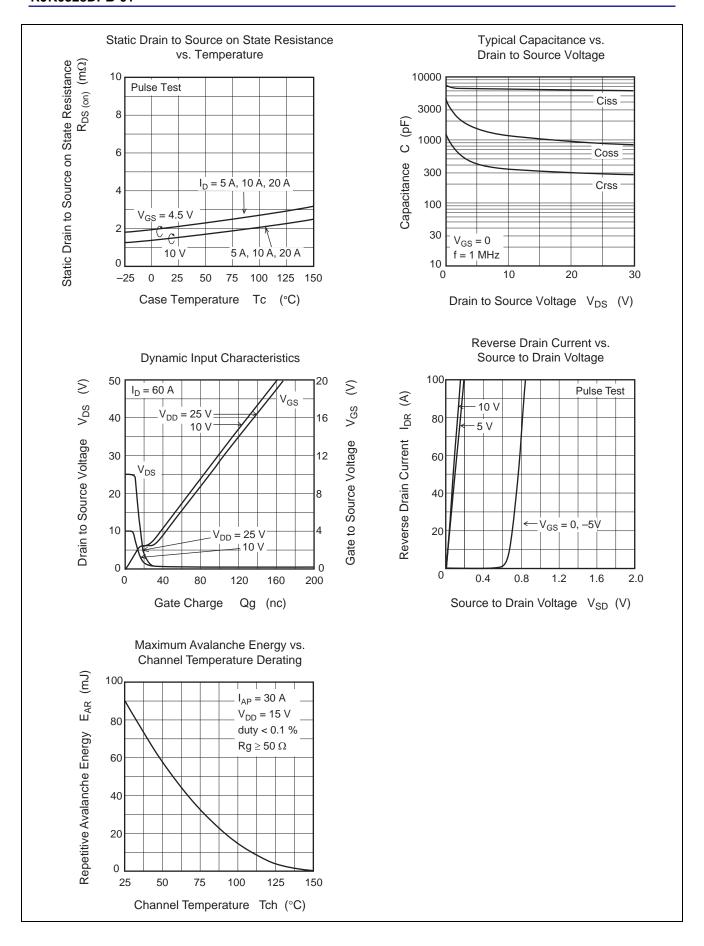
 $(Ta = 25^{\circ}C)$

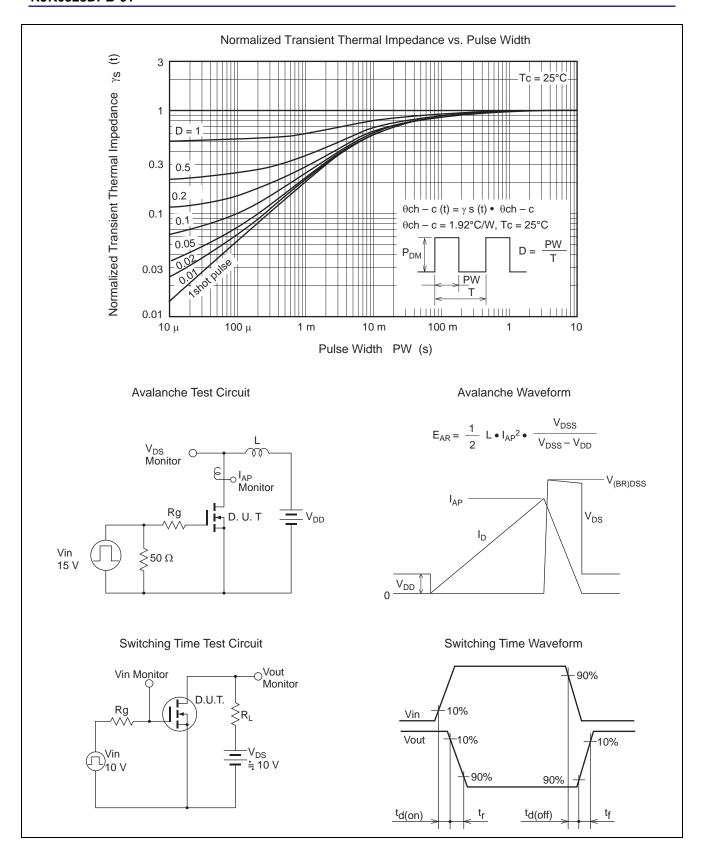
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	1.6	2.1	mΩ	$I_D = 30 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	2.1	2.9	mΩ	$I_D = 30 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	_	100	_	S	$I_D = 30 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	6380	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	1150	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	330	_	pF	
Gate Resistance	Rg	_	0.7	_	Ω	
Total gate charge	Qg	_	42	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$
Gate to source charge	Qgs	_	15	_	nC	I _D = 60 A
Gate to drain charge	Qgd	_	8.8	_	nC	1
Turn-on delay time	t _{d(on)}	_	9.4	_	ns	$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A},$
Rise time	t _r	_	4.3	_	ns	$V_{DD}\cong 10~V,~R_L=0.33~\Omega,$
Turn-off delay time	t _{d(off)}	_	61.5	_	ns	$Rg = 4.7 \Omega$
Fall time	t _f	_	7.3	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.78	1.02	V	$I_F = 60 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery	t _{rr}	_	42	_	ns	I _F = 60 A, V _{GS} = 0
time						di _F / dt = 100 A/ μs
Body–drain diode reverse recovery charge	Q _{rr}	_	46	_	nC	

Notes: 4. Pulse test

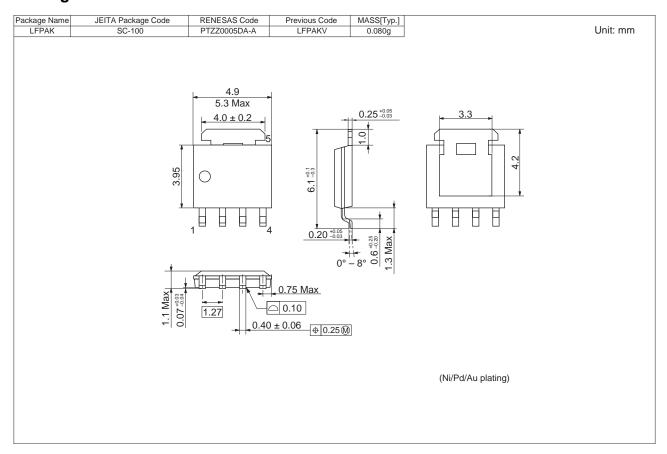
Main Characteristics







Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK0328DPB-01-J0	2500 pcs	Taping

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