

Async Boost with 600KHz switching

FEATURES

- PFM control Asynchronous Booster
- Up to 86% Efficiency
- Quiescent Supply Current: 4.5μA
- Zero Shutdown current
- Output Voltage Accuracy: ±1%
- Fixed Output Voltage
- Provides 20mA from single AAA cell
- Input Voltage Range: 0.8V to 5.5V
- Startup Voltage: 0.9V
- 6L SOT-26 Package

APPLICATIONS

- Cellular and cordless phones
- Wireless LAN cards
- Palmtop computers
- Personal communication equipment
- Pen drives
- Bluetooth devices
- .

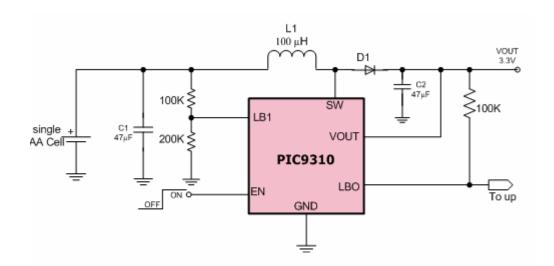
DESCRIPTION

The PIC9310 compact, high-efficiency, step-up DC-DC converters are available in tiny, 6-pin thin SOT26 packages. They feature an extremely low 5µA quiescent supply current to ensure the highest possible light-load efficiency. Optimized for operation from one to two alkaline or nickel-metal-hydride (NiMH) cells, or a single Li+ cell, these devices are ideal for applications where extremely low quiescent current and ultra-small size are critical.

The PIC9310 also feature proprietary noisereduction circuitry, which suppresses electromagnetic interference (EMI) caused by the inductor in many step-up applications. The family offers different combinations of fixed outputs, shutdown, and EMI reduction (see Selector Guide).

Its unique design is optimized for high efficiency at light load condition.

TYPICAL APPLICATION CIRCUIT



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ELECTRICAL CHARACTERISTICS

(Typical values are at T_A = +25°C. V_{BATT} = 1.2V, V_{OUT} = 3.3V, V_{OUT} = $V_{OUT(NOM)}$, SHDN = OUT, R_L = ∞ , unless otherwise noted.)

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Operating Input Voltage	V _{IN}	(Note 1)			5.5	٧
Output Voltage variation	V _{out}		3.224	3.307	3.389	٧
Minimum Startup Input Voltage		I _{OUT} =1mA, V _{IN} :0 -> 2V		0.86	0.92	V
Hold on Voltage		I _{OUT} =1mA, V _{IN} :0 -> 2V			0.7	٧
No Load Input Current		I _{OUT} =0mA		10		μA
N-Channel On-Resistance	R _{DS(ON)}	Measured from SW pin voltage and inductor current during SW on time		1.4		Ω
Maximum oscillating freq	Fosc	V_{OUT} forced = V_{OUT} (NOM) * 0.95	75	107	140	KHz
Duty ratio	D _{MAX}	V_{OUT} forced = V_{OUT} (NOM) * 0.95	70	77	85	%
Efficiency	Eff			86		%
Quiescent Current into OUT (no switching)		$V_{IN} = V_{OUT} + 0.5V (V_{OUT} pin)$ (Notes 2, 3)		4.5		μΑ
Quiescent Current into OUT		V_{OUT} forced = V_{OUT} (NOM) * 0.95		13		μΑ
Switch leakage		VSW=5.5V,VIN= VOUT + 0.5V		1		nA
SHDN Voltage Threshold	V _{IL}		1.2		0.4	mV
SHDN Input Bias current	- 111	V _{SHDN} = 5.5V		1.1		nA
LBI voltage threshold		V_{OUT} forced = V_{OUT} (NOM)*1.05	645	670	700	mV
LBI input hysteresis		V_{OUT} forced = V_{OUT} (NOM)*1.05		18		mV
LBI input current		V_{OUT} forced = V_{OUT} (NOM)*1.05		0.02		nA
LBO output low voltage		Force 20mA into LBO		0.5	0.6	V

Note 1: Guaranteed with the addition of a Schottky MBR0520L external diode between LX and OUT when using the PIC9310 with only one cell, and assumes a 0.3V voltage drop across the Schottky diode.

Note 2: Supply current is measured with an ammeter between the output and OUT pin. This current correlates Directly with actual battery supply current, but is reduced in value according to the step-up ratio and efficiency.

Note 3: V_{OUT} forced to the following conditions to inhibit switching: $V_{OUT} = 1.05 * V_{OUT(NOM)}$, $V_{OUT} = 3.307V$.