

DIM400PBM17-A000

IGBT Bi-Directional Switch Module

Preliminary Information

DS5524-2.2 July 2002

Replaces issue June 2002, version DS5524-2.0

FEATURES

- 10us Short Circuit Withstand
- High Thermal Cycling Capability
- Non Punch Through Silicon
- Isolated MMC Base with AIN Substrates

APPLICATIONS

- Matrix Converters
- Brushless Motor Controllers
- Frequency Converters

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 3300V and currents up to 2400A.

The DIM400PBM17-A000 is a bi-directional 1700V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) switch. The IGBT has a wide reverse bias safe operating area (RBSOA) plus full 10 μ s short circuit withstand. This module is optimised for applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM400PBM17-A000

Note: When ordering, please use the whole part number.

KEY PARAMETERS

V_{DRM}		±1700V
V	(typ)	4.9V
I _c	(max)	400A
I _{C(PK)}	(max)	A008

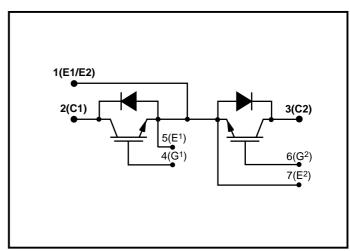


Fig. 1 Bi-directional switch circuit diagram

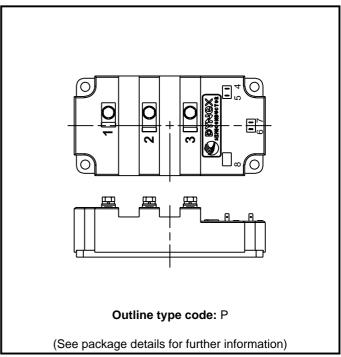


Fig. 2 Electrical connections - (not to scale)



ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
$V_{_{\mathrm{DRM}}}$	Off-state repetitive maximum voltage	$V_{GE} = 0V$	±1700	V
	(measured across terminals 2 and 3)			
$V_{\sf GES}$	Gate-emitter voltage	-	±20	V
I _c	Continuous collector current	$T_{case} = 75^{\circ}C$	400	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 110°C	800	Α
P_{max}	Max. transistor power dissipation	$T_{\text{case}} = 25^{\circ}\text{C}, T_{\text{j}} = 150^{\circ}\text{C}$	3470	W
l²t	Diode I ² t value	$V_R = 0, t_p = 10 \text{ms}, T_{vj} = 125^{\circ}\text{C}$	30	kA ² s
V _{isol}	Isolation voltage - per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V
$Q_{_{PD}}$	Partial discharge - per module	IEC1287. V ₁ = 1800V, V ₂ = 1300V, 50Hz RMS	10	PC



THERMAL AND MECHANICAL RATINGS

Internal insulation material: AIN
Baseplate material: AISiC
Creepage distance: 20mm
Clearance: 10mm
CTI (Critical Tracking Index): 175

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
R _{th(j-c)}	Thermal resistance - transistor (per arm)	Continuous dissipation -	-	-	36	°C/kW
		junction to case				
R _{th(j-c)}	Thermal resistance - diode (per arm)	Continuous dissipation -	-	-	80	°C/kW
		junction to case				
R _{th(c-h)}	Thermal resistance - case to heatsink	Mounting torque 5Nm	-	-	8	°C/kW
	(per module)	(with mounting grease)				
T _j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
-	Screw torque	Mounting - M6	-	-	5	Nm
		Electrical connections - M4	-	-	2	Nm
		Electrical connections - M8	-	-	10	Nm



ELECTRICAL CHARACTERISTICS

 $T_{case} = 25$ °C unless stated otherwise.

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Units
I _{CES}	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$		-	-	1	mA
		$V_{GE} = 0V$, $V_{CE} = V_{CES}$, $T_{case} = 125$	s°C	-	-	12	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$		-	-	2	μА
V _{GE(TH)}	Gate threshold voltage	$I_C = 20$ mA, $V_{GE} = V_{CE}$		4.5	5.5	6.5	V
V _{CE(sat)}	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 400A		-	2.7	3.2	V
		$V_{GE} = 15V, I_{C} = 400A, T_{case} = 125$	5°C	-	3.4	4.0	V
V _T	On-state voltage	V _{GE} = 15V, I _C = 400A		-	4.9	-	V
	(measured across terminals 2 and 3)	$V_{GE} = 15V, I_{C} = 400A, T_{case} = 128$	5°C	-	5.7	-	V
I _F	Diode forward current	DC		-	-	400	А
I _{FM}	Diode maximum forward current	t _p = 1ms		-	-	800	А
V _F	Diode forward voltage	I _F = 400A		-	2.2	2.5	V
		I _F = 400A, T _{case} = 125°C		-	2.3	2.6	V
C _{ies}	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		-	30	-	nF
L _M	Module inductance - per arm	-		-	20	-	nH
R _{INT}	Internal transistor resistance - per arm	-		-	0.27	-	mΩ
SC _{Data}	Short circuit. I _{sc}	$T_j = 125^{\circ}C, V_{cc} = 1000V,$	I ₁	-	1850	-	Α
		$t_p \le 10\mu s$, $V_{CE(max)} = V_{CES} - L^*$. di/dt	I ₂	-	1600	-	Α
		IEC 60747-9					

Note:

L* is the circuit inductance + L_M



ELECTRICAL CHARACTERISTICS

 $T_{case} = 25$ °C unless stated otherwise

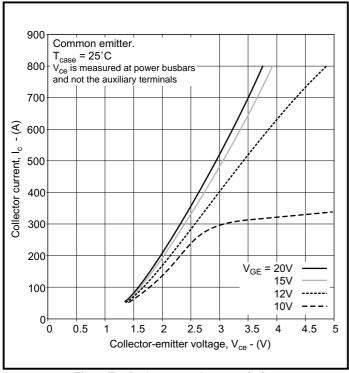
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 400A	-	1150	-	ns
t _f	Fall time	V _{GE} = ±15V	-	100	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 900V	-	120	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 4.7\Omega$	-	250	-	ns
t _r	Rise time	L ~ 100nH	-	250	-	ns
E _{on}	Turn-on energy loss		-	150	-	mJ
Q_g	Gate charge		-	4.5	-	μС
Q _{rr}	Diode reverse recovery charge	I _F = 400A, V _R = 900V,	-	100	-	μС
I _{rr}	Diode reverse current	dl _F /dt = 3000A/μs	-	230	-	А
E _{REC}	Diode reverse recovery energy		-	70	-	mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 400A	-	1400	-	ns
t,	Fall time	$V_{GE} = \pm 15V$	-	130	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 900V	-	180	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 4.7\Omega$	-	400	-	ns
t _r	Rise time	L ~ 100nH	-	250	-	ns
E _{on}	Turn-on energy loss		-	170	-	mJ
Q _{rr}	Diode reverse recovery charge	I _F = 400A, V _R = 900V,	-	170	-	μС
I _m	Diode reverse current	dI _F /dt = 2500A/μs	-	270	-	Α
E _{REC}	Diode reverse recovery energy		-	100	-	mJ



TYPICAL CHARACTERISTICS



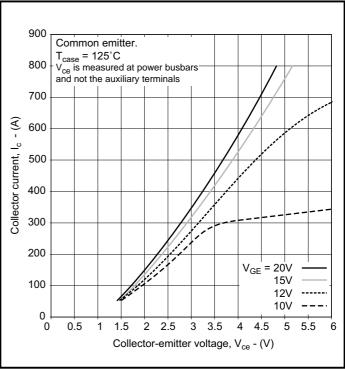


Fig. 3 Typical output characteristics



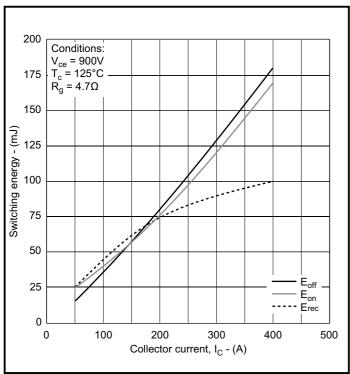


Fig. 5 Typical switching energy vs collector current

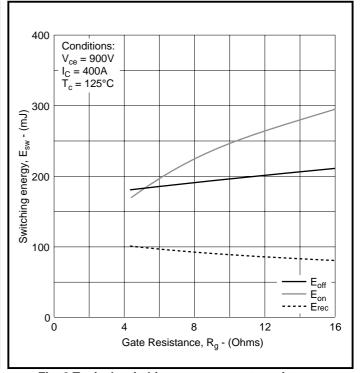
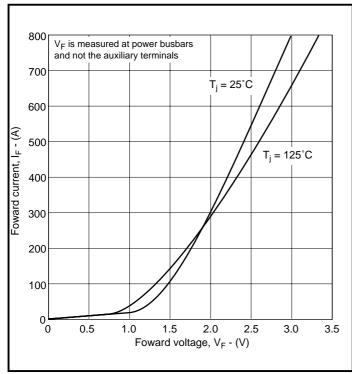


Fig. 6 Typical switching energy vs gate resistance

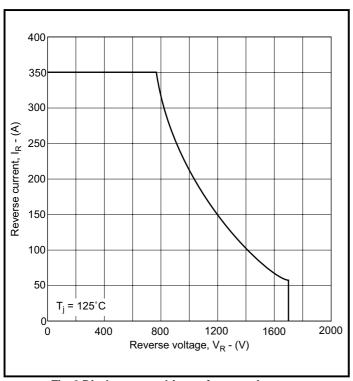




900 800 Chip 700 200 Conditions: 100 T_{case} = 125°C $V_{ge} = 15V$ $R_{g(off)} = 4.7ohms$ 200 400 600 800 1000 1200 1400 1600 1800 0 Collector emitter voltage, V_{ce} - (V)

Fig. 7 Diode typical forward characteristics

Fig. 8 Reverse bias safe operating area





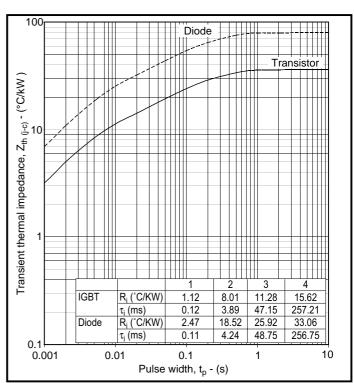
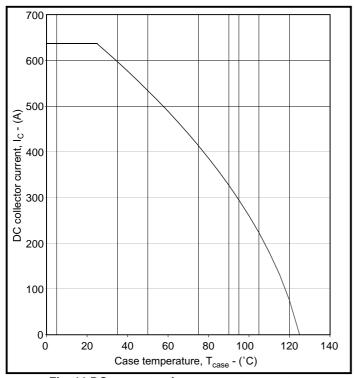


Fig. 10 Transient thermal impedance





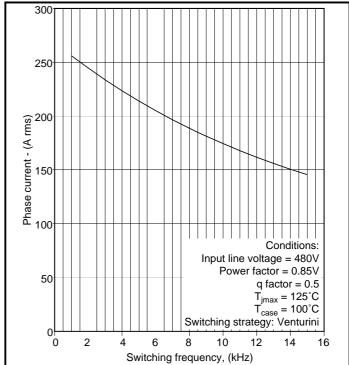


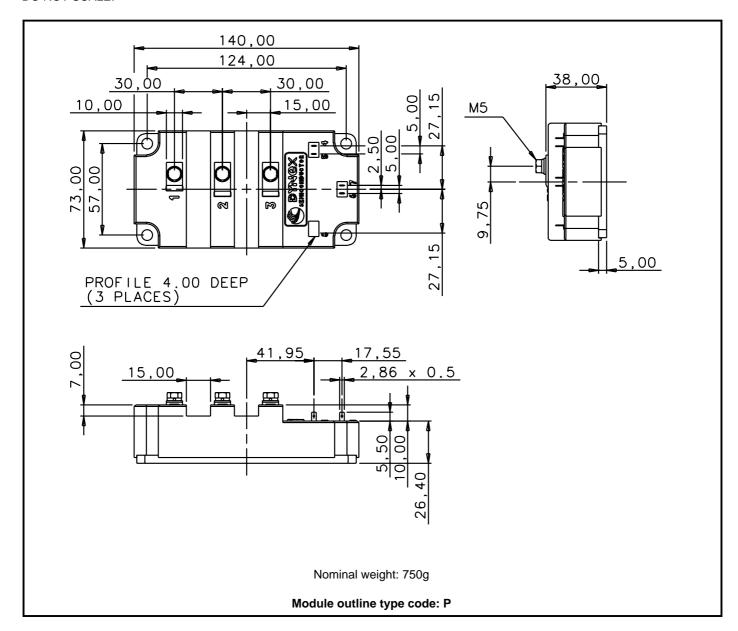
Fig. 11 DC current rating vs case temperature

Fig. 12 Matrix converter phase current vs switching frequency



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For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





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Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

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The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



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