UNRF2AM

Silicon NPN epitaxial planar transistor

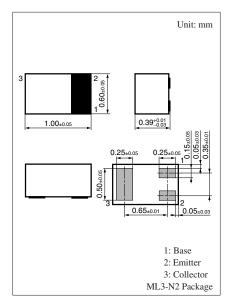
For digital circuits

Features

• Suitable for high-density mounting and downsizing of the equipment for Ultraminiature leadless package 0.6 mm × 1.0 mm (height 0.39 mm)

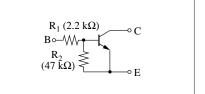
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Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V _{CBO}	50	V
Collector-emitter voltage (Base open)	V _{CEO}	50	V
Collector current	I _C	80	mA
Total power dissipation	P _T	100	mW
Junction temperature	Tj	125	°C
Storage temperature	T _{stg}	-55 to +125	°C

Absolute Maximum Ratings $T_a = 25^{\circ}C$



Marking Symbol: 2Z

Internal Connection

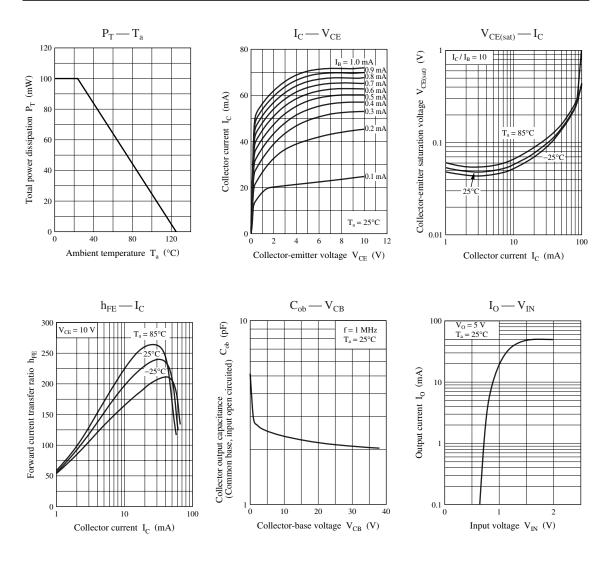


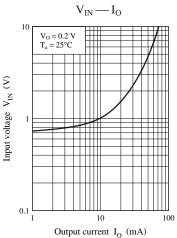
Symbol Conditions Parameter Min Тур Max Unit Collector-base voltage (Emitter open) $I_{C} = 10 \ \mu A, I_{E} = 0$ 50 V V_{CBO} V Collector-emitter voltage (Base open) V_{CEO} $I_{C} = 2 \text{ mA}, I_{B} = 0$ 50 $V_{CB} = 50 \text{ V}, I_E = 0$ Collector-base cutoff current (Emitter open) I_{CBO} 0.1 μΑ $V_{CE} = 50 \text{ V}, I_{B} = 0$ Collector-emitter cutoff current (Base open) 0.5 I_{CEO} μΑ Emitter-base cutoff current (Collector open) $V_{EB} = 6 V, I_C = 0$ $\mathbf{I}_{\mathrm{EBO}}$ 0.2 mA Forward current transfer ratio \mathbf{h}_{FE} $V_{CE} = 10 \text{ V}, I_C = 5 \text{ mA}$ 80 ____ $I_{C} = 10 \text{ mA}, I_{B} = 0.3 \text{ mA}$ 0.25 v Collector-emitter saturation voltage V_{CE(sat)} Output voltage high-level V_{CC} = 5 V, V_B = 0.5 V, R_L = 1 k Ω 4.9 V VOH Output voltage low-level V_{OL} $V_{CC} = 5 \text{ V}, V_B = 2.5 \text{ V}, R_L = 1 \text{ k}\Omega$ V 0.2 Input resistance R_1 -30% 2.2 +30%kΩ 0.047 Resistance ratio R_1 / R_2 150 Transition frequency $V_{CB} = 10 \text{ V}, I_E = -2 \text{ mA}, f = 200 \text{ MHz}$ MHz \mathbf{f}_{T}

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

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