## **Single Unbuffered Inverter**

The NL17SZU04 is a single unbuffered inverter in a tiny footprint package. The SC–70/SC–88A occupies a very small board area. The device performs much as LCX multi–gate products in speed and drive.

- Tiny SC-70/SC-88A Package
- Source/Sink ±16 mA at 4.5 V V<sub>CC</sub>
- Over–Voltage Tolerant Inputs and Outputs
- Pin For Pin with NC7SZU04
- Chip Complexity: FETs = L6

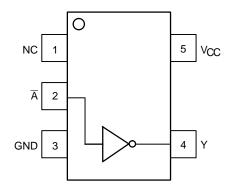
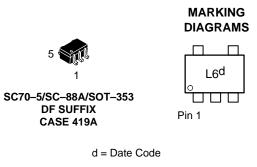


Figure 1. Pinout (Top View)



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#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.



Figure 2. Logic Symbol

#### **PIN ASSIGNMENT**

Pin	Function
1	NC
2	Ā
3	GND
4	Y
5	Vcc

#### **FUNCTION TABLE**

Input	Output Y = Ā
A	Y
L	Н
н	L

#### MAXIMUM RATINGS

Symbol	Parame	eter	Value	Unit
VCC	DC Supply Voltage	-0.5 to $+7.0$	V	
V <sub>IN</sub>	DC Input Voltage		-0.5 to +7.0	V
VOUT	DC Output Voltage		-0.5 to +7.0	V
lik	DC Input Diode Current		-50	mA
lok	DC Output Diode Current		-50	mA
IOUT	DC Output Sink Current		±50	mA
ICC	DC Supply Current per Supply Pin		±100	mA
TSTG	Storage Temperature Range	-65 to +150	°C	
ΤL	Lead Temperature, 1 mm from Case for 10	Seconds	260	°C
TJ	Junction Temperature Under Bias		+ 150	°C
$\theta_{JA}$	Thermal Resistance	SC-70/SC-88A (Note 1) TSOP-5	350 230	°C/W
PD	Power Dissipation in Still Air at 85°C	SC-70/SC-88A TSOP-5	150 200	mW
MSL	Moisture Sensitivity		Level 1	
FR	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V–0 @ 0.125 in	
VESD	ESD Withstand Voltage	Human Body Model (Note 2) Machine Model (Note 3) Charged Device Model (Note 4)	>2000 >200 N/A	V

Maximum Ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute maximum–rated conditions is not implied. Functional operation should be restricted to the Recommended Operating Conditions.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2-ounce copper trace with no air flow.

2. Tested to EIA/JESD22-A114-A.

3. Tested to EIA/JESD22-A115-A.

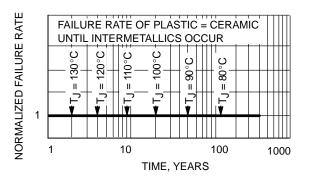
4. Tested to JESD22-C101-A.

#### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter			Max	Unit
VCC	DC Supply Voltage		2.0	5.5	V
V <sub>IN</sub>	DC Input Voltage		0	5.5	V
VOUT	DC Output Voltage		0	5.5	V
T <sub>A</sub>	Operating Temperature Range		-40	+85	°C
t <sub>r</sub> , t <sub>f</sub>	Input Rise and Fall Time V V	CC = 3.0 V ±0.3 V CC = 5.0 V ±0.5 V	0 0	100 20	ns/V

# DEVICE JUNCTION TEMPERATURE VERSUS TIME TO 0.1% BOND FAILURES

Junction Temperature °C	Time, Hours	Time, Years
80	1,032,200	117.8
90	419,300	47.9
100	178,700	20.4
110	79,600	9.4
120	37,000	4.2
130	17,800	2.0
140	8,900	1.0





			Vcc	T <sub>A</sub> = 25°C			$-40^{\circ}C \leq -$	T <sub>A</sub> ≤ 85°C	
Symbol	Parameter	Condition	(V)	Min	Тур	Max	Min	Max	Unit
VIH	High-Level Input Voltage		2.3 to 5.5	0.7 V <sub>CC</sub>			0.7 V <sub>CC</sub>		V
VIL	Low–Level Input Voltage		2.3 to 5.5			0.3 V <sub>CC</sub>		0.3 V <sub>CC</sub>	V
VOH	High–Level Output Voltage	I <sub>OH</sub> = 100 μA	2.3 to 5.5	V <sub>CC</sub> - 0.1	VCC		V <sub>CC</sub> - 0.1		V
	$V_{IN} = V_{IL} \text{ or } V_{IH}$	I <sub>OH</sub> =8 mA	2.3	1.9	2.1		1.9		
		I <sub>OH</sub> = -12 mA	2.7	2.2	2.4		2.2		
		I <sub>OH</sub> = -16 mA	3.0	2.4	2.7		2.4		
		I <sub>OH</sub> = -24 mA	3.0	2.3	2.5		2.3		
		I <sub>OH</sub> = -32 mA	4.5	3.8	4.0		3.8		
VOL	Low–Level Output Voltage	I <sub>OL</sub> = 100 μA	2.3 to 5.5			0.1		0.1	V
	$V_{IN} = V_{IH} \text{ or } V_{OH}$	I <sub>OL</sub> = 8 mA	2.3		0.20	0.3		0.3	
		I <sub>OL</sub> = 12 mA	2.7		0.22	0.4		0.4	
		I <sub>OL</sub> = 16 mA	3.0		0.28	0.4		0.4	
		I <sub>OL</sub> = 24 mA	3.0		0.38	0.55		0.55	
		I <sub>OL</sub> = 32 mA	4.5		0.42	0.55		0.55	
I <sub>IN</sub>	Input Leakage Current	$V_{IN} = V_{CC}$ or GND	0 to 5.5			±0.1		±1.0	μA
IOFF	Power Off–Output Leakage Current	V <sub>OUT</sub> = 5.5 V	0			1		10	μΑ
ICC	Quiescent Supply Current	$V_{IN} = V_{CC}$ or GND	5.5			1		10	μΑ

#### DC ELECTRICAL CHARACTERISTICS

### AC ELECTRICAL CHARACTERISTICS $t_R = t_F = 3.0 \text{ ns}$

			۷ <sub>CC</sub>	T <sub>A</sub> =	25°C	-40°C ≤ 1	<b>A</b> ≤ 85°C	
Symbol	Parameter	Condition	(V)	Min	Max	Min	Max	Unit
<sup>t</sup> PLH	Propagation Delay	$R_L$ = 1 M $\Omega$ , $C_L$ = 15 pF	$2.5\pm0.2$	0.8	6.2	0.8	6.5	ns
<sup>t</sup> PHL	(Figure 4 and 5)	$R_L = 1 M\Omega, C_L = 15 pF$	$3.3\pm0.3$	0.5	4.5	0.5	4.8	
		$R_{L} = 500 \ \Omega, \ C_{L} = 50 \ pF$		1.0	6.0	1.0	6.5	
		$R_L = 1 M\Omega$ , $C_L = 15 pF$	$5.0\pm0.5$	0.5	3.9	0.5	1.0	
		$R_{L} = 500 \ \Omega, \ C_{L} = 50 \ pF$		0.8	5.0	0.8	5.5	

#### **CAPACITIVE CHARACTERISTICS**

Symbol	Parameter	Condition	Typical	Unit
C <sub>IN</sub>	Input Capacitance	$V_{CC}$ = 5.5 V, $V_{I}$ = 0 V or $V_{CC}$	4.5	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 5)	10 MHz, $V_{CC} = 3.3 \text{ V}$ , $V_{I} = 0 \text{ V or } V_{CC}$	6.5	pF
		10 MHz, $V_{CC}$ = 5.5 V, $V_{I}$ = 0 V or $V_{CC}$	10	

5. CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation:  $I_{CC(OPR)} = C_{PD} \bullet V_{CC} \bullet f_{in} + I_{CC}$ . CPD is used to determine the no–load dynamic power consumption;  $P_D = C_{PD} \bullet V_{CC}^2 \bullet f_{in} + I_{CC} \bullet V_{CC}$ .

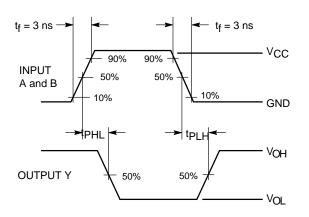
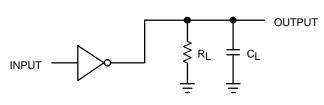


Figure 4. Switching Waveform



A 1–MHz square input wave is recommended for propagation delay tests.



### **DEVICE ORDERING INFORMATION**

	Device Nomenclature								
Device Order Number	Logic Circuit Indicator	No. of Gates per Package	Temp Range Identifier	Technology	Device Function	Package Suffix	Tape and Reel Suffix	Package Type	Tape and Reel Size
NL17SZU04DFT2	NL	1	7	SZ	U04	DF	T2	SC-88A/ SOT-353/ SC70-5	178 mm, 3000 Unit

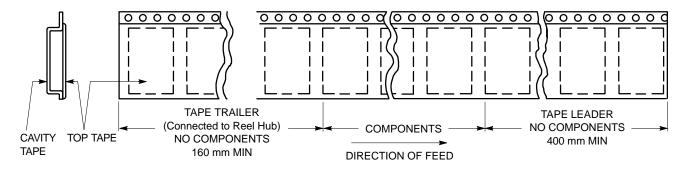


Figure 6. Tape Ends for Finished Goods

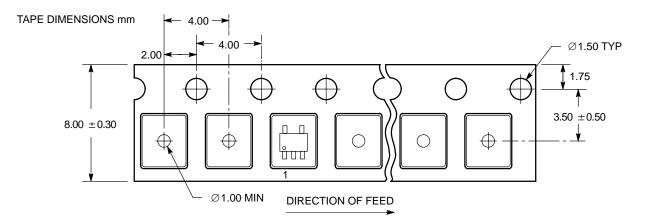
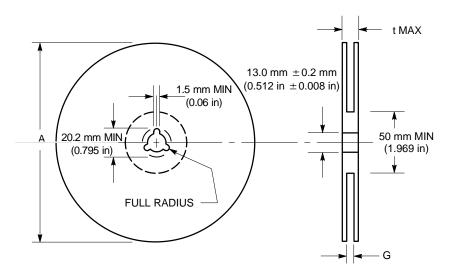


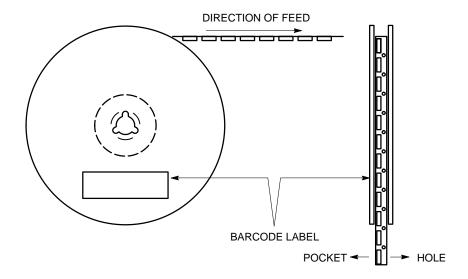
Figure 7. SC-70/SC-88A/SOT-353 DFT2 Reel Configuration/Orientation





#### **REEL DIMENSIONS**

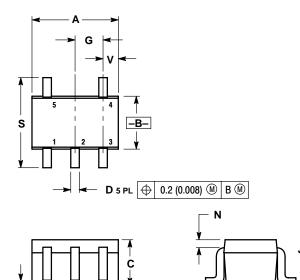
Tape Size	T and R Suffix	A Max	G	t Max
8 mm	T1, T2	178 mm (7 in)	8.4 mm, + 1.5 mm, –0.0 (0.33 in + 0.059 in, –0.00)	14.4 mm (0.56 in)





### PACKAGE DIMENSIONS

SC70-5/SC-88A/SOT-353 DF SUFFIX 5-LEAD PACKAGE CASE 419A-01 ISSUE E

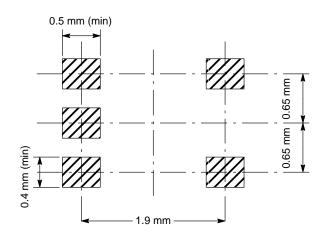


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2	CONTROLLING DIMENSION: INCH.
<u> </u>	CONTINUELING DIMENSION. INON.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.071	0.087	1.80	2.20
В	0.045	0.053	1.15	1.35
C	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026	BSC	0.65 BSC	
Н		0.004		0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
Ν	0.008 REF		0.20	REF
S	0.079	0.087	2.00	2.20
۷	0.012	0.016	0.30	0.40



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