

## A CLOCK DESIGN USING THE ST6-REALIZER

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### INTRODUCTION

Today Clock systems are used in more and more applications because of the added features they open: Current Time display, Event dating, process start time programming,...

Generally, this feature is part of a more global system, and cost effective solutions are required.

MCU solutions are more and more often used thanks to the onboard provided Timer and all the possibilities offered by the MCU embedded resources to manage other tasks. This approach allows single chip solutions which brings a great advantage in fields like small home appliances.

A simple time of day clock, provided with an alarm feature, has then been developed as example with the help of the ST6-REALIZER.

### 1 APPLICATION OVERVIEW

The clock system provides the following features:

- Current time counting
- Alarm triggering at a defined time
- Current time setup
- Alarm time setup

The time values are represented in the HH:MM format, but the described concept can easily be extended to representation in seconds. By using this HH:MM format, the time value is represented by a pair of integer variables ranging in [0..59] for the minutes and [0..23] for the hours.

The user interface consists of 4 keys: TIME SETUP, ALARM SETUP, HOURS and MINUTES.

With these 4 keys, both the current time and the alarm time can be adjusted:

- When the key TIME SETUP is activated, the *Hours (Resp. Minutes)* variable of the current time is increased at each activation of the key HOURS (Resp. MINUTES).
- When the key ALARM SETUP is activated, the *Hours (Resp. Minutes)* variable of the Alarm time is increased at each activation of the key HOURS (Resp. MINUTES).

A decrementation of any of the time variables is achieved by successive incrementations since they are reset when they reach the maximum value (23 for the hours and 59 for the minutes). For instance, passing from 22 Hours to 2 Hours needs 4 steps: 22 > 23 > 0 > 1 > 2.

## 2 FUNCTIONAL PRINCIPLE

All the featured functions of the clock system are managed on the final application under software control by the MCU. However, using the ST6-REALIZER allows to generate this application software by functional description of the system. Thus the software development can be achieved by a system oriented or hardware like description.

### 2.1 Current time counting

The system uses a 1 Hz timebase generated with the embedded Timer of the ST62 MCU. This oscillating timebase is used to trigger three chained Modulo-N counters (Unit Counter):

- one for the seconds ( $n = 60$ )
- one for the minutes ( $n = 60$ )
- one for the hours ( $n = 24$ ).

When any of these Unit Counter reaches its maximum value, a clock is issued to increment the Unit counter of the next stage.

### 2.2 Current time setup

The current time value is modified by incrementation of the counters used for the current time counting. This is achieved by duplicating the clock input of the involved counter directly in the current time counting block. Practically, two different additional clocks are needed, one for the minutes incrementation and one for the hours incrementation. Each of these additional clocks is controlled by the combination of the keys, TIME SETUP, HOURS and MINUTES.

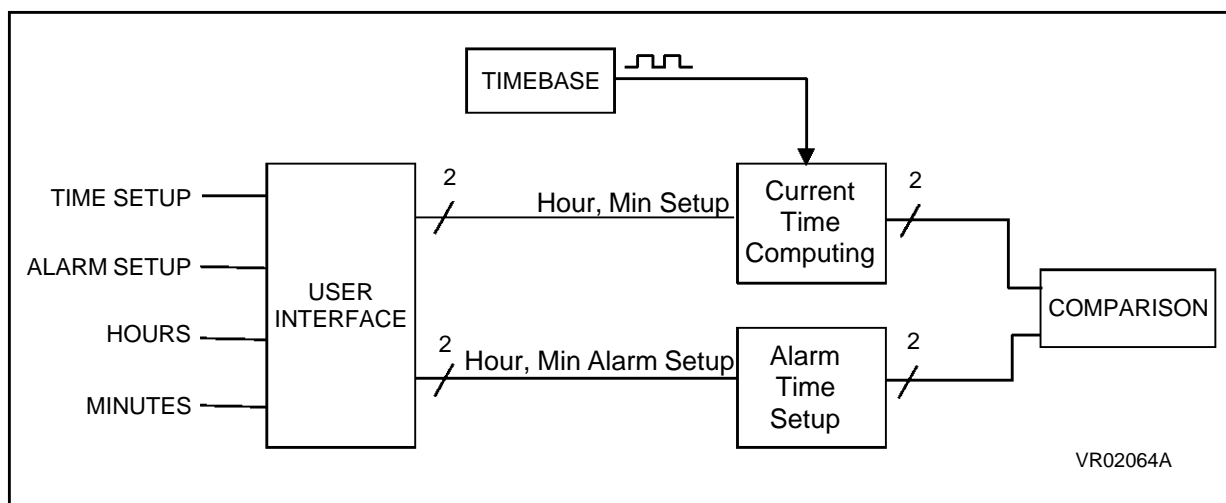
### 2.3 Alarm time setup

A structure very close to the current time setup has been used. Two counters are used, one for the minutes, and one for the hours. Unlike current time counting, the content of these counters can be modified only by pressure on the keys ALARM SETUP, HOURS and MINUTES.

### 2.4 Alarm triggering

An alarm process is launched when the current time is equal to the alarm predefined time. The occurrence is enabled by a double comparison: Hours equal, Minutes equal.

**Figure 1. Application block diagram**



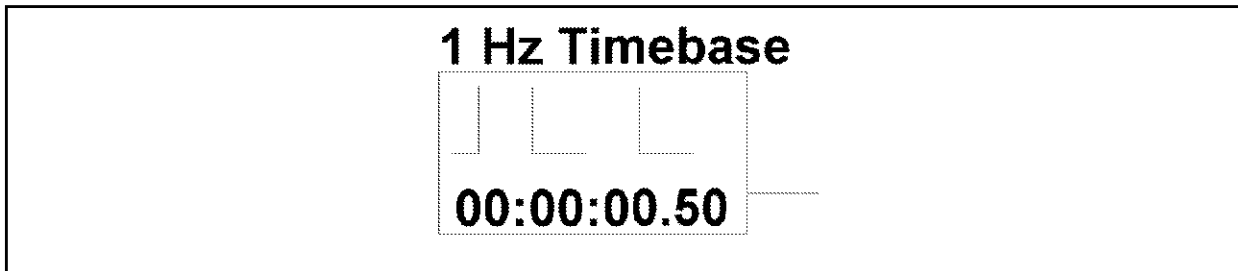
## 3 PRACTICAL REALISATION

### 3.1 Timebase

The timebase is described by using the Oscillator component of the ST6-REALIZER library. Any “real-time” value can be defined for the period in a very explicit way. In the present case, the 1Hz timebase is defined as an oscillating squarewave with half a period (Level High duration) of 00:00:00.50 in the HH:MM:SS.xx format (Figure 2).

Practically, the ST6-REALIZER uses the embedded Timer of the ST62 MCU as timebase, and generates by software any periodic variable.

Figure 2. Timebase description



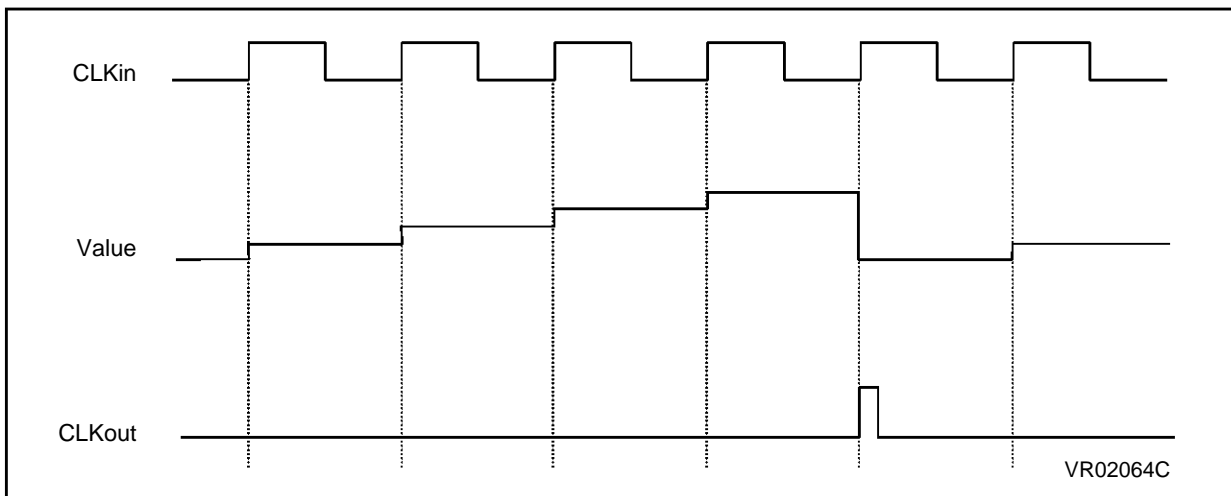
### 3.2 Current time counting

Each of the 3 chained Modulo-N counters presents:

- An input clock issued from the previous stage (Timebase for the 1st stage).
- An output clock to activate the following stage.
- A bitwise output with the current counted value (Second, Minute or Hour).
- An optional clock for the incremental set-up process.

Any content change can be performed only during a input clock transition from 0 to 1. Thus the reset phase when the counter reaches its maximum value has to be anticipated. For instance the Hours Modulo-24 counter is reset if the two following conditions occur: Content equal to 23 and input clock transition. In parallel, the resulting Reset signal is issued as output clock to increment the next stage as shown on the Figure 3 in the case of a Modulo-5 counter.

Figure 3. Clock synchronism principle

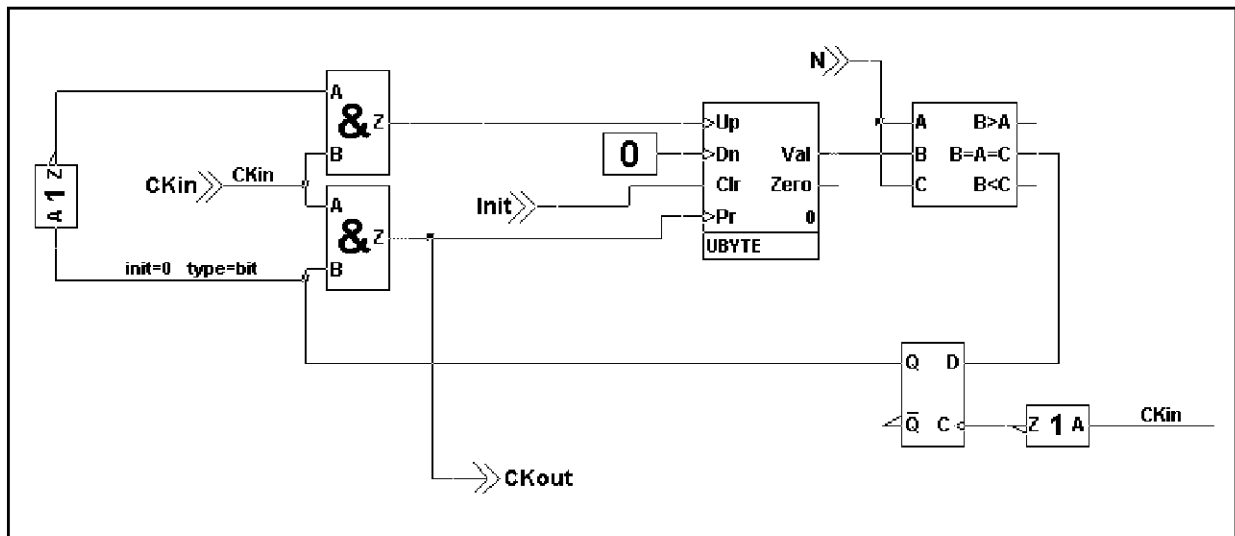


Each Modulo N block is based on a Counter component provided within the ST6-REALIZER standard library. This symbol presents the advantage of generating a numeric variable as output. This means that any numeric or arithmetic operation can be directly performed on that variable without worrying on its binary representation. In the present case, the detection of the maximum value is achieved with a comparator symbol.

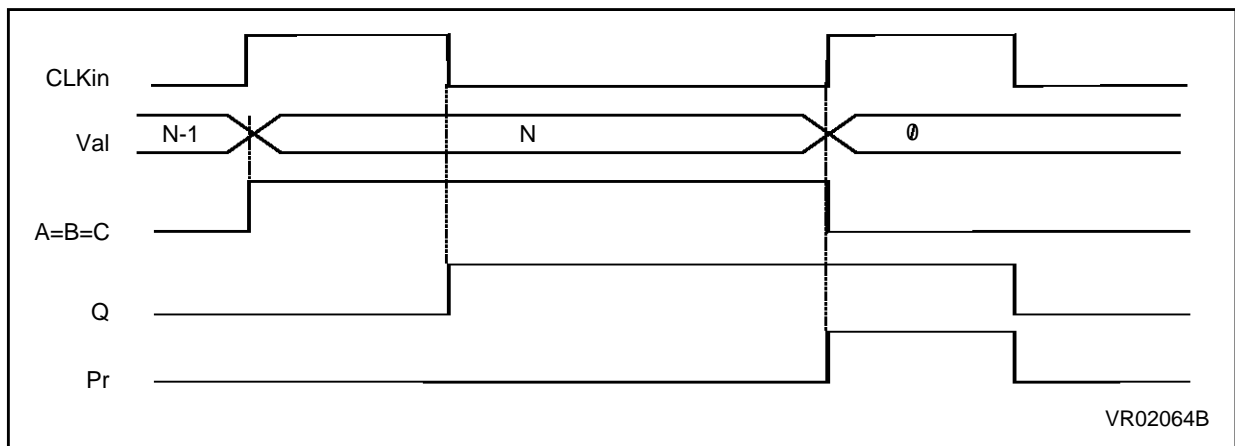
The comparator output is injected into the feedback loop at the falling edge of the input clock. This avoids any parasitic Preset during the High level of the input clock when the comparator output switches to High level. In parallel, the counter incrementation is prevented by forcing to Low level the *Up* input of the counter.

Finally, each Unit Counter is represented in the ST6-REALIZER environment as shown in the Figure 4.

**Figure 4. Unit Counter description**



**Figure 5. Waveforms of the Current Time Counters**



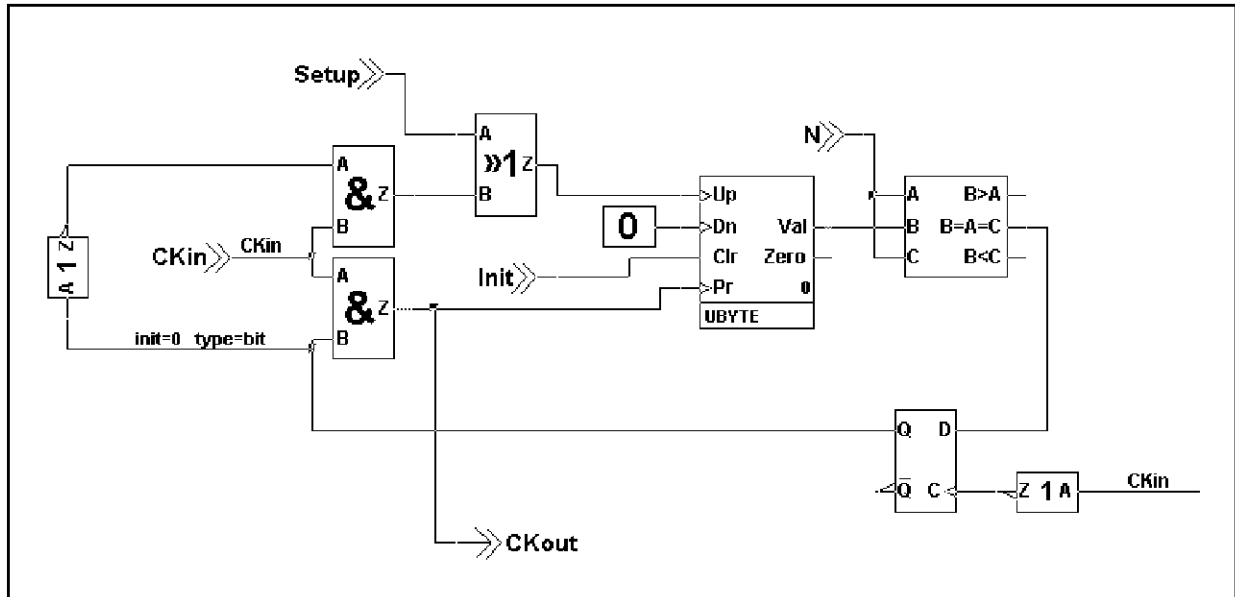
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## A CLOCK DESIGN USING THE ST6-REALIZER

### 3.3 Current Time setup

The incrementation clock (*Up* input of the symbol) is duplicated through an OR gate with an external clock, *Setup* signal, for Hours or Minutes setup. Any activation on this clock increments of 1 the content of the counter (Figure 6).

Figure 6. Current Time setup with a duplicated clock

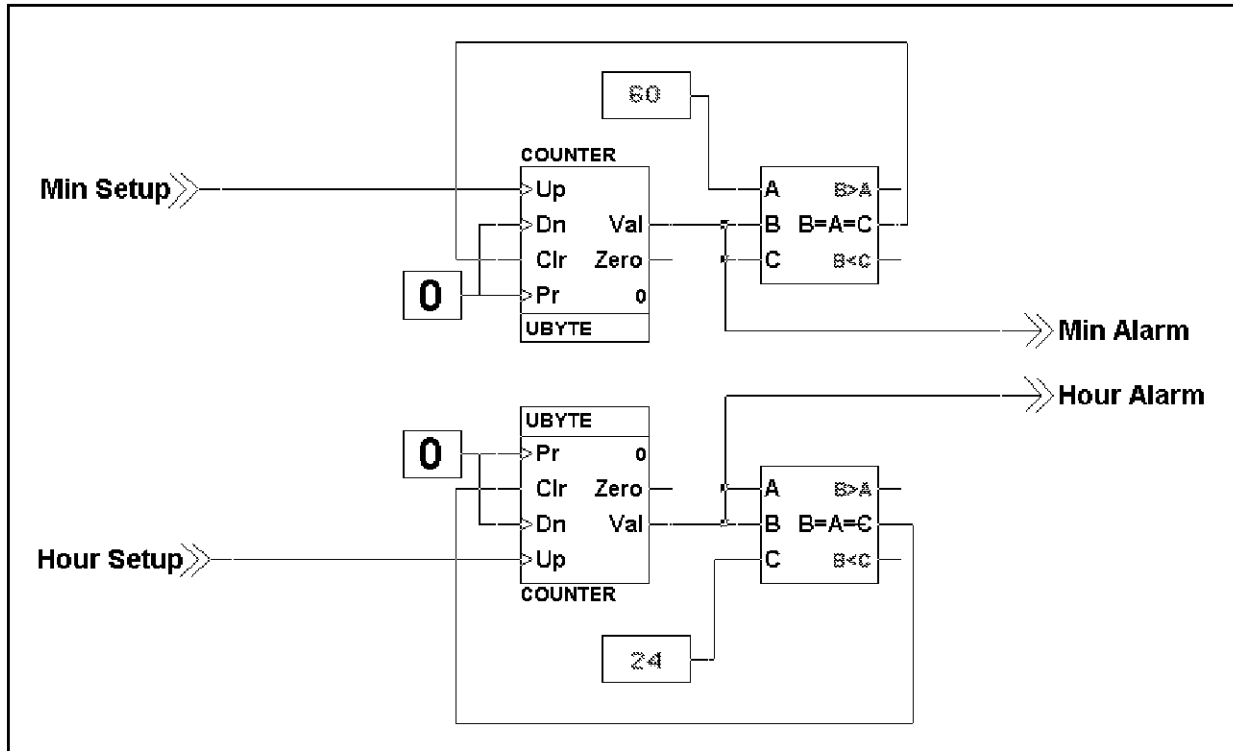


3.4 Alarm Time setup

As for the Current Time setup, the Alarm Time setup is achieved by incrementation of two Modulo-N counters, one for the Minutes and one for the Hours. Each of them has his own input clock controlled by the combination of the keys ALARM SETUP, HOURS and MINUTES.

A feedback loop is still needed to reset the counter when the maximum value is reached but its implementation can be much more simple than in the Current Time counting blocks. In fact a precise synchronism is not mandatory, and a basic solution can be used (Figure 7).

Figure 7. Alarm Time setup



## A CLOCK DESIGN USING THE ST6-REALIZER

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### 4 CONCLUSION

With the help of the ST6-REALIZER, the basic modules of a time of day clock have been developed. The graphical description facility of the tool allows a modular description of the application, and therefore a progressive development.

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