

ULN-3753B AND ULN-3753W DUAL POWER OPERATIONAL AMPLIFIERS

FEATURES

- Operating Supply Range $\pm 3\text{V}$ to $\pm 20\text{V}$
- Output Current to $\pm 3.5\text{A}$ Peak
- Output-Current Limiting
- Output-Current Sensing
- High Output-Voltage Swing
- Low Crossover Distortion
- Low Input Offset Voltage
- Externally Compensated
- High Open-Loop Gain
- Output Protection Diodes
- Thermal Shutdown Protection
- Excellent Supply and Common-Mode Rejection
- Single or Dual In-Line Power Packages

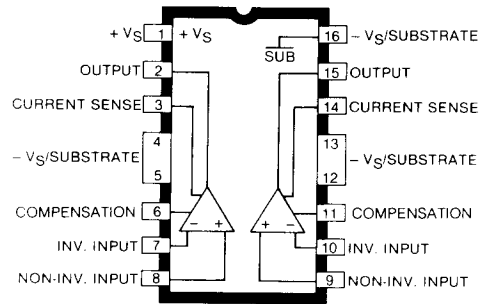
APPLICATIONS

- Dual Half-Bridge and Full-Bridge Motor Drivers
- Linear Servo Motors
- Voice Coil Motors
- AC and DC Motors
- Microstepping Applications
- Power Transconducting Amplifier
- Audio Power Amplifier, Stereo or BTL
- Power Oscillator/Amplifier
- Dual Bipolar Voltage Regulator

High-current linear servo loads, such as voice coil motors used in disc-drive applications, are ideal applications for the ULN-3753B and ULN-3753W dual high-power operational amplifiers. Their building block design concept also makes them ideal for a wide variety of other motor drive applications, audio power amplifiers, power oscillators, and linear voltage regulators. External compensation permits user adjustment of bandwidth and phase margin at any gain level.

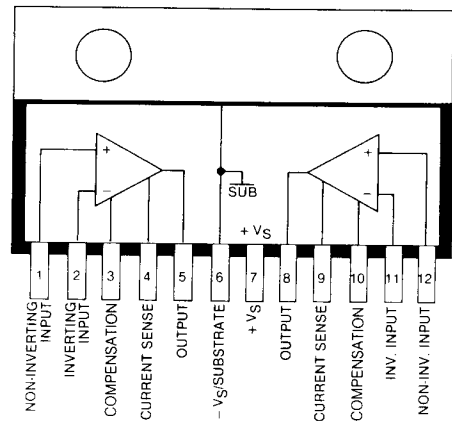
The ULN-3753B is furnished in a 16-pin dual in-line package with copper heat-sink contact tabs. For higher power requirements, the ULN-3753W is supplied in a 12-pin single in-line power tab package.

The inputs are designed to allow a wide common mode range from the negative supply, (or ground in



Dwg. No. A-13, 636

ULN-3753B



Dwg. No. A-13, 632

ULN-3753W

single supply applications) to within approximately 2V of the positive supply. Common-mode and power supply rejection are in excess of 60 dB. The amplifiers' wide output swing is complemented by current sensing, which is referenced to the negative supply and allows for feedback as required to produce a transconductance characteristic.

ULN-3753B AND ULN-3753W DUAL POWER OPERATIONAL AMPLIFIERS

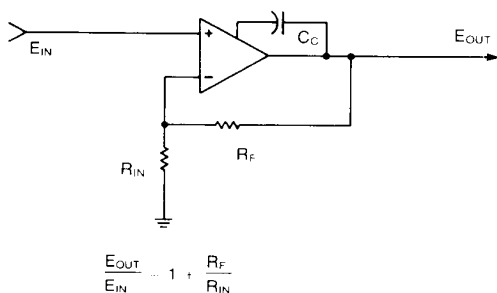
ELECTRICAL CHARACTERISTICS at $T_A = +25^\circ\text{C}$, $T_{\text{TAB}} \leq +70^\circ\text{C}$, $V_S = \pm 6\text{V}$, $C_C = 0$, each amplifier tested separately (unless otherwise specified)

Characteristic	Test Conditions	Limits			
		Min.	Typ.	Max.	Units
Functional Supply Voltage Range	$+V_S$ to $-V_S$	6.0	—	40	V
Quiescent Supply Current		—	90	150	mA
Input Bias Current	$V_{\text{OUT}} = 0$	—	-80	-1000	nA
Input Offset Voltage	$V_{\text{OUT}} = 0$, $I_{\text{OUT}} = 0$	—	± 1.0	+10	mV
Input Offset Volt. TC^\dagger	Over Op. Temp. Range	—	-15	—	$\mu\text{V}/^\circ\text{C}$
Input Offset Current	$V_{\text{OUT}} = 0$, $I_{\text{OUT}} = 0$	—	10	100	nA
Input Noise Voltage [†]	$\text{BW} = 40\text{Hz to } 15\text{kHz}$	—	4.0	—	μV
Input Noise Current [†]	$\text{BW} = 40\text{Hz to } 15\text{kHz}$	—	60	—	pA
Crossover Distortion [†]	$P_{\text{OUT}} = 50\text{mW}$, $R_L = 4\Omega$	—	0.2	—	%
Common Mode Rejection	$V_{\text{CM}} = 3\text{V}$	60	85	—	dB
Input Common Mode Range*	$V_S = +6\text{V}$	-6.3	—	+4.0	V
	$V_S = +15\text{V}$	-15.3	—	+13	V
Open Loop Voltage Gain	$f = 0$	80	100	—	dB
Slew Rate	$V_{\text{IN}} = V_{\text{OUT}} = 6\text{V}_{\text{pp}}$	5.0	10	—	$\text{V}/\mu\text{s}$
Gain-Bandwidth Product [†]	$A_V = 40\text{dB}$	—	3.0	—	MHz
Channel Separation [†]	$I_{\text{OUT}} = 100\text{mA}$, $f = 1\text{kHz}$	—	60	—	dB
Output Voltage Swing	$I_{\text{OUT}} = 1\text{A}$	9.0	9.5	—	V_{pp}
Supply Voltage Rejection	$+V_S$, $\Delta V = 1\text{V}$	60	85	—	dB
	$-V_S$, $\Delta V = 1\text{V}$	60	80	—	dB
Thermal Resistance, Θ_{JT} *	ULN-3753B	—	—	15	$^\circ\text{C}/\text{W}$
	ULN-3753W	—	—	3.0	$^\circ\text{C}/\text{W}$
Thermal Shutdown Temp. [†]		—	165	—	$^\circ\text{C}$

*This parameter is tested to a lot sample plan only.

[†]Typical values given for circuit design information only.

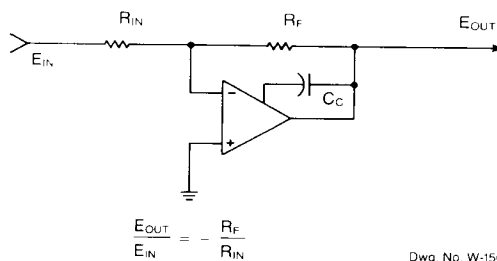
NON-INVERTING AMPLIFIER



IF $R_F = 0$ or $R_{\text{IN}} = \infty$, $E_{\text{OUT}} = E_{\text{IN}}$

Dwg No. W-155

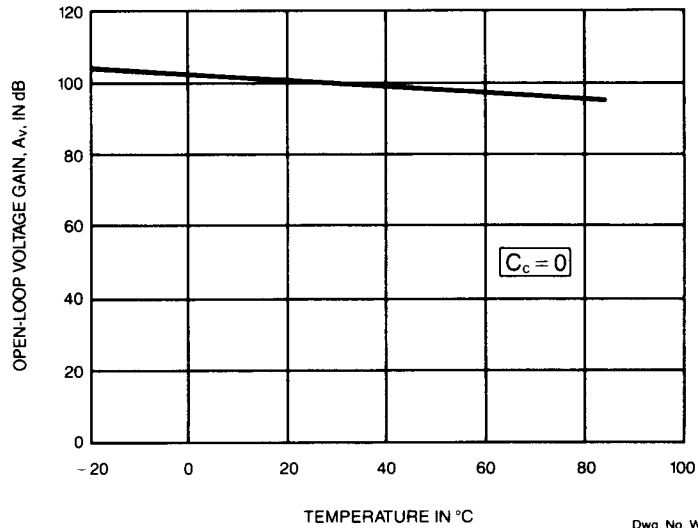
INVERTING AMPLIFIER



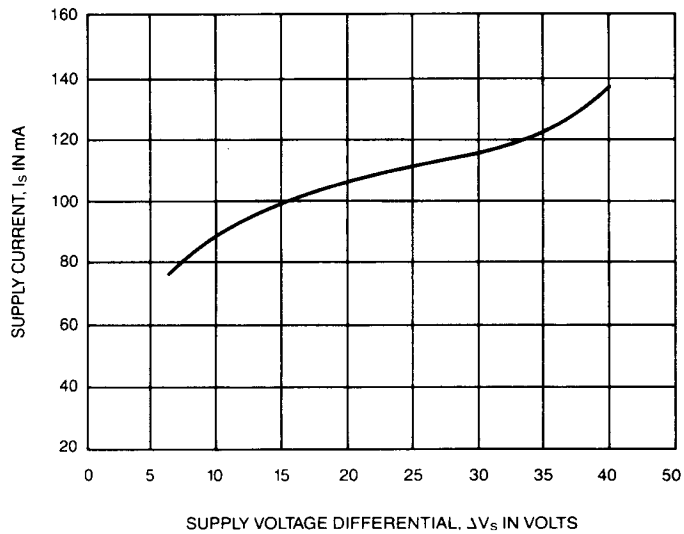
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TYPICAL CHARACTERISTICS

OPEN-LOOP VOLTAGE GAIN
AS A FUNCTION OF TEMPERATURE

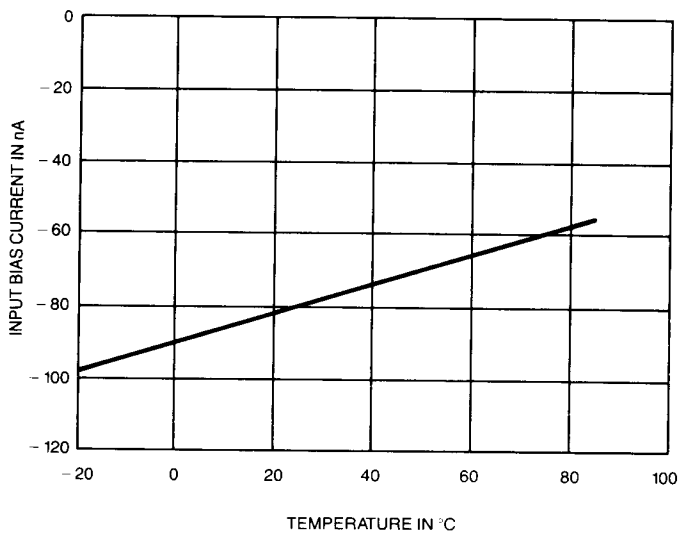


SUPPLY CURRENT AS A FUNCTION
OF SUPPLY VOLTAGE DIFFERENTIAL



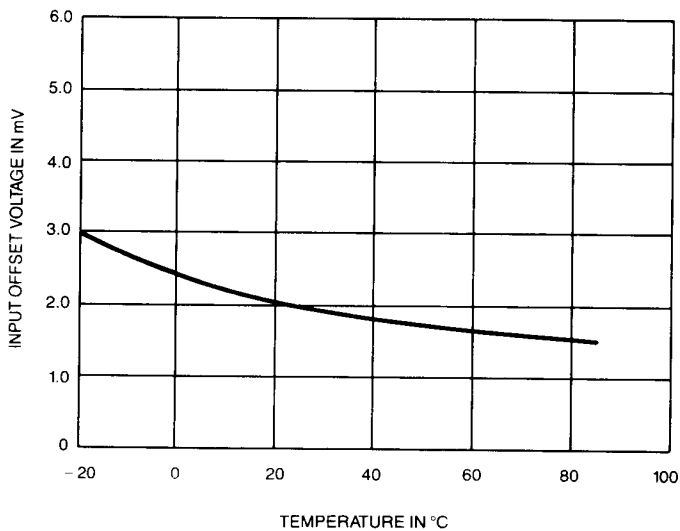
TYPICAL CHARACTERISTICS

INPUT BIAS CURRENT
AS A FUNCTION OF TEMPERATURE



Dwg. No. W-159

INPUT OFFSET VOLTAGE
AS A FUNCTION OF TEMPERATURE



Dwg. No. W-160

The circuit diagram illustrates a precision current source. It features two operational amplifiers, labeled A and B, configured as voltage followers (buffers). The non-inverting input of op-amp A is connected to the input voltage V_{IN} , and its inverting input is connected to the output of op-amp A. The non-inverting input of op-amp B is connected to the reference voltage V_{REF} , and its inverting input is connected to the output of op-amp B. The outputs of both op-amps are connected to a Wheatstone bridge. The bridge consists of four resistors: R_S and R_5 are in the left branch, R_A and R_B are in the right branch, and R_L and R are in the top branch. The current through the load resistor R_L is denoted as I_L . The bridge is powered by a supply voltage V_S and a reference voltage V_{REF} . The output of the bridge is connected to a load resistor R_B and a reference voltage V_{REF} . The current through the load resistor R_B is denoted as I_B . The current through the sense resistor R_S is denoted as I_4 , and the current through the sense resistor R_5 is denoted as I_9 . The bridge is connected to a supply voltage V_S and a reference voltage V_{REF} . The output of the bridge is connected to a load resistor R_B and a reference voltage V_{REF} . The current through the load resistor R_B is denoted as I_B . The current through the sense resistor R_S is denoted as I_4 , and the current through the sense resistor R_5 is denoted as I_9 .

Dwg. No. W-162

APPLICATIONS

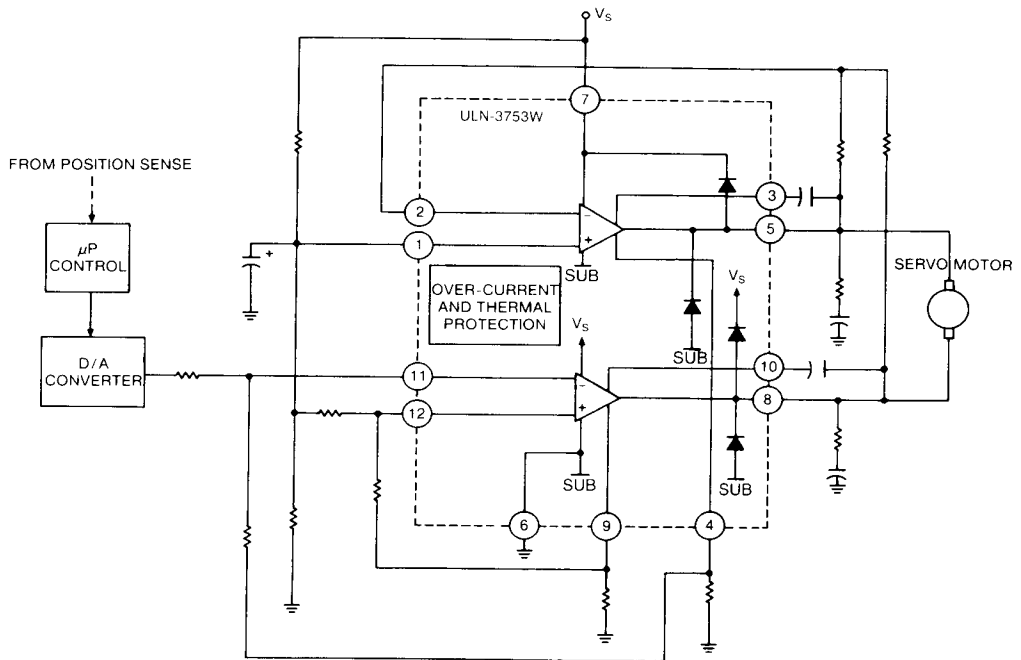


FIGURE 2

Dwg No. W-163

DIGITALLY CONTROLLED POSITION SERVO

In a position-control application, a microprocessor is often used to control a servomotor's shaft angle or to control position as in a disk drive application. The circuit requires small-signal input op amps, drivers, and power output stages. The circuit derives its input from the D/A converter whose output is determined by a code from the controlling microprocessor and related digital control circuitry. The sensed position signal normally undergoes processing and comparison with the desired position, through the microprocessor system that produces an error signal to control the servo amplifier's output.

The circuit includes thermal and short-circuit

protection, matching and thermal tracking inherent to monolithic construction. The configuration shown in Figure 2 uses a ULN-3753W dual power operational amplifier whose two independent outputs are connected in a push-pull, H-bridge arrangement. The IC's outputs also include clamping diodes with current-handling capacity equal to that of the output drivers.

The current-sense pins (4 and 9) provide access to the emitters of the H-bridge current sinks, thereby providing convenient output current sensing, while allowing separate low-current signal ground returns.

APPLICATIONS

TWO-PHASE 60 Hz AC MOTOR DRIVER

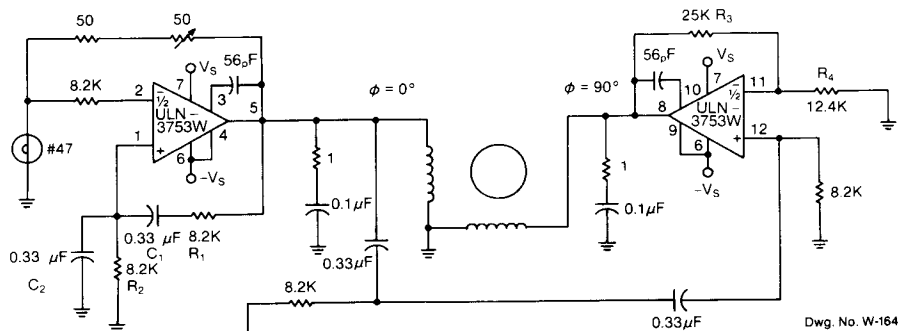


FIGURE 3

THREE-PHASE 400 Hz AC MOTOR DRIVER

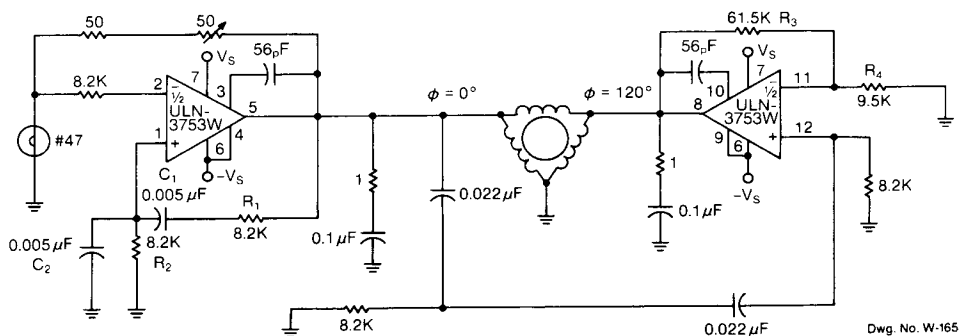


FIGURE 4

N-PHASE MOTOR DRIVE

Because of its high amplification factor and built-in power-output stage, an integrated power operational amplifier makes a convenient driver for ac motors. One op amp can be configured as an oscillator to generate the required ac signal. The power-output stage, of course, supplies the high-current drive to the motor.

As shown in the motor-drive circuits in Figure 3 and 4, the controlling op amp is configured as a Weinbridge oscillator. The R_1C_1 , R_2C_2 feedback networks determine the oscillation frequency, according to the following expression:

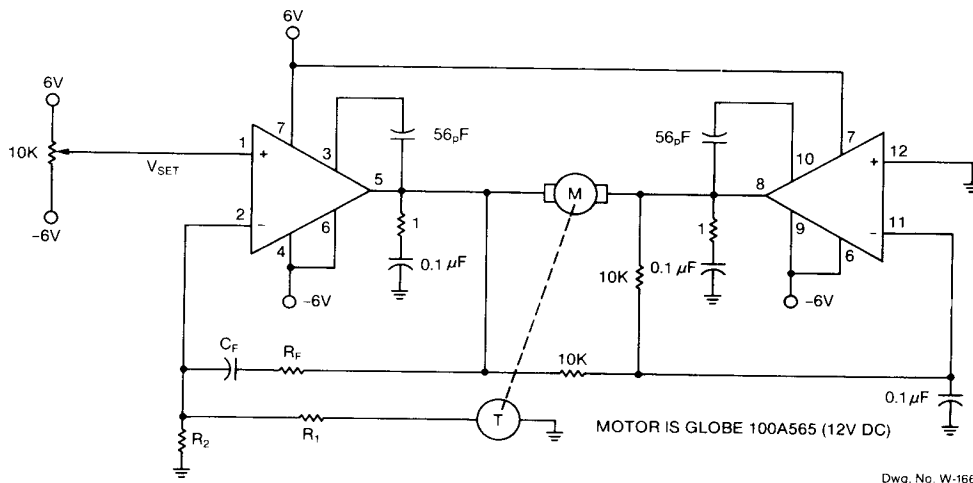
$$f_o = 1/(2\pi R_1R_2C_1C_2)$$

By varying either R_1 or R_2 , the oscillator frequency can be adjusted over a narrow range.

The R_3/R_4 ratio sets the second amplifier's gain to compensate for signal attenuation occurring in the phase shifters.

The circuits can be driven from an external source, such as a pulse or square wave, setting the gain of the left-hand amplifier to a level less than that required for oscillation. The RC feedback networks then function as an active filter causing the outputs to be sinusoidal.

APPLICATIONS



Dwg. No. W-166

FIGURE 5

DC MOTOR SPEED CONTROL

In addition to the synchronous ac motor drives described above, the ULN-3753B/W can be used to provide accurate speed control of dc motors. Figure 5 shows a closed-loop system for controlling the speed of a 12 V dc motor. The circuit provides bidirectional speed control. The amplifiers' push-pull configuration ensures a full rail-to-rail voltage swing (minus the output stages' saturation drops) across the motor in either direction.

The circuit uses a mechanically-coupled tachometer to provide speed-stabilizing feedback to the first amplifier section. The motor's speed and direction of rotation is set by adjusting the 10 k Ω poten-

tiometer at the amplifier's noninverting input. The motor speed, in rpm, is given by the following expression:

$$S = V_{SET}(R_1 + R_2)/.0027 R_2$$

The $R_F C_F$ feedback network prevents oscillation by compensating for the inherent dynamic mechanical lag of the motor. The $R_F C_F$ time constant is selected to match the particular motor's response or dynamic time constant. This should yield a good starting point for stabilizing the system with optimum response achieved by varying the compensating capacitor.