

1-, 2- and 4-Channel Low Capacitance ESD Protection Arrays

CM1213A

Features

- One, two, and four channels of ESD protection
 Note: For 6- and 8-channel devices, see the CM1213 datasheet.
- Provides ESD protection to IEC61000-4-2 Level 4
 - ±8kV contact discharge
- Low channel input capacitance of 0.85pF typical
- Minimal capacitance change with temperature and voltage
- Channel input capacitance matching of 0.02pF typical is ideal for differential signals
- Zener diode protects supply rail and eliminates the need for external by-pass capacitors
- Each I/O pin can withstand over 1000 ESD strikes*
- Available in SOT, SC70, and MSOP RoHScompliant, lead-free packages

Applications

- USB2.0 ports at 480Mbps in desktop PCs, notebooks and peripherals
- IEEE1394 Firewire® ports at 400Mbps / 800Mbps
- DVI ports, HDMI ports in notebooks, set top boxes, digital TVs, LCD displays
- Serial ATA ports in desktop PCs and hard disk drives
- PCI Express ports
- General purpose high-speed data line ESD protection

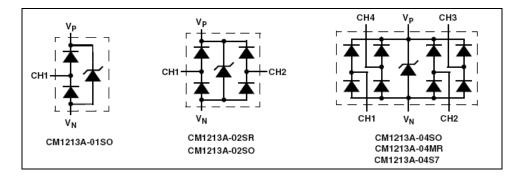
Product Description

The CM1213A family of diode arrays has been designed to provide ESD protection for electronic components or subsystems requiring capacitive loading. These devices are ideal for protecting systems with high data and clock rates or for circuits requiring low capacitive loading. Each ESD channel consists of a pair of diodes in series which steer the positive or negative ESD current pulse to either the positive $(V_{\scriptscriptstyle R})$ or negative $(V_{\scriptscriptstyle N})$ supply rail. A Zener diode is embedded between V_P and V_N, offering two advantages. First, it protects the V_{cc} rail against ESD strikes, and second, it eliminates the need for a bypass capacitor that would otherwise be needed for absorbing positive ESD strikes to ground. The CM1213A will protect against ESD pulses up to 8kV per the IEC 61000-4-2 standard.

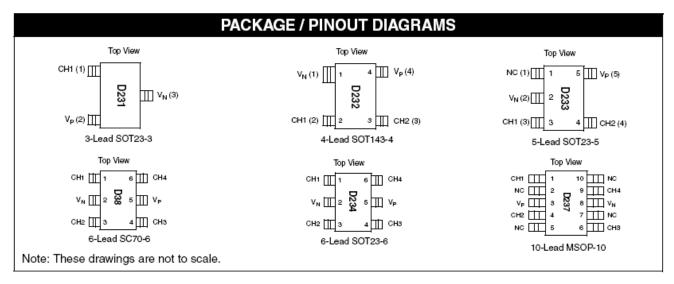
These devices are particularly well-suited for protecting systems using high-speed ports such as USB 2.0, IEEE1394 (Firewire®, iLinkTM), Serial ATA, DVI, HDMI and corresponding ports in removable storage, digital camcorders, DVD-RW drives and other applications where extremely low loading capacitance with ESD protection are required in a small package footprint.

The CM1213A family of devices is available with RoHS-compliant, lead-free finishing.

Block Diagram



Package/Pinout Diagrams



Pin Descriptions

1-CHANNEL, 3-LEAD SOT23-3 PACKAGE (CM1213A-01SO)						
PIN	NAME	TYPE	DESCRIPTION			
1	CH1	I/O	ESD Channel			
2	V _P	PWR	Positive voltage supply rail			
3	V _N	GND	Negative voltage supply rail			

	2-CHANNEL, 4-LEAD SOT143-4 PACKAGE (CM1213A-02SR)					
PIN	NAME	TYPE	DESCRIPTION			
1	$V_{_{\rm N}}$	GND	Negative voltage supply rail			
2	CH1	I/O	ESD Channel			
3	CH2	I/O	ESD Channel			
4	V _P	PWR	Positive voltage supply rail			
	2-CHANNE	L, 5-LEAD SO	T23-5 PACKAGE (CM1213A-02SO)			
PIN	NAME	TYPE	DESCRIPTION			
1	NC		No Connect			
2	$V_{_{\rm N}}$	GND	Negative voltage supply rail			
3	CH1	I/O	ESD Channel			
4	CH2	I/O	ESD Channel			
5	$V_{_{P}}$	PWR	Positive voltage supply rail			

4-CH, 6	4-CH, 6-LEAD SOT23-6 (CM1213A-04SO) AND SC70-6 (CM1213A- 04S7)						
PIN	NAME	TYPE	DESCRIPTION				
1	CH1	I/O	ESD Channel				
2	$V_{_{N}}$	GND	Negative voltage supply rail				
3	CH2	I/O	ESD Channel				
4	CH3	I/O	ESD Channel				
5	$V_{_{P}}$	PWR	Positive voltage supply rail				
6	CH4	I/O	ESD Channel				
4-CH	4-CHANNEL, 10-LEAD MSOP-10 PACKAGE (CM1213A04MR)						
PIN	NAME	TYPE	DESCRIPTION				
1	CH1	I/O	ESD Channel				
2	NC		No Connect				
3	$V_{_{P}}$	PWR	Positive voltage supply rail				
4	CH2	I/O	ESD Channel				
5	NC		No Connect				
6	CH3	I/O	ESD Channel				
7	NC		No Connect				
8	$V_{_{N}}$	GND	Negative voltage supply rail				
9	CH4	I/O	ESD Channel				
10	NC		No Connect				

Ordering Information

PART NUMBERING INFORMATION							
			Lead-free	Finish			
# of Channels	Leads	Package	Ordering Part Number ¹	Part Marking			
1	3	SOT23-3	CM1213A-01SO	D231			
2	4	SOT143-4	CM1213A-02SR	D232			
2	5	SOT23-5	CM1213A-02SO	D233			
4	6	SOT23-6	CM1213A-04SO	D234			
4	6	SC70-6	CM1213A-04S7	D38			
4	10	MSOP-10	CM1213A-04MR	D237			

Note 1: Parts are shipped in Tape and Reel form unless otherwise specified.

Specifications

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	RATING	UNITS			
Operating Supply Voltage (V _P - V _N)	6.0	V			
Operating Temperature Range	-40 to +85	.€			
Storage Temperature Range	−65 to +150	℃			
DC Voltage at any channel input	$(V_{_{\rm N}}$ - 0.5) to $(V_{_{\rm P}}$ + 0.5)	V			

STANDARD OPERATING CONDITIONS					
PARAMETER	RATING	UNITS			
Operating Temperature Range	-40 to +85	°C			
Package Power Rating SOT23-3, SOT143-4,SOT23-5, SOT23-6, and SC70-6 Packages MSOP-10 Package	225 400	mW mW			

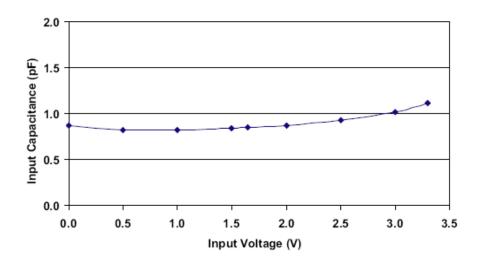
	ELECTRICAL OPERATING CHARACTERISTICS (SEE NOTE 1)								
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS			
V _P	Operating Supply Voltage (V _P -V _N)			3.3	5.5	V			
I _P	Operating Supply Current	$(V_P - V_N) = 3.3V$			8.0	μΑ			
V _F	Diode Forward Voltage Top Diode Bottom Diode	I _F = 8mA; T _A =25 ℃	0.60 0.60	0.80 0.80	0.95 0.95	V V			
I _{LEAK}	Channel Leakage Current	$T_{A}=25$ °C; $V_{P}=5V$, $V_{N}=0V$		0.1	1.0	μΑ			
C _{IN}	Channel Input Capacitance	At 1 MHz, V _p =3.3V, V _N =0V, V _{IN} =1.65V; Note 2		0.85	1.2	pF			
ΔC_{IN}	Channel Input Capacitance Matching	At 1 MHz, $V_p=3.3V$, $V_N=0V$, $V_{IN}=1.65V$; Note 2		0.02		pF			
V _{ESD}	ESD Protection - Peak Discharge Voltage at any channel input, in system Contact discharge per IEC 61000-4-2 standard	T _A =25 °C; Notes 2, 3, and 4	8			kV			
V _{CL}	Channel Clamp Voltage Positive Transients Negative Transients	T_A =25 °C, I_{pp} = 1A, t_p = 8/20 μ S; Notes 2 and 4		+10 -1.7		V			
R _{DYN}	Dynamic Resistance Positive Transients Negative Transients	I_{pp} = 1A, t_p = 8/20 μ S Any I/O pin to Ground; Notes 2 and 4		0.9 0.5		Ω			

Note 1: All parameters specified at $T_{_A}$ = -40 °C to +85 °C unless otherwise noted. Note 2: Standard IEC 61000-4-2 with $C_{_{Discharge}}$ = 150pF, $R_{_{Discharge}}$ = 330 Ω , $V_{_P}$ = 3.3V, $V_{_N}$ grounded. Note 3: These measurements performed with no external capacitor on $V_{_P}$ ($V_{_P}$ floating).

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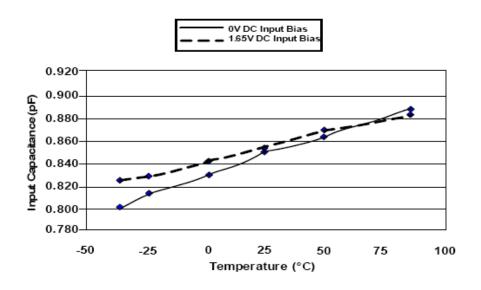
Performance Information

Input Channel Capacitance Performance Curves



Typical Variation of C_{IN} vs. V_{IN}

(f=1MHz, Vp = 3.3V, VN = 0V, 0.1 μ F chip capacitor between Vp and VN, 25 $^{\circ}$ C)



Typical Variation of C_{IN} vs. Temp

(f=1MHz, V_{IN} =30mV, V_P = 3.3V, V_N = 0V, 0.1 μ F chip capacitor between V_P and V_N)

Performance Information (cont'd)

Typical Filter Performance (nominal conditions unless specified otherwise, 50 Ohm Environment)

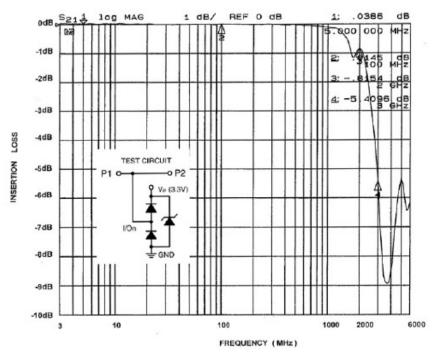


Figure 1. Insertion Loss (S21) VS. Frequency (0V DC Bias, V_p=3.3V)

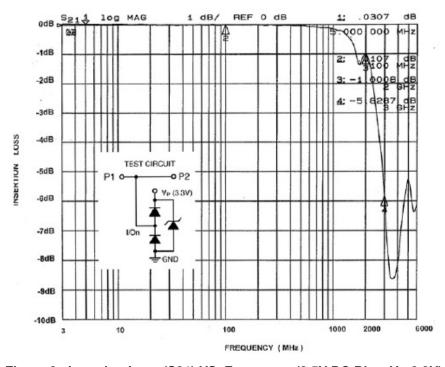


Figure 2. Insertion Loss (S21) VS. Frequency (2.5V DC Bias, V_p=3.3V)

CM1213A

Application Information

Design Considerations

In order to realize the maximum protection against ESD pulses, care must be taken in the PCB layout to minimize parasitic series inductances on the Supply/Ground rails as well as the signal trace segment between the signal input (typically a connector) and the ESD protection device. Refer to **Error! Reference source not found.**, which illustrates an example of a positive ESD pulse striking an input channel. The parasitic series inductance back to the power supply is represented by L_1 and L_2 . The voltage V_{CL} on the line being protected is:

$$V_{cL}$$
 = Fwd voltage drop of D_1 + V_{SUPPLY} + L_1 x d(I_{ESD}) / dt + L_2 x d(I_{ESD}) / dt

where I_{ESD} is the ESD current pulse, and V_{SUPPLY} is the positive supply voltage.

An ESD current pulse can rise from zero to its peak value in a very short time. As an example, a level 4 contact discharge per the IEC61000-4-2 standard results in a current pulse that rises from zero to 30 Amps in 1ns. Here $d(I_{ESD})/dt$ can be approximated by $\Delta I_{ESD}/\Delta t$, or $30/(1\times10^{-9})$. So just 10nH of series inductance (L₁ and L₂ combined) will lead to a 300V increment in V_{CL} !

Similarly for negative ESD pulses, parasitic series inductance from the V_{N} pin to the ground rail will lead to drastically increased negative voltage on the line being protected.

The CM1213A has an integrated Zener diode between $V_{_{P}}$ and $V_{_{N}}$. This greatly reduces the effect of supply rail inductance $L_{_{2}}$ on $V_{_{CL}}$ by clamping $V_{_{P}}$ at the breakdown voltage of the Zener diode. However, for the lowest possible $V_{_{CL}}$, especially when $V_{_{P}}$ is biased at a voltage significantly below the Zener breakdown voltage, it is recommended that a $0.22\mu F$ ceramic chip capacitor be connected between $V_{_{P}}$ and the ground plane.

As a general rule, the ESD Protection Array should be located as close as possible to the point of entry of expected electrostatic discharges. The power supply bypass capacitor mentioned above should be as close to the V_P pin of the Protection Array as possible, with minimum PCB trace lengths to the power supply, ground planes and between the signal input and the ESD device to minimize stray series inductance.

Additional Information

See also California Micro Devices Application Note AP209, "Design Considerations for ESD Protection," in the Applications section at www.calmicro.com.

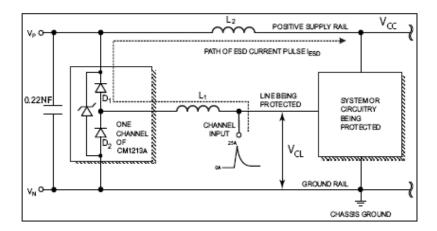


Figure 3. Application of Positive ESD Pulse Between Input Channel and Ground

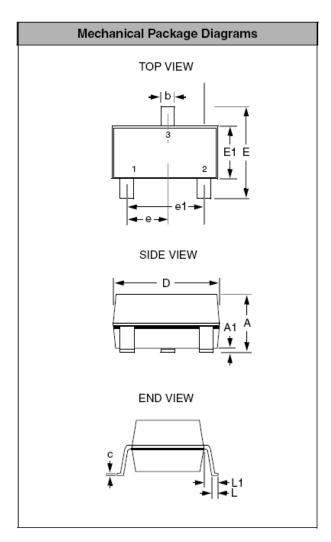
Mechanical Details

The CM1213A is available in SOT23-3, SOT143-4, and SOT23-5, SOT23-6, SC70-6, and MSOP-10 lead-free packages.

SOT23-3 Mechanical Specifications, 3 pin

The CM1213A-01SO is supplied in a 3-pin SOT23 package. Dimensions are presented below.

PACKAGE DIMENSIONS						
Package	SOT2	3-3 (JEDEC	name is T	O-236)		
JEDEC No.		TO-236	(Var. AB)			
Pins/Leads		;	3			
Dimensions	Millir	neters	Inc	hes		
Difficusions	Min	Max	Min	Max		
Α	0.89	1.12	0.0350	0.0441		
A1	0.01	0.10	0.0004	0.0039		
b	0.30	0.50	0.0118	0.0197		
С	0.08	0.20	0.0031	0.0079		
D	2.80	3.04	0.1102	0.1197		
E	2.10	2.64	0.0827	0.1039		
E1	1.20	1.40	0.0472	0.0551		
е	0.95	BSC	0.037	4 BSC		
e1	1.90	BSC	0.074	8 BSC		
L	0.40	0.60	0.0157	0.0236		
L1	0.54	REF	0.0213 REF			
# per tape and reel	3000 pieces					
Controlling dimension: millimeters						

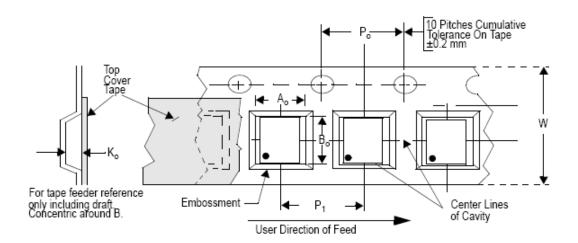


Package Dimensions for SOT23-3

CM1213A

Tape and Reel Specifications

PART NUMBE	PACKAGE SIZE (mm)	POCKET SIZE (mm) B ₀ X A ₀ X K ₀	TAPE WIDTH W	REEL DIAMETER	QTY PER REEL	P _o	P ₁
CM1213A-01S	O 2.92 X 2.37 X 1.01	2.77 X 3.15 X 1.22	8mm	178mm (7")	3000	4mm	4mm

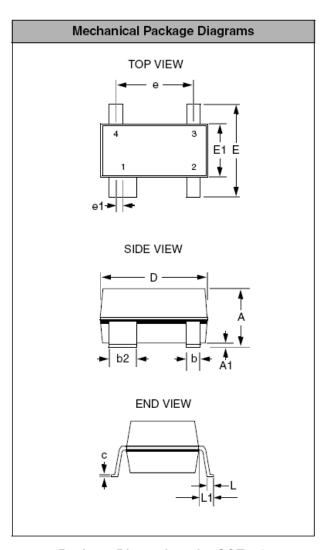


Mechanical Details (Cont'd)

SOT143-4 Mechanical Specifications, 4 pin

The CM1213A-02SR is packaged in a 4-pin SOT143 package. Dimensions are presented below.

PACKAGE DIMENSIONS						
Package		SO	Γ143			
Pins			4			
Dimensions	Millir	neters	Inc	hes		
Dimensions	Min	Max	Min	Max		
Α	0.80	1.22	0.031	0.048		
A1	0.05	0.15	0.002	0.006		
b	0.30	0.50	0.012	0.019		
b2	0.76	0.89	0.030	0.035		
С	0.08	0.20	0.003	0.008		
D	2.80	3.04	0.110	0.119		
E	2.10	2.64	0.082	0.103		
E1	1.20	1.40	0.047	0.055		
е	1.92	BSC	0.075	5 BSC		
e1	0.20	BSC	0.008	B BSC		
L	0.4	0.6	0.016	0.024		
L1	0.54	REF	0.021	I REF		
# per tape and reel	3000 pieces					
Controlling dimension: millimeters						

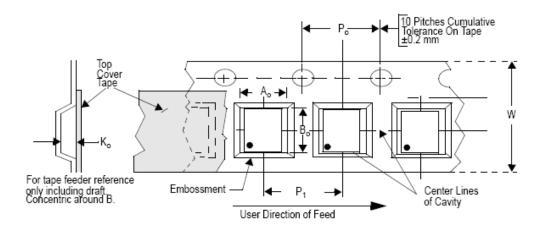


Package Dimensions for SOT143

CM1213A

Tape and Reel Specifications

PART NUMBER	PACKAGE SIZE (mm)	POCKET SIZE (mm) B _o X A _o X K _o	TAPE WIDTH W	REEL DIAMETER	QTY PER REEL	P _o	P,
CM1213A-02SR	2.92 X 2.37 X 1.01	2.60 X 3.15 X 1.20	8mm	178mm (7'')	3000	4mm	4mm

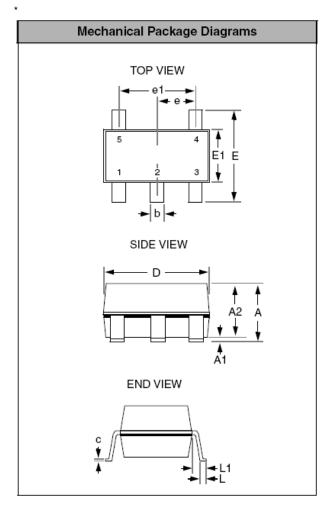


Mechanical Details (cont'd)

SOT23-5 Mechanical Specifications, 5 pin

The CM1213A-02SO is supplied in a 5-pin SOT23 package. DImensions are presented below.

	PAC	KAGE	DIME	NSIO	NS			
Package		SOT23-5						
JEDEC No.		MO-178 (Var. AA)						
Leads				5				
Dim.	N	lillimete	rs		Inches			
Diiii.	Min	Nom	Max	Min	Nom	Max		
Α	1.14	1.24	1.35	0.045	0.049	0.053		
A 1	0.05	0.10	0.15	0.002	0.004	0.006		
A2	1.09	1.14	1.19	0.043	0.045	0.047		
b	0.30	0.35	0.40	0.012	0.014	0.016		
С	0.08	0.15	0.22	0.003	0.006	0.009		
D	2.87	2.92	2.97	0.113	0.115	0.117		
E	2.67	2.79	2.92	0.105	0.110	0.115		
E1	1.55	1.63	1.68	0.061	0.064	0.066		
е	(0.95 RE	F	C	.020 RE	F		
e1		1.90 RE	F	C	.075 RE	F		
L	0.35	0.40	0.45	0.014	0.016	0.018		
L1	0.53	0.58	0.64	0.021	0.023	0.025		
# per tape and reel	3000 pieces							
	Controlling dimension: millimeters							

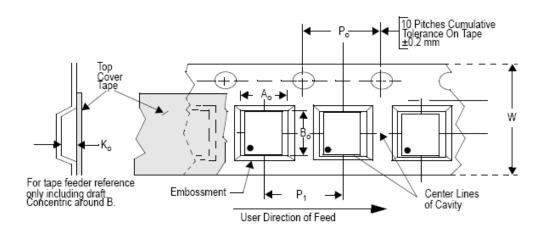


Package Dimensions for SOT23-5

CM1213A

Tape and Reel Specifications

PART NUMBER	PACKAGE SIZE (mm)	POCKET SIZE (mm) B _o X A _o X K _o	TAPE WIDTH W	REEL DIAMETER	QTY PER REEL	P _o	P ₁
CM1213A-02SO	2.92 X 2.79 X 1.24	3.20 X 3.20 X 1.40	8mm	178mm (7")	3000	4mm	4mm





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