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PMIC N/A	PREPARED BY <i>Marcia B Kelleher</i>	DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444		
<b>STANDARDIZED MILITARY DRAWING</b>  THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE  AMSC N/A	CHECKED BY <i>Thomas J. Picanti</i>			MICROCIRCUITS, DIGITAL, HIGH SPEED CMOS, 8-BIT SYNCHRONOUS BCD DOWN COUNTER, TTL, COMPATIBLE, MONOLITHIC SILICON
	APPROVED BY <i>[Signature]</i>			
	DRAWING APPROVAL DATE 20 SEPTEMBER 1990	SIZE <b>A</b>	CAGE CODE <b>67268</b>	
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5962-E1657

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

# 1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:

5962-90574	01	E	X
Drawing number	Device type (1.2.1)	Case outline (1.2.2)	Lead finish per MIL-M-38510

1.2.1 Device type. The device type shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	54HCT40102	8-bit synchronous BCD down counter, TTL compatible inputs

1.2.2 Case outline. The case outline shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
E	D-2 (16-lead, .840" x .310" x .200"), dual-in-line package

## 1.3 Absolute maximum ratings. 1/

Supply voltage range ( $V_{CC}$ )	- - - - -	-0.5 V dc to +7.0 V dc
DC input voltage range	- - - - -	-0.5 V dc to $V_{CC} + 0.5$ V dc
DC output voltage range	- - - - -	-0.5 V dc to $V_{CC} + 0.5$ V dc
DC input diode current	- - - - -	±20 mA
DC output diode current (per pin)	- - - - -	±20 mA
DC drain current (per pin)	- - - - -	±25 mA
DC $V_{CC}$ or GND current	- - - - -	±50 mA
Storage temperature range	- - - - -	-65°C to +150°C
Maximum power dissipation ( $P_D$ )	- - - - -	500 mW 2/
Lead temperature (soldering, 10 seconds)	- - - - -	+300°C
Thermal resistance, junction-to-case ( $\theta_{JC}$ )	- - - - -	See MIL-M-38510, appendix C
Junction temperature ( $T_J$ )	- - - - -	+175°C

1/ Unless otherwise specified, all voltages are referenced to ground.

2/ For  $T_C = +100^\circ\text{C}$  to  $+125^\circ\text{C}$ , derate linearly at 8 mW/°C.

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#### 1.4 Recommended operating conditions.

Supply voltage range ( $V_{CC}$ ) - - - - - +4.5 V dc to +5.5 V dc

Input voltage range ( $V_{IN}$ ) - - - - - 0.0 V dc to  $V_{CC}$

Output voltage range ( $V_{OUT}$ ) - - - - - 0.0 V dc to  $V_{CC}$

Case operating temperature range ( $T_C$ ) - - -55°C to +125°C

Input rise or fall time ( $t_r$ ,  $t_f$ ):

$V_{CC} = 4.5$  V - - - - - 0 to 500 ns

Maximum CP frequency ( $f_{MAX}$ ):

$T_C = +25^\circ\text{C}$ ,  $V_{CC} = 4.5$  V - - - - - 14 MHz

$T_C = -55^\circ\text{C}$  to +125°C,  $V_{CC} = 4.5$  V - - - 9 MHz

Minimum pulse width,  $\overline{MR}$ , CP ( $t_{w1}$ ):

$T_C = +25^\circ\text{C}$ ,  $V_{CC} = 4.5$  V - - - - - 35 ns

$T_C = -55^\circ\text{C}$  to +125°C,  $V_{CC} = 4.5$  V - - - 53 ns

Minimum pulse width,  $\overline{PL}$  ( $t_{w2}$ ):

$T_C = +25^\circ\text{C}$ ,  $V_{CC} = 4.5$  V - - - - - 38 ns

$T_C = -55^\circ\text{C}$  to +125°C,  $V_{CC} = 4.5$  V - - - 57 ns

Minimum removal time,  $\overline{MR}$  to CP ( $t_{rem}$ ):

$T_C = +25^\circ\text{C}$ ,  $V_{CC} = 4.5$  V - - - - - 10 ns

$T_C = -55^\circ\text{C}$  to +125°C,  $V_{CC} = 4.5$  V - - - 15 ns

Minimum setup time, Pn to CP ( $t_{s1}$ ):

$T_C = +25^\circ\text{C}$ ,  $V_{CC} = 4.5$  V - - - - - 24 ns

$T_C = -55^\circ\text{C}$  to +125°C,  $V_{CC} = 4.5$  V - - - 36 ns

Minimum setup time,  $\overline{PE}$  to CP ( $t_{s2}$ ):

$T_C = +25^\circ\text{C}$ ,  $V_{CC} = 4.5$  V - - - - - 20 ns

$T_C = -55^\circ\text{C}$  to +125°C,  $V_{CC} = 4.5$  V - - - 30 ns

Minimum setup time,  $\overline{TE}$  to CP ( $t_{s3}$ ):

$T_C = +25^\circ\text{C}$ ,  $V_{CC} = 4.5$  V - - - - - 40 ns

$T_C = -55^\circ\text{C}$  to +125°C,  $V_{CC} = 4.5$  V - - - 60 ns

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#### 1.4 Recommended operating conditions - Continued.

Minimum hold time, Pn to CP ( $t_{h1}$ ):

$T_C = +25^\circ\text{C}$ ,  $V_{CC} = 4.5\text{ V}$  - - - - - 5 ns

$T_C = -55^\circ\text{C}$  to  $+125^\circ\text{C}$ ,  $V_{CC} = 4.5\text{ V}$  - - - 5 ns

Minimum hold time, TE to CP ( $t_{h2}$ ):

$T_C = +25^\circ\text{C}$ ,  $V_{CC} = 4.5\text{ V}$  - - - - - 0 ns

$T_C = -55^\circ\text{C}$  to  $+125^\circ\text{C}$ ,  $V_{CC} = 4.5\text{ V}$  - - - 0 ns

Minimum hold time, PE to CP ( $t_{h3}$ ):

$T_C = +25^\circ\text{C}$ ,  $V_{CC} = 4.5\text{ V}$  - - - - - 2 ns

$T_C = -55^\circ\text{C}$  to  $+125^\circ\text{C}$ ,  $V_{CC} = 4.5\text{ V}$  - - - 2 ns

#### 2. APPLICABLE DOCUMENTS

2.1 Government specification, standard, and bulletin. Unless otherwise specified, the following specification, standard, and bulletin of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

##### SPECIFICATION

###### MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

##### STANDARD

###### MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

##### BULLETIN

###### MILITARY

MIL-BUL-103 - List of Standardized Military Drawings (SMD's).

(Copies of the specification, standard, and bulletin required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

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### 3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Logic diagram. The logic diagram shall be as specified on figure 3.

3.2.4 Timing diagram. The timing diagram shall be as specified on figure 4.

3.2.5 Case outline. The case outline shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and apply over the full case operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in MIL-BUL-103 (see 6.6 herein).

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (see 6.6 herein). The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.9 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

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TABLE 1. Electrical performance characteristics.							
Test	Symbol	Conditions -55°C < T <sub>C</sub> < +125°C 1/ unless otherwise specified		Group A subgroups	Limits		Unit
					Min	Max	
High level output voltage	V <sub>OH</sub>	V <sub>CC</sub> = 4.5 V, V <sub>IN</sub> = 2.0 V or 0.8 V	I <sub>O</sub>   = 20 μA   I <sub>O</sub>   = 4.0 mA	1, 2, 3	4.4  3.7		V
Low level output voltage	V <sub>OL</sub>	V <sub>CC</sub> = 4.5 V, V <sub>IN</sub> = 2.0 V or 0.8 V	I <sub>O</sub>   = 20 μA   I <sub>O</sub>   = 4.0 mA	1, 2, 3		0.1  0.4	
High level input voltage	V <sub>IH</sub>	V <sub>CC</sub> = 4.5 V 2/		1, 2, 3	2.0		
Low level input voltage	V <sub>IL</sub>	V <sub>CC</sub> = 4.5 V 2/		1, 2, 3		0.8	
Input capacitance	C <sub>IN</sub>	See 4.3.1c		4		10	pF
Quiescent current	I <sub>CC</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = V <sub>CC</sub> or GND I <sub>OUT</sub> = 0.0 A		1, 2, 3		160	μA
Input leakage current	I <sub>IN</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = V <sub>CC</sub> or GND		1, 2, 3		±1.0	
Functional tests		See 4.3.1d		7, 8			
Additional quiescent supply current 3/	delta I <sub>CC</sub>	Any 1 input: V <sub>IN</sub> = 2.4 V Other inputs: V <sub>IN</sub> = V <sub>CC</sub> or GND I <sub>OUT</sub> = 0.0 A V <sub>CC</sub> = 5.5 V		1, 2, 3		3.0	mA
See footnotes at end of table.							
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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C < T <sub>C</sub> < +125°C 1/ unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
Propagation delay time, CP to TC <u>3/</u> (asynchronous preset)	t <sub>PLH1</sub> , t <sub>PHL1</sub>	V <sub>CC</sub> = 4.5 V C <sub>L</sub> = 50 pF See figure 5	9		60	ns
			10, 11		90	
Propagation delay time, CP to TC <u>3/</u> (synchronous preset)	t <sub>PLH2</sub> , t <sub>PHL2</sub>		9		63	
			10, 11		95	
Propagation delay time, TE to TC <u>3/</u>	t <sub>PLH3</sub> , t <sub>PHL3</sub>		9		50	
			10, 11		75	
Propagation delay time, PL to TC	t <sub>PLH4</sub> , t <sub>PHL4</sub>		9		68	
			10, 11		102	
Propagation delay time, MR to TC <u>3/</u>	t <sub>PLH5</sub> , t <sub>PHL5</sub>		9		55	
			10, 11		83	
Output transition time <u>4/</u>	t <sub>TLH</sub> , t <sub>THL</sub>		9		15	
			10, 11		22	

1/ For a power supply of 5 V ±10%, the worst case output voltages (V<sub>OH</sub> and V<sub>OL</sub>) occur for HCT at 4.5 V. Thus, the 4.5 V values should be used when designing with this supply. Worst cases V<sub>IH</sub> and V<sub>IL</sub> occur at V<sub>CC</sub> = 5.5 V and 4.5 V respectively.

2/ The V<sub>IH</sub> and V<sub>IL</sub> tests are not required, and shall be applied as forcing functions for the V<sub>OH</sub> or V<sub>OL</sub> tests.

3/ This parameter is guaranteed, if not tested, to the specified limits in table I.

4/ Transition time (t<sub>TLH</sub>, t<sub>THL</sub>), if not tested, shall be guaranteed to the specified limits.

#### 4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) T<sub>A</sub> = +125°C, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

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Device type	01
Case outline	E
Terminal number	Terminal symbol
1	CP
2	MR
3	TE
4	P0
5	P1
6	P2
7	P3
8	GND
9	PT
10	P4
11	P5
12	P6
13	P7
14	TC
15	PE
16	V <sub>CC</sub>

FIGURE 1. Terminal connections.

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Inputs				Preset mode	Action
MR	PL	PE	TE		
H	H	H	H	Synchronous	Inhibit counter
H	H	H	L		Count down
H	H	L	X		Preset on next positive clock transition
H	L	X	X	Asynchronous	Preset asynchronously
L	X	X	X		Clear to maximum count

NOTES:

1. H = High voltage level  
L = Low voltage level  
X = Irrelevant
2. Clock connected to clock input.
3. Synchronous operation:  
Changes occur on negative-to-positive clock transition.
4. Load input:  
MSD = P7, P6, P5, P4, (P7's is MSB).  
LSD = P3, P2, P1, P0, (P3's is MSB).

FIGURE 2. Truth table.

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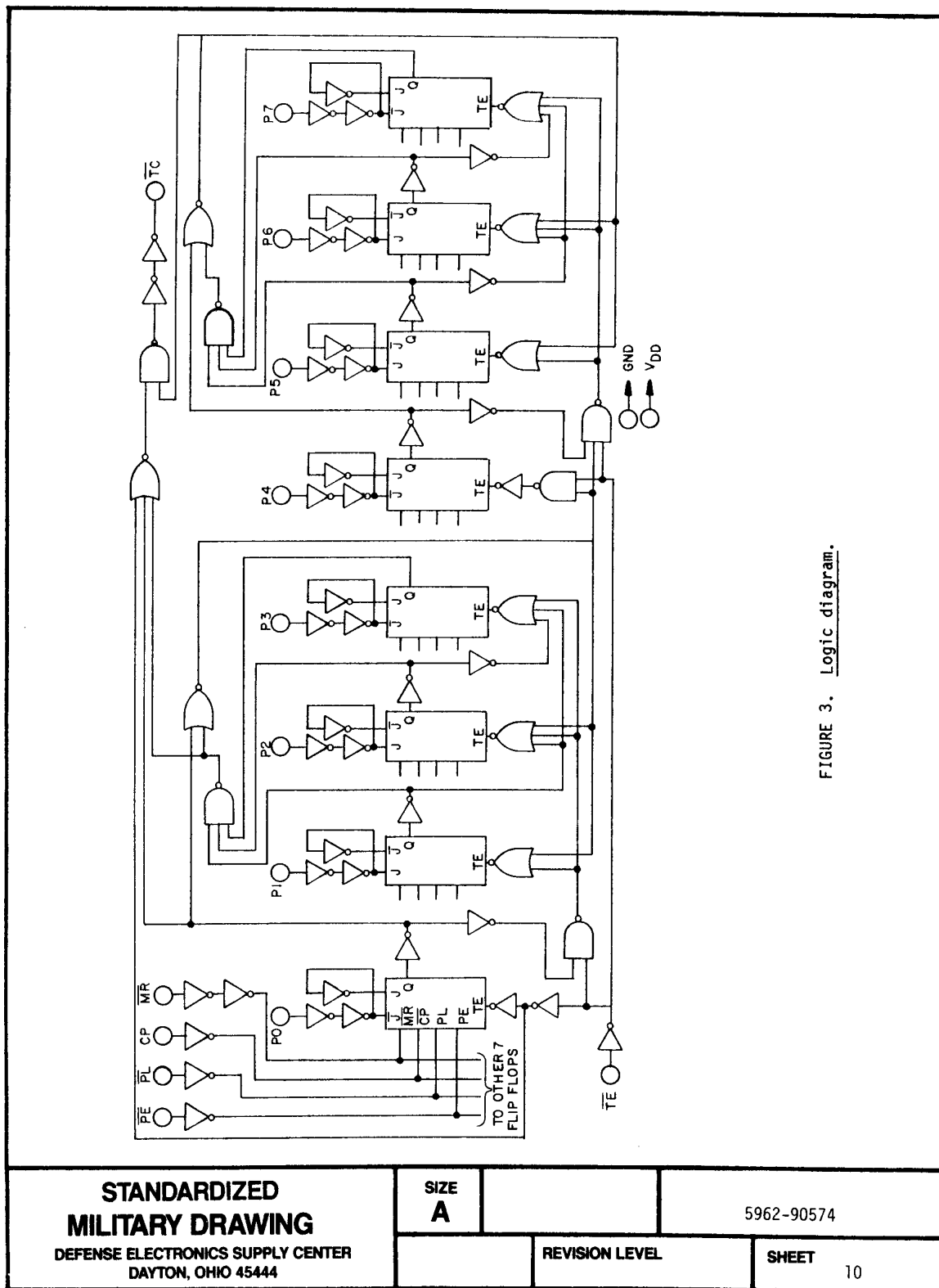


FIGURE 3. Logic diagram.

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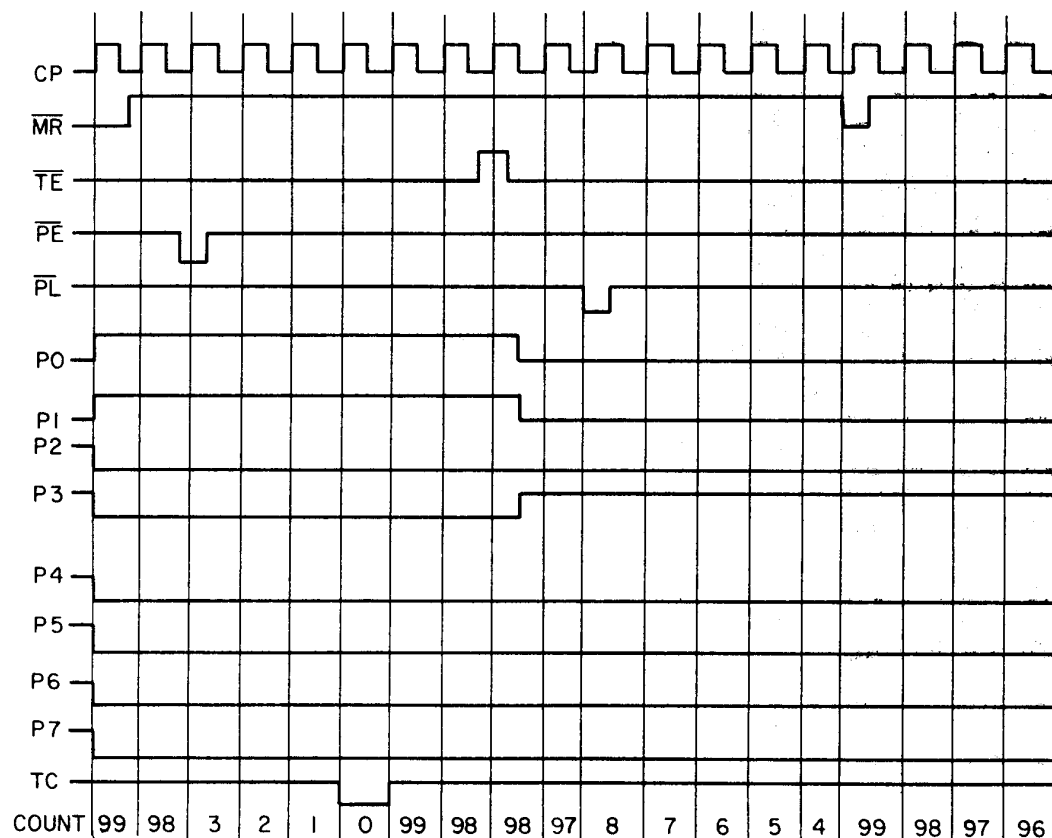
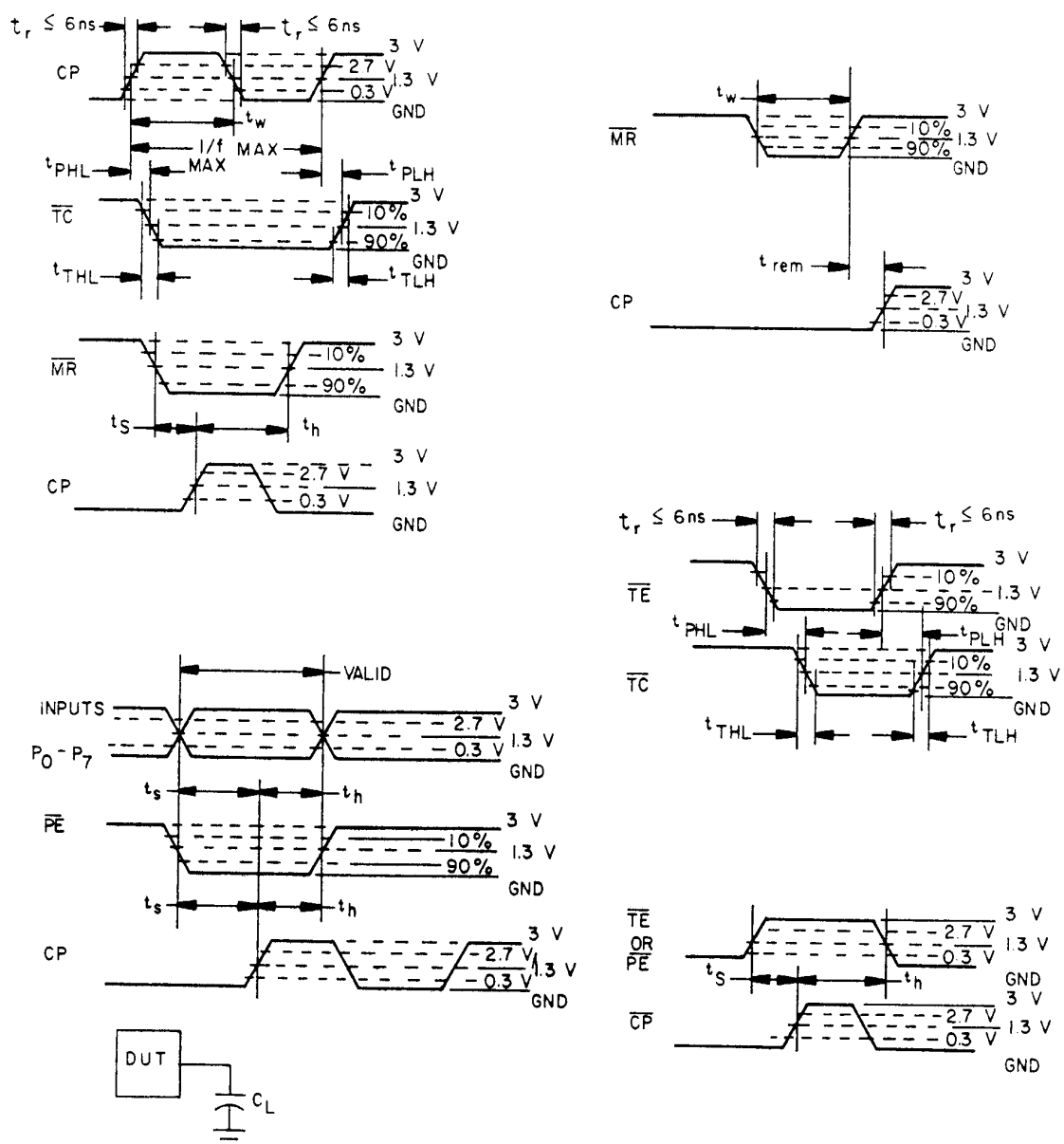


FIGURE 4. Timing diagram.

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$C_L = 50\text{ pF}$ , includes probe and jig capacitance.

FIGURE 5. Switching waveforms and test circuit.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*, 2, 3, 7, 8, 9
Group A test requirements (method 5005)	1, 2, 3, 4, 7, 8, 9, 10, 11
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

\* PDA applies to subgroup 1.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

- Tests shall be as specified in table II herein.
- Subgroups 5 and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.
- Subgroup 4 ( $C_{IN}$  measurement) shall be measured only for the initial test and after process or design changes which may affect capacitance. Capacitance shall be measured between the designated terminal and GND at a frequency of 1 MHz. Test all applicable pins on five devices with zero failures.
- Subgroups 7 and 8 tests shall verify the truth table as specified on figure 2.

4.3.2 Groups C and D inspections.

- End-point electrical parameters shall be as specified in table II herein.
- Steady-state life test conditions, method 1005 of MIL-STD-883.
  - Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).
  - $T_A = +125^\circ\text{C}$ , minimum.
  - Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

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6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-ECS, telephone (513) 296-6022.

6.5 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone (513) 296-5375.

6.6 Approved source of supply. An approved source of supply is listed in MIL-BUL-103. Additional sources will be added to MIL-BUL-103 as they become available. The vendor listed in MIL-BUL-103 has agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DESC-ECS. The approved source of supply listed below is for information purposes only and is current only to the date of the last action of this document.

Military drawing part number	Vendor CAGE number	Vendor similar part number 1/
5962-9057401EX	34371	CD54HCT40102F/3A

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE  
number

34371

Vendor name  
and address

Harris Semiconductor  
P.O. Box 883  
Melbourne, FL 32901

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