



#### COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

#### **Features**

Low On-Resistance

N-Channel: 32mΩ @ 10V

46mΩ @ 4.5V

P-Channel: 39mΩ @ 10V

 $53m\Omega$  @ 4.5V

- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Complementary Pair MOSFET
- Lead Free/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

• Case: SO-8

Case Material: Molded Plastic, "Green" Molding Compound.
UL Flammability Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020

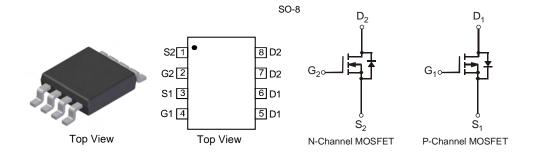
Terminals Connections: See Diagram

Terminals: Finish - Matte Tin annealed over Copper lead frame.
Solderable per MIL-STD-202, Method 208

Marking Information: See Page 6

Ordering Information: See Page 6

Weight: 0.072 grams (approximate)



### Maximum Ratings N-CHANNEL – Q1 @TA = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	30	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 3)	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 85°C	I <sub>D</sub>	8.1 5.1	А
Pulsed Drain Current (Note 4)			I <sub>DM</sub>	25	А

### Maximum Ratings P-CHANNEL – Q2 @TA = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	-30	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 3)	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 85°C	I <sub>D</sub>	-7.0 -4.5	Α
Pulsed Drain Current (Note 4)			I <sub>DM</sub>	-25	А

## **Thermal Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P <sub>D</sub>	2.5	W
Thermal Resistance, Junction to Ambient (Note 3)	$R_{\theta JA}$	50	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

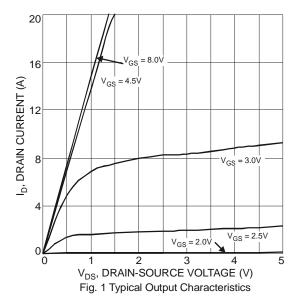
Notes:

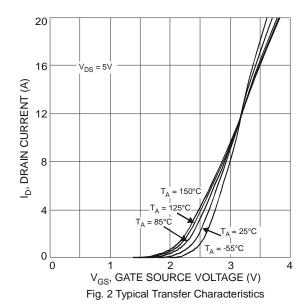
- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
- 3. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- 4. Repetitive rating, pulse width limited by junction temperature.



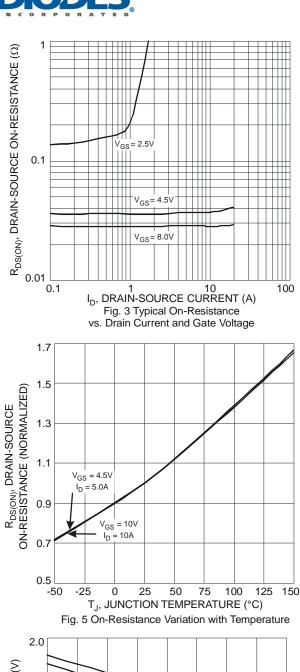
# Electrical Characteristics N-CHANNEL - Q1 @TA = 25°C unless otherwise specified

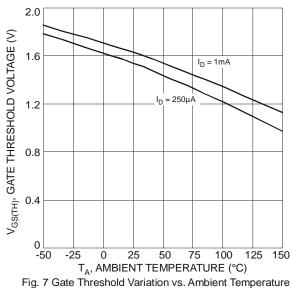
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T <sub>J</sub> = 25°C	I <sub>DSS</sub>	-	-	1.0	μΑ	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	1	1.45	2.1	V	$V_{DS} = V_{GS}$ , $I_C = 250\mu A$
Static Drain-Source On-Resistance	D		23	32	mΩ	$V_{GS} = 10V, I_C = 7A$
Static Dialit-Source Off-Resistance	R <sub>DS (ON)</sub>	-	32	46	111 2 2	$V_{GS} = 4.5V, I_{C} = 5.6A$
Forward Transfer Admittance	Y <sub>fs</sub>	-	7.6	-	S	$V_{DS} = 5V$ , $I_C = 7A$
Diode Forward Voltage (Note 5)	$V_{SD}$	-	0.7	1.0	V	$V_{GS} = 0V, I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 6)						_
Input Capacitance	C <sub>iss</sub>	-	404.5	-	pF	\\ 45\\\\\ 0\\
Output Capacitance	Coss	-	51.8	-	pF	$V_{DS} = 15V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	-	45.1	-	pF	1 – 1.01/11/12
Gate Resistance	$R_{g}$	-	1.5	-	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
Total Gate Charge (10V)	$Q_g$	-	9.2	-	nC	101/11/ 451/
Gate-Source Charge	Q <sub>gs</sub>	-	1.2	-	nC	$V_{GS} = 10V, V_{DS} = 15V,$
Gate-Drain Charge	$Q_{gd}$	-	1.8	-	nC	$I_D = 5.8A$
Turn-On Delay Time	t <sub>D(on)</sub>	-	3.4	-	ns	
Turn-On Rise Time	t <sub>r</sub>	-	6.18	-	ns	$V_{GS} = 10V, V_{DS} = 15V,$
Turn-Off Delay Time	t <sub>D(off)</sub>	-	13.92	-	ns	$R_G = 3\Omega$ , $R_L = 2.6\Omega$
Turn-Off Fall Time	t <sub>f</sub>	-	2.84	-	ns	











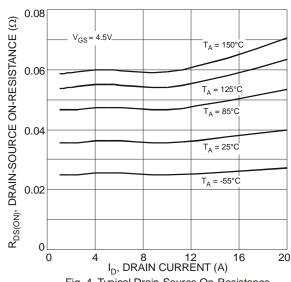


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

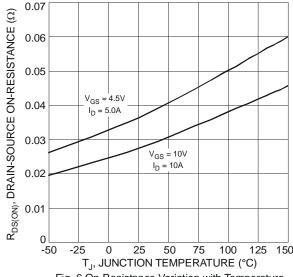


Fig. 6 On-Resistance Variation with Temperature

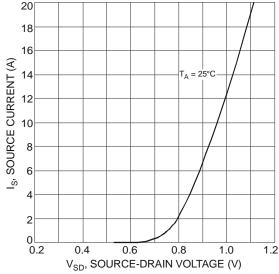
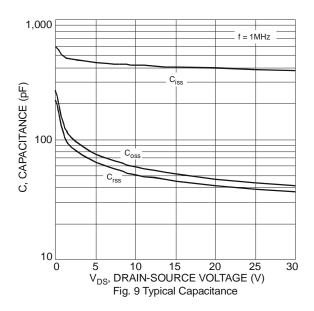
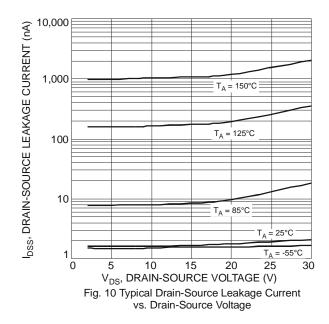


Fig. 8 Diode Forward Voltage vs. Current







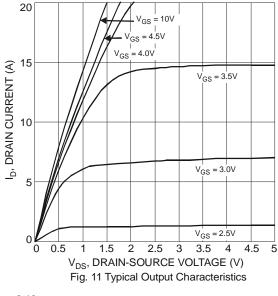
## Electrical Characteristics P-CHANNEL @TA = 25°C unless otherwise specified

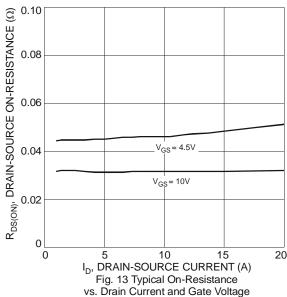
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)		·				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	-	-	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current T <sub>J</sub> = 25°C	I <sub>DSS</sub>	-	-	-1.0	μΑ	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)		_		_	_	
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1	-1.7	-2.2	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance			30	39	mΩ	$V_{GS} = -10V, I_D = -4.3A$
Static Dialif-Source Off-Resistance	R <sub>DS (ON)</sub>	-	42	53	111 2 2	$V_{GS} = -4.5V, I_D = -3.7A$
Forward Transfer Admittance	Y <sub>fs</sub>	-	7	-	S	$V_{DS} = -5V, I_{D} = -4.3A$
Diode Forward Voltage (Note 5)	$V_{SD}$	-	-0.75	-1.0	V	$V_{GS} = 0V, I_{S} = -1.7A$
DYNAMIC CHARACTERISTICS (Note 6)						•
Input Capacitance	C <sub>iss</sub>	-	1002	-	pF	151/1/
Output Capacitance	Coss	-	125	-	pF	$V_{DS} = -15V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	-	118	-	pF	1 = 1:0lvii iz
Gate Resistance	Rg	-	13	-	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
Total Gate Charge (4.5V)	Qg	-	10.1	-	nC	
Total Gate Charge (10V)	Qg	-	21.1	-	nC	$V_{GS} = -4.5V/-10V, V_{DS} = -15V,$
Gate-Source Charge	Q <sub>gs</sub>	-	2.8	-	nC	I <sub>D</sub> = -6A
Gate-Drain Charge	Q <sub>gd</sub>	-	3.2	-	nC	7
Turn-On Delay Time	t <sub>D(on)</sub>	_	10.1	-	ns	
Turn-On Rise Time	t <sub>r</sub>	-	6.5	-	ns	$V_{GS} = -10V, V_{DS} = -15V,$
Turn-Off Delay Time	t <sub>D(off)</sub>	-	50.1	-	ns	$R_G = 6\Omega$ , $I_D = -1A$
Turn-Off Fall Time	t <sub>f</sub>	-	22.2	-	ns	7

Notes: 5. Short duration pulse test used to minimize self-heating effect.

6. Guaranteed by design. Not subject to production testing.







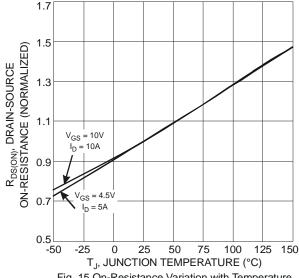
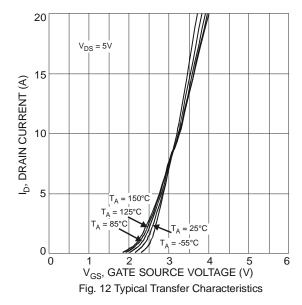


Fig. 15 On-Resistance Variation with Temperature



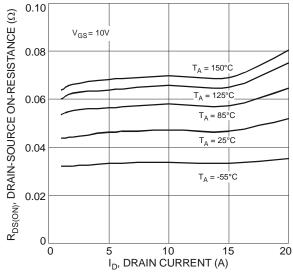


Fig. 14 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

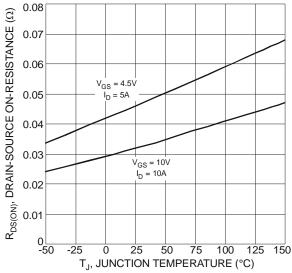


Fig. 16 On-Resistance Variation with Temperature



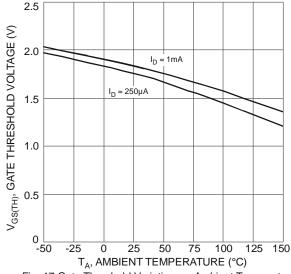
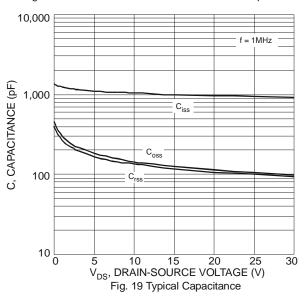
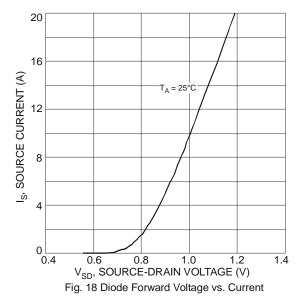
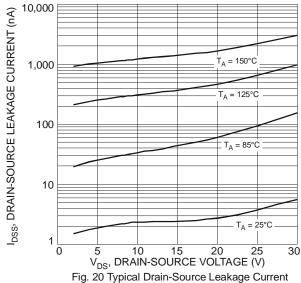


Fig. 17 Gate Threshold Variation vs. Ambient Temperature







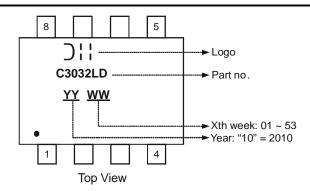
vs. Drain-Source Voltage

Ordering Information (Note 7)

Part Number	Case	Packaging
DMC3032LSD-13	SO-8	2500/Tape & Reel

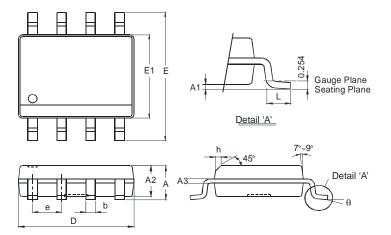
s: 7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# Marking Information



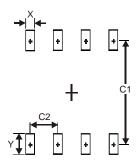


# **Package Outline Dimensions**



SO-8					
Dim	Min	Max			
Α	-	1.75			
A1	0.10	0.20			
A2	1.30	1.50			
A3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
Е	5.90	6.10			
E1	3.85	3.95			
е	1.27 Typ				
h	-	0.35			
٦	0.62	0.82			
θ	0°	8°			
All Dimensions in mm					

# **Suggested Pad Layout**



Dimensions	Value (in mm)
Х	0.60
Υ	1.55
C1	5.4
C2	1.27



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