Silicon N-Channel MOS FET

## HITACHI

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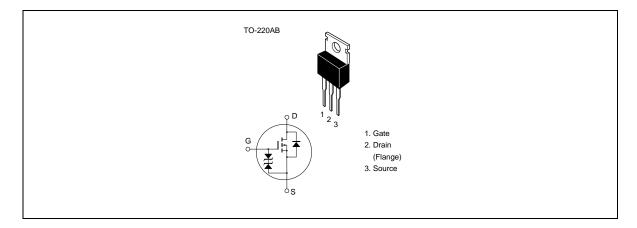
#### Application

High speed power switching

#### Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device can be driven from 5 V source
- Suitable for Switching regulator, DC-DC converter
- Avalanche ratings

#### Outline



### **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	60	V
Gate to source voltage	V <sub>gss</sub>	±20	V
Drain current	I <sub>D</sub>	15	А
Drain peak current	+1 D(pulse)	60	А
Body to drain diode reverse drain current	I <sub>DR</sub>	15	А
Avalanche current	* <sup>3</sup>	10	А
Avalanche energy	E <sub>AR</sub> * <sup>3</sup>	8.5	mJ
Channel dissipation	Pch* <sup>2</sup>	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1 \ \%$ 

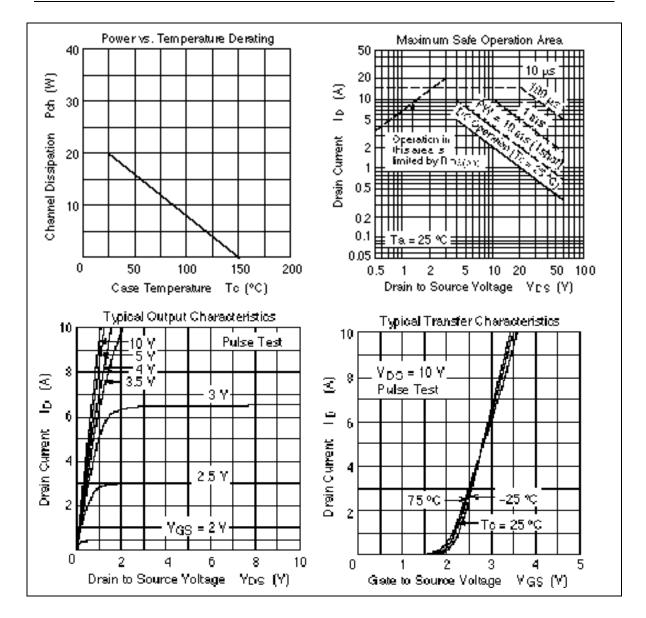
2. Value at Tc = 25 °C

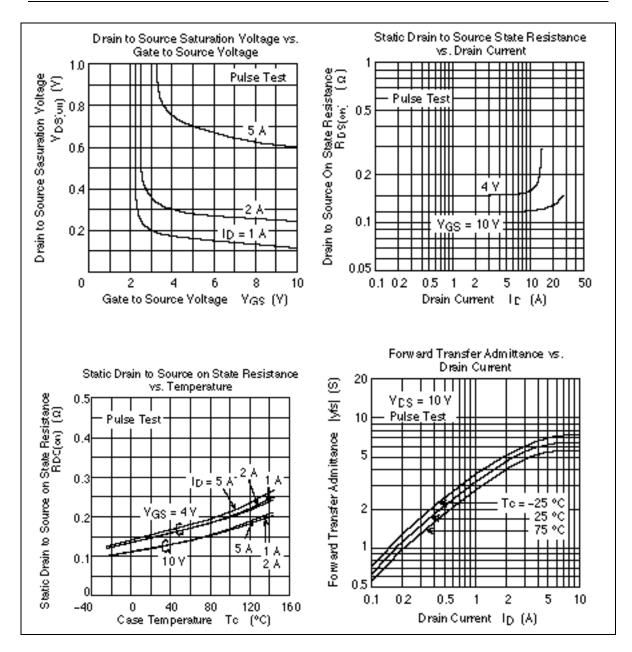
3. Value at Tch = 25 °C, Rg  $\ge$  50  $\Omega$ 

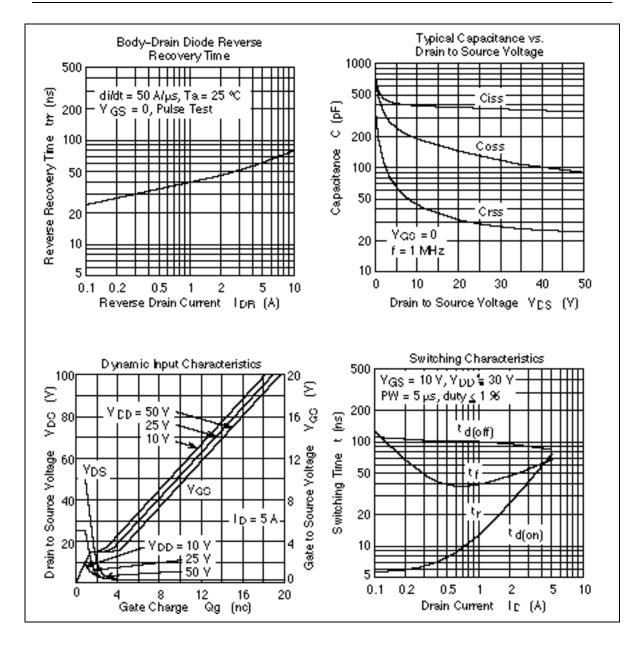
### **Electrical Characteristics** (Ta = 25°C)

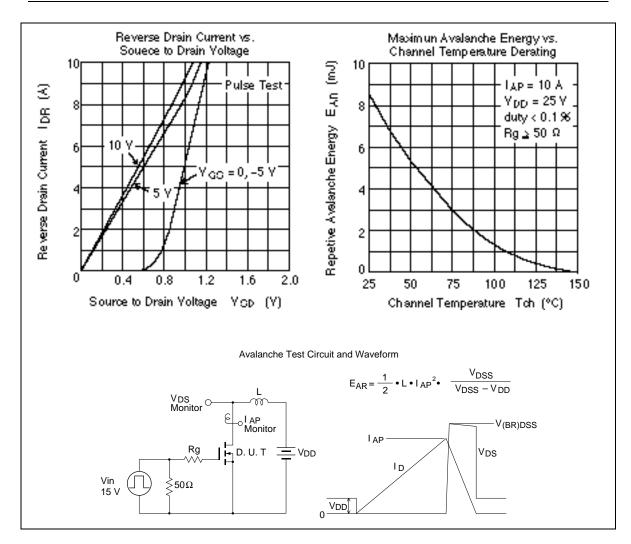
Item	Symbol	Min	Тур	Мах	Unit	Test conditions
Drain to source breakdown voltage	$V_{\scriptscriptstyle (BR)DSS}$	60	_	_	V	$I_{_{D}}$ = 10 mA, $V_{_{GS}}$ = 0
Gate to source breakdown voltage	$V_{\scriptscriptstyle (BR)GSS}$	±20	_	_	V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>		_	±10	μΑ	$V_{_{GS}} = \pm 16 \text{ V}, \text{ V}_{_{DS}} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>		_	250	μA	$V_{_{DS}} = 50 \text{ V}, \text{ V}_{_{GS}} = 0$
Gate to source cutoff voltage	$V_{\rm GS(off)}$	1.0		2.25	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{\rm DS(on)}$	—	0.10	0.13	Ω	$I_{_{ m D}} = 8 \text{ A}$ $V_{_{ m GS}} = 10 \text{ V}^{*1}$
		_	0.13	0.18	Ω	$I_{\rm D} = 8 \text{ A}$ $V_{\rm GS} = 4 \text{ V}^{*1}$
Forward transfer admittance	y <sub>fs</sub>	4.5	8	—	S	$I_{_{D}} = 8 \text{ A}$ $V_{_{DS}} = 10 \text{ V}^{*1}$
Input capacitance	Ciss	—	390	—	pF	$V_{DS} = 10 V$ $V_{GS} = 0$ f = 1 MHz
Output capacitance	Coss	_	190	_	pF	
Reverse transfer capacitance	Crss	_	45	_	pF	
Turn-on delay time	t <sub>d(on)</sub>		10	_	ns	$I_{_{D}} = 8 \text{ A}$ $V_{_{GS}} = 10 \text{ V}$ $R_{_{L}} = 3.75 \Omega$
Rise time	t <sub>r</sub>		65	_	ns	_
Turn-off delay time	t <sub>d(off)</sub>		90	—	ns	
Fall time	t <sub>r</sub>		90	—	ns	
Body to drain diode forward voltage	$V_{DF}$		1.3	—	V	$I_{_{\rm F}} = 15 \text{ A}, \text{ V}_{_{\rm GS}} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>		90		ns	$I_{_{\rm F}}$ = 15 A, $V_{_{\rm GS}}$ = 0, diF / dt = 50 A / µs
Note 1. Pulse Test						

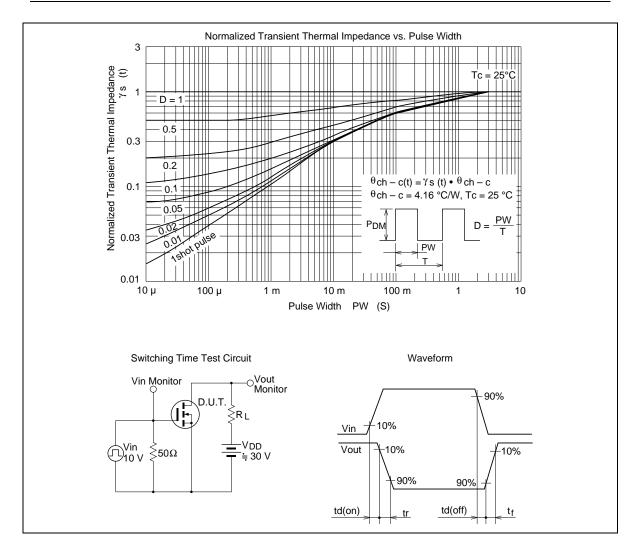
Note 1. Pulse Test











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