

T-47-13

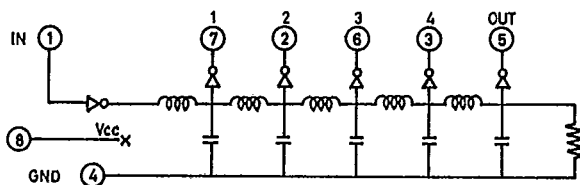
ACTIVE DIGITAL TAPPED DELAY LINES

DT05C-□□□ (5-TAP TYPE)

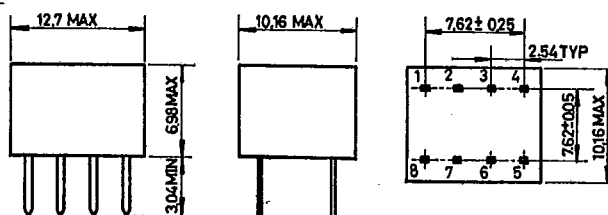
ELECTRICAL SPECIFICATIONS

Part No.	Delay Time T_d (ns)	Delay Per Tap (ns)	Rise Time T_r (ns) Max	TTL GATE IC
DT05C-025	25 ± 3	5 ± 2.5	5	74S04
DT05C-050	50 ± 4	10 ± 3	5	74S04
DT05C-075	75 ± 4	15 ± 3	5	74S04
DT05C-100	$100 \pm 5\%$	20 ± 3	5	74S04
DT05C-150	$150 \pm 5\%$	30 ± 4	5	74S04
DT05C-200	$200 \pm 5\%$	40 ± 4	5	74S04
DT05C-250	$250 \pm 5\%$	50 ± 5	5	74S04
DT05C-300	$300 \pm 5\%$	60 ± 6	5	74S04

PIN CONNECTIONS



MECHANICAL DIMENSIONS



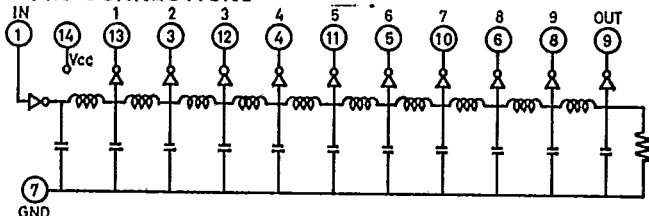
UNIT: mm

DT10A-□□□

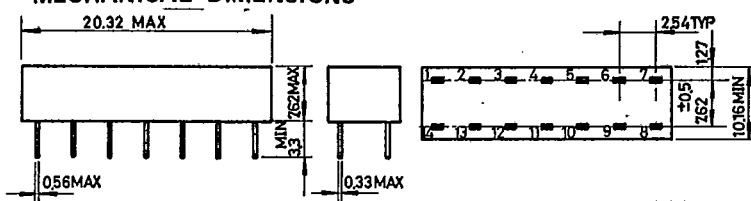
ELECTRICAL SPECIFICATIONS

Part No.	Delay Time T_d (ns)	Delay Per Tap (ns)	Rise Time T_r (ns) Max	TTL GATE IC
DT10A-50	50 ± 4	5 ± 2.5	5	74S04
DT10A-100	$100 \pm 5\%$	10 ± 3	5	74S04
DT10A-150	$150 \pm 5\%$	15 ± 3	5	74S04
DT10A-200	$200 \pm 5\%$	20 ± 3	5	74S04
DT10A-250	$250 \pm 5\%$	25 ± 4	5	74S04
DT10A-300	$300 \pm 5\%$	30 ± 4	5	74S04

PIN CONNECTIONS



MECHANICAL DIMENSIONS



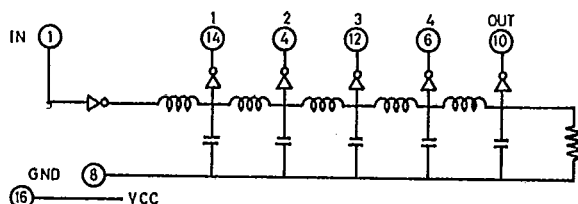
UNIT: mm

DT05D-□□□ (5-TAP TYPE)

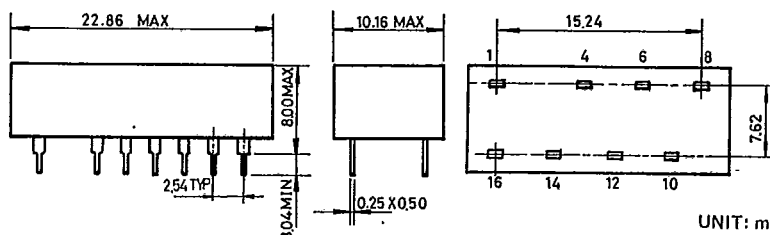
ELECTRICAL SPECIFICATIONS

Part No.	Delay Time T_d (ns)	Delay Per Tap (ns)	Rise Time T_r (ns) Max	TTL GATE IC
DT05D-025	25 ± 3	5 ± 2.5	5	74S04
DT05D-050	50 ± 4	10 ± 3	5	74S04
DT05D-075	75 ± 4	15 ± 3	5	74S04
DT05D-100	$100 \pm 5\%$	20 ± 3	5	74S04
DT05D-150	$150 \pm 5\%$	30 ± 4	5	74S04
DT05D-200	$200 \pm 5\%$	40 ± 4	5	74S04
DT05D-250	$250 \pm 5\%$	50 ± 5	5	74S04
DT05D-300	$300 \pm 5\%$	60 ± 6	5	74S04
DT05D-500	$500 \pm 5\%$	100 ± 10	5	74S04

PIN CONNECTIONS



MECHANICAL DIMENSIONS



UNIT: mm