

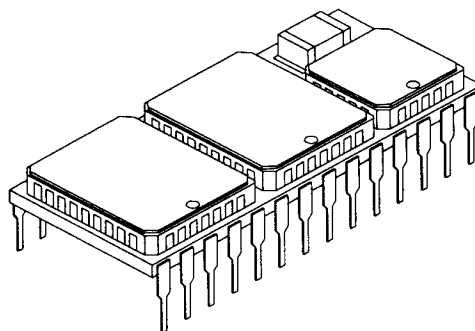
NOT RECOMENDED FOR NEW DESIGNS

DESCRIPTION:

The DPS41257 is a high-performance Static Random Access Memory (SRAM) module and is organized as a 32K X 8 memory.

The module is built with four low-power CMOS 8K X 8 SRAMs and a high speed CMOS 138 decoder. The decoder uses A13 and A14 to select between the four memories.

The DPS41257 is ideally suited for high performance applications where either fast access time or low power consumption is required.

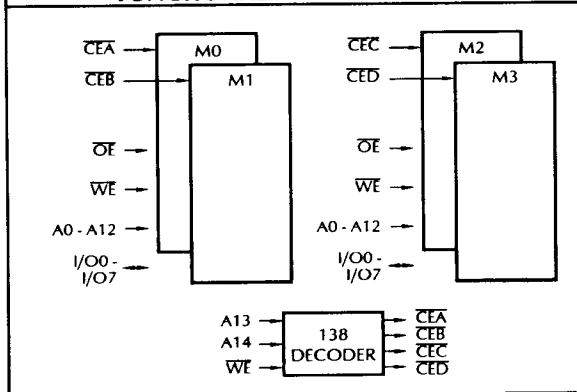


FEATURES:

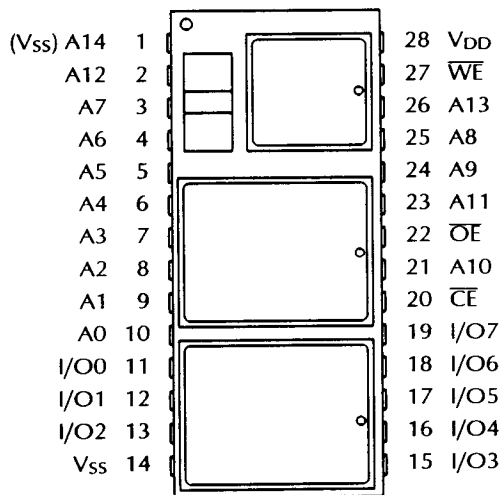
- 32K X 8 Organization
- Fast Read Access Times:
45, 55, 70, 100, 120, 150ns (max.)
- TTL-compatible Inputs and Outputs
- Common Data Inputs and Outputs
- Single +5V Operation ($\pm 10\%$ Tolerance)
- Available with All Semiconductor Components used in the Construction of the Module Compliant to MIL-STD-883; Class B
- 28-Pin, 0.600" Ceramic DIP Package, JEDEC Pin-out

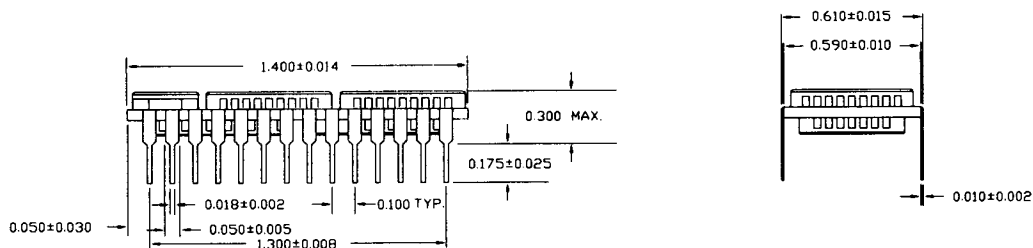
PIN NAMES	
A0 - A13	Address Inputs
I/O0 - I/O7	Data Input/Output
$\overline{CE}$	Chip Enable
$\overline{OE}$	Output Enable
$\overline{WE}$	Write Enable
VDD	Power (+5V)
Vss	Ground

FUNCTIONAL BLOCK DIAGRAM



PIN-OUT DIAGRAM



MECHANICAL DRAWING

- CONTACT DENSE-PAC SALES FOR MORE INFORMATION -

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