

TSGF SERIES**TSGF04****2nd TO 4th ORDER ANALOG FILTER ARRAY**

With the TSGF04 array, whose block diagram is given below, user is given 2 different pin-out configurations :

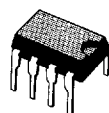
- 8 pin DIL only the filter up to 4th order is accessible.
- 14 pin DIL version where in addition, one uncommitted Op-amp and one internal oscillator capability are offered.

When the external driving of output sample-and-hold is used (CLKSH pin), PWF pin realizes the power adjustment of both uncommitted Op-amp and filter unit.

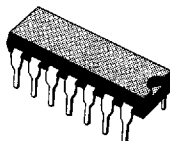
TSGF04 are also available in SO wide package version (0.3 inch) : 16 pin version only.

**TSGF04
BLOCK DIAGRAM**

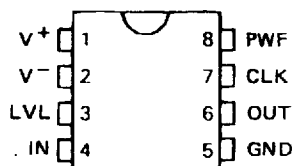
See figure 4 (E88TSGFSERIES-05)



**P
DIP-8**
(Plastic Package)

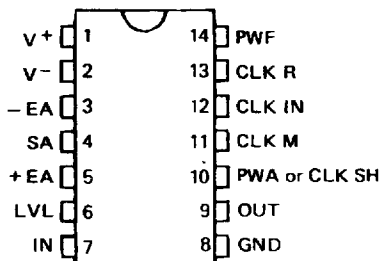


**P
DIP-14**
(Plastic Package)

PIN CONNECTIONS

E88TSGF04-01

8 pins : FILTER ONLY
Compatible with TSGF08



E88TSGF04-02

**14 pins : Filter
: + 1 Op - Amp**

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CLOCK OSCILLATOR

The TSGF04 base accepts external compatible TTL/CMOS clocks on CLKIN pin and provides an internal oscillator performed either by RC or crystal connected between CLKIN and CLKR pins.

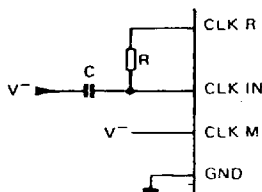
The clock selection mode is provided by CLKM pad which can be connected to V- or GND voltage levels. This connection is realized by two means, depending on the package type chosen :

- with 14-pin package, via CLKM pin.
- with 8-pin package, by internal connection readily performed, only on custom filters.

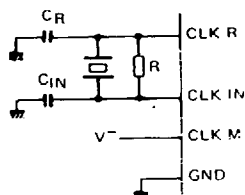
(Note that CLKM pin connected to V+, allows the selection of the internal crystal-controlled oscillator, but the selection by CLKM connected to V- is recommended).

The different possibilities are :

- two internal oscillator modes :
 - RC
 - Crystal

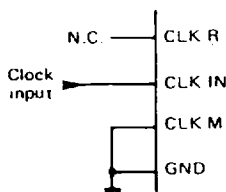


E88TSGF04-03



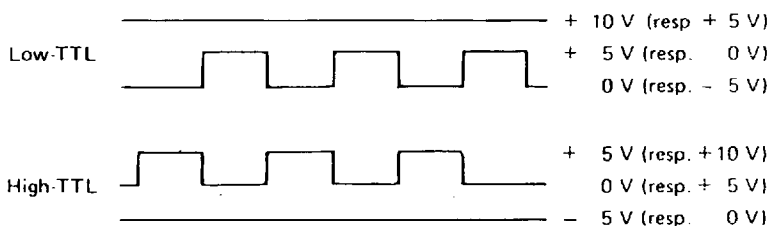
E88TSGF04-04

- three external clocks :
 - low-TTL
 - high-TTL
 - CMOS



E88TSGF04-05

The "low-TTL" and "high-TTL" clock levels are :



E88TSGF04-06

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For each package version, the following tables resume, the availability of the different clocks, in terms of the power supply.

Note that in 8-pin version, the clock mode (CLKM)

is internally set to GND voltage, except in the case of CMOS clock and 0-5V power supply, where CLKM is internally connected to V- voltage.

8-pin Package			
	0/5V	0/10V	- 5/+ 5V
Low-TTL	NO	C	C
High-TTL	NO	YES	YES
CMOS	C	YES	YES
RC Mode	NO	NO	NO
Crystal Mode	NO	NO	NO

14-pin Package			
	0/5V	0/10V	- 5/+ 5V
Low-TTL	NO	C	C
High-TTL	NO	CLKM=GND	CLKM=GND
CMOS	CLKM=V-	CLKM=GND	CLKM=GND
RC Mode	CLKM=V-	CLKM=V-	CLKM=V-
Crystal Mode	CLKM=V-	CLKM=V-	CLKM=V-

C - Customization option.

ELECTRICAL OPERATING CHARACTERISTICS :

WITH SINGLE SUPPLY VOLTAGE :

T_{amb} = 25°C, V₊ = 10V, V₋ = 0V, GND = 5V (unless otherwise specified)

CLKM	Parameter	Min.	Typ.	Max.	Unit
GND	Threshold Voltage External Clock Frequency		1.5	5	V MHz
V -	RC MODE : High Threshold Voltage on CLKIN Corresponding Voltage on CLKR	1	1.25 - 5	1.5	V V
	Low Threshold Voltage on CLKIN Corresponding Voltage on CLKR	1.5	- 1.25 + 5	- 1	V V
	Oscillator Frequency			5	MHz
	Resistor	2		10 000	kΩ
	Capacitor	0		47	nF
V -	CRYSTAL MODE : Oscillator Frequency			5	MHz
	Resistor		1		MΩ
	Capacitor C _R	10		100	pF
	Capacitor C _{IN}	10		30	pF

ELECTRICAL OPERATING CHARACTERISTICS (continued)

WITH DUAL SUPPLY VOLTAGE :

 $T_{amb} = 25^{\circ}\text{C}$, $V_{+} = 5\text{V}$, $V_{-} = -5\text{V}$, $\text{GND} = 0\text{V}$ (unless otherwise specified)

CLKM	Parameter	Min.	Typ.	Max.	Unit
GND	Threshold Voltage External Clock Frequency		6.5	5	V MHz
V -	RC MODE :				
	High Threshold Voltage on CLKIN Corresponding Voltage on CLKR	6	6.25 0	6.5	V V
	Low Threshold Voltage on CLKIN Corresponding Voltage on CLKR	3.5	3.75 + 10	4	V V
	Oscillator Frequency			5	MHz
	Resistor	2		10 000	k Ω
	Capacitor	0		47	nF
V -	CRYSTAL MODE :				
	Oscillator Frequency			5	MHz
	Resistor		1		M Ω
	Capacitor C_R	10		100	pF
	Capacitor C_{IN}	10		30	pF

WITH SINGLE SUPPLY VOLTAGE :

 $T_{amb} = 25^{\circ}\text{C}$, $V_{+} = 5\text{V}$, $V_{-} = 0\text{V}$, $\text{GND} = 2.5\text{V}$ (unless otherwise specified)

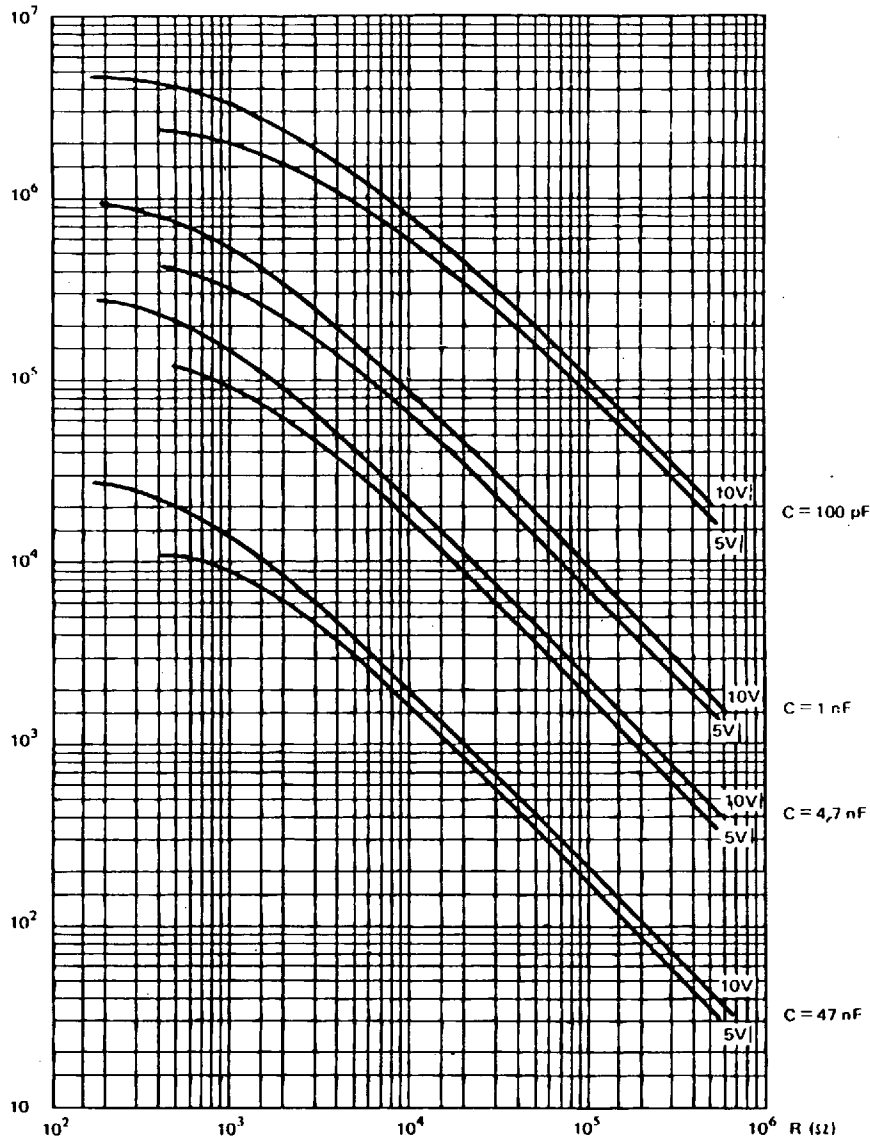
CLKM	Parameter	Min.	Typ.	Max.	Unit
GND	Threshold Voltage External Clock Frequency		3.8	5	V MHz
V -	RC MODE :				
	High Threshold Voltage on CLKIN Corresponding Voltage on CLKR	3	3.2 0	3.4	V V
	Low Threshold Voltage on CLKIN Corresponding Voltage on CLKR	1.5	1.8 + 5	2	V V
	Oscillator Frequency			5	MHz
	Resistor	2		10 000	k Ω
	Capacitor	0		47	nF
V -	CRYSTAL MODE :				
	Oscillator Frequency			5	MHz
	Resistor		1		M Ω
	Capacitor C_R	10		100	pF
	Capacitor C_{IN}	10		30	pF

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INVERTING TRIGGER FUNCTIONING FREQUENCY VARIATION AS FUNCTION OF R

With internal RC oscillator mode, the user's guide for R and C choice is given by following curves and for both supply voltages : 0.5V, 0.10V.

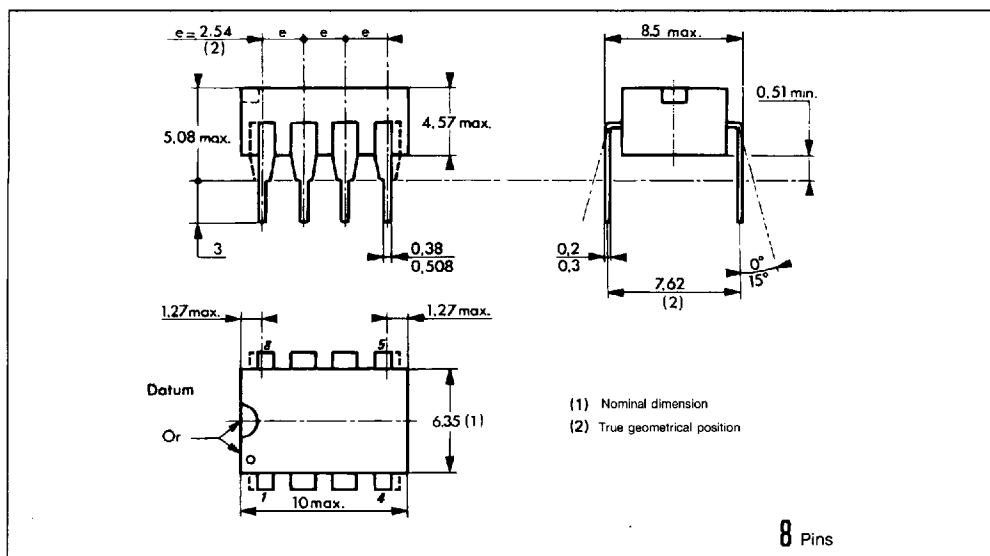
F (Hz)



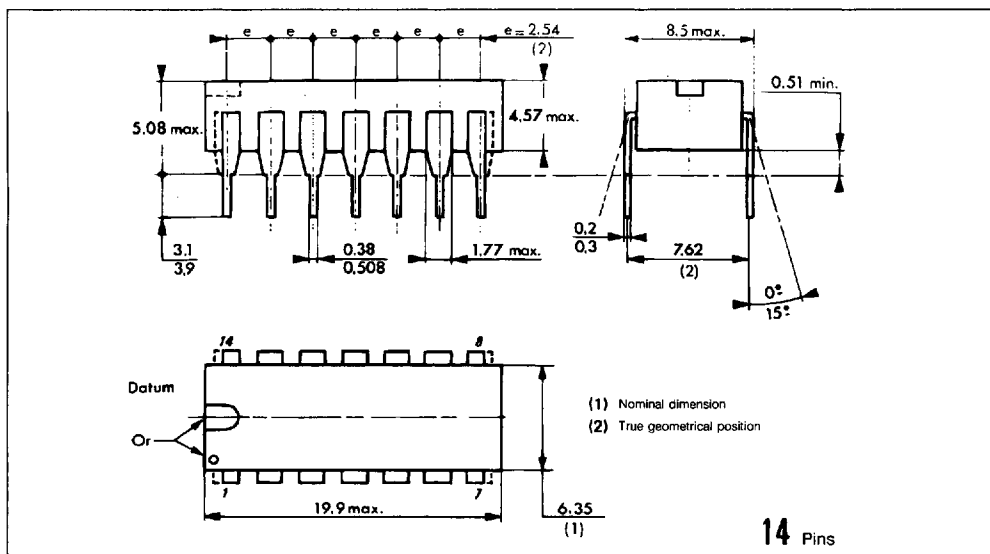
E88TSGF04-07

PACKAGE MECHANICAL DATA S G S-THOMSON

8 PINS - PLASTIC DIP



14 PINS - PLASTIC DIP

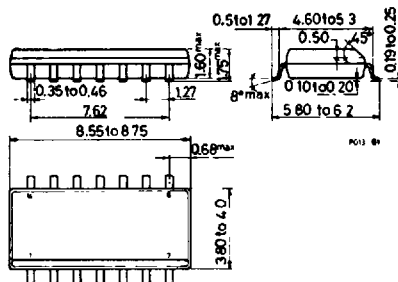
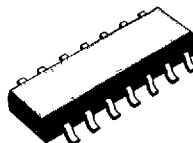
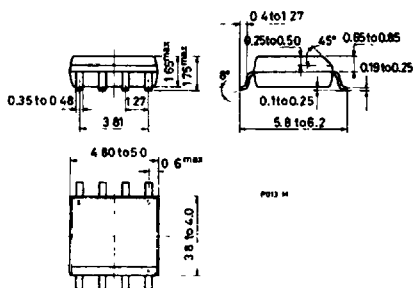
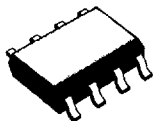
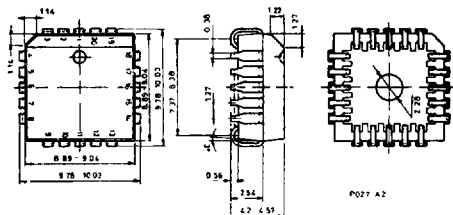
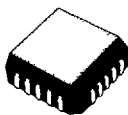
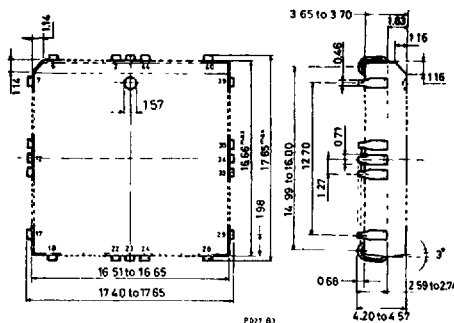
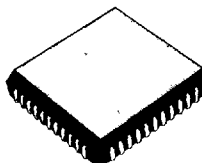


SO-8J

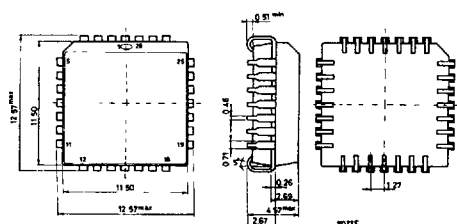
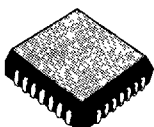
SO-14J

T-90-20

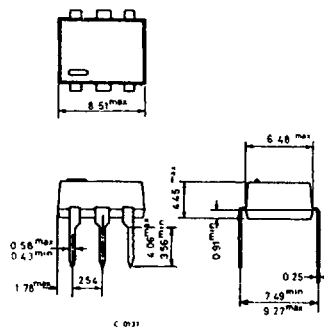
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**PLCC20****PLCC44**

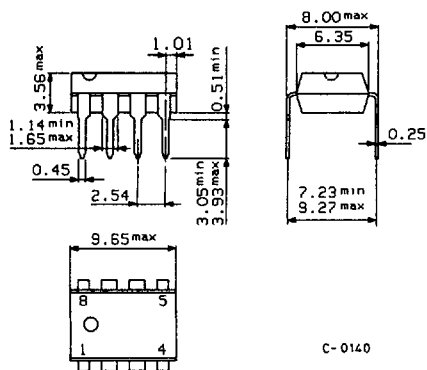
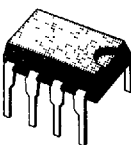
PLCC-28 Plastic Chip Carrier



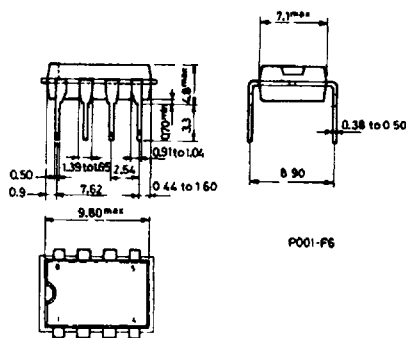
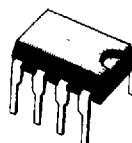
DIP-6



Minidip A Plastic

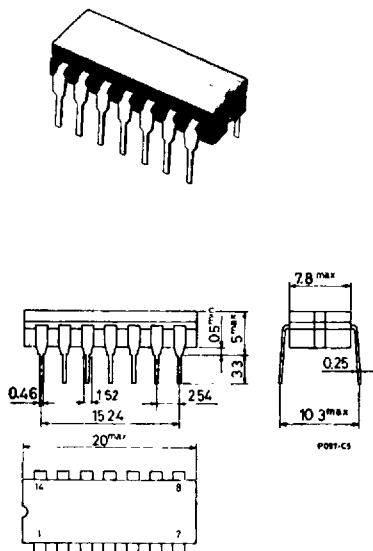


8 lead Plastic Minidip

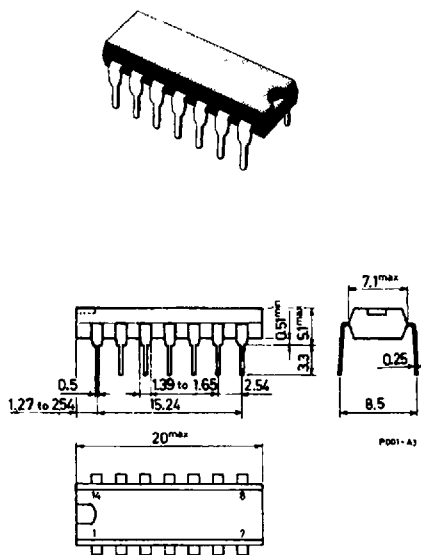


S G S-THOMSON

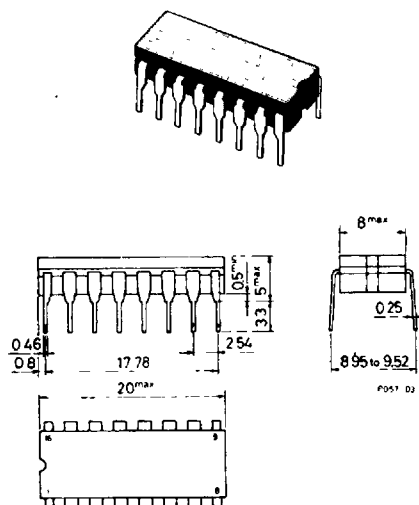
14 lead Ceramic Dip



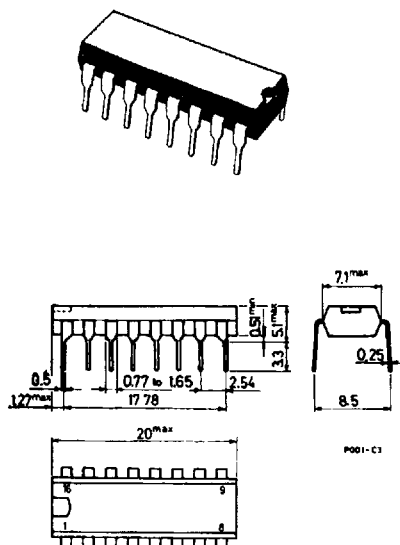
14 lead Plastic Dip



16 lead Ceramic Dip



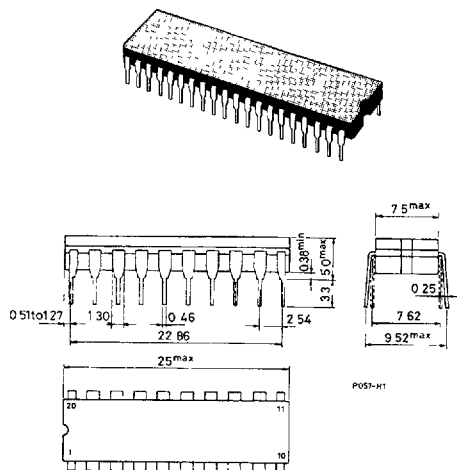
16 lead Plastic Dip (0.25)



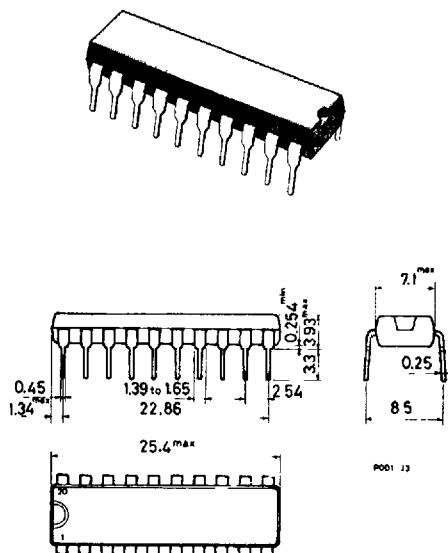
PACKAGES

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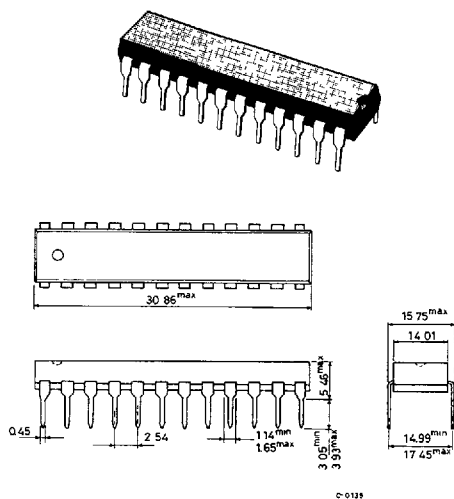
DIP-20 Ceramic



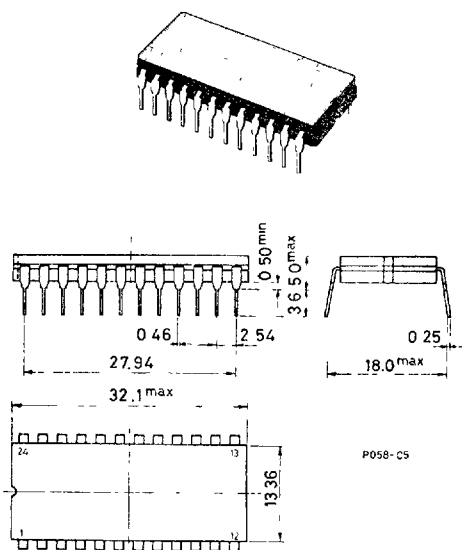
20 lead Plastic Dip (0.25)



DIP-24 Plastic

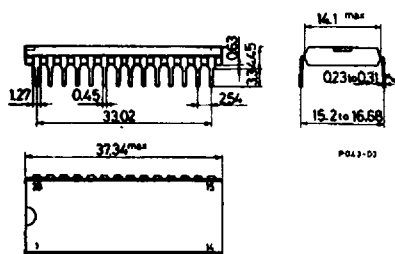
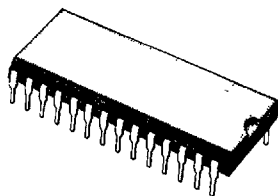


DIP-24 Ceramic (0.25)

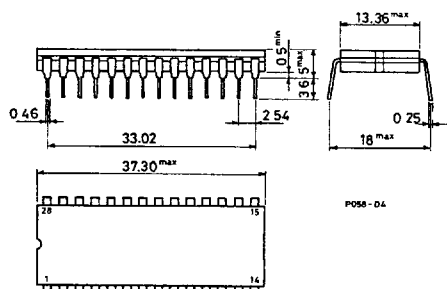
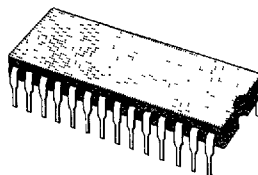


S G S-THOMSON

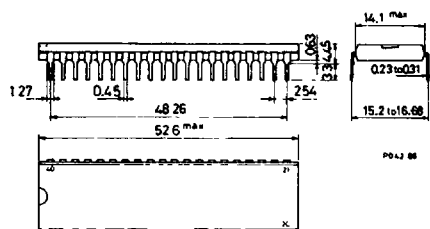
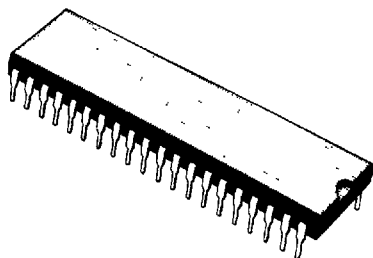
28 lead Plastic Dip



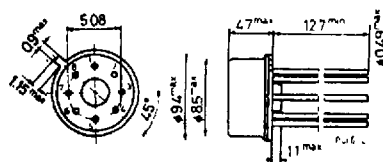
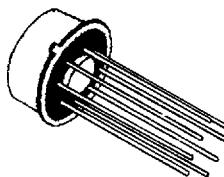
DIP-28 Ceramic (0.25)



40 lead Plastic Dip



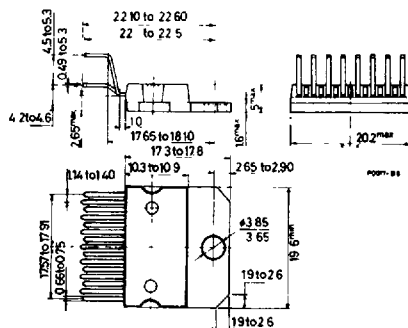
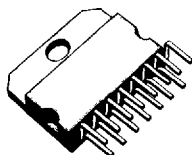
TO-99



PACKAGES

S G S-THOMSON

MULTIWATT-15



FLEXIWATT-15

