



Integrated Device Technology, Inc.

BiCMOS STATIC RAM 256K (32K x 8-BIT)

IDT71B256

FEATURES:

- 32K x 8 BiCMOS Static RAM
- High-speed address /chip select time
 - Military: 20ns
 - Commercial: 12/15/20ns
- One Chip Select plus one Output Enable pin
- Single 5V ($\pm 10\%$) power supply
- Input and output directly TTL-compatible
- Available in 28-pin sidebrazed ceramic, 300 mil DIP; 300 mil plastic DIP and 28-pin, 300 mil plastic SOJ packages

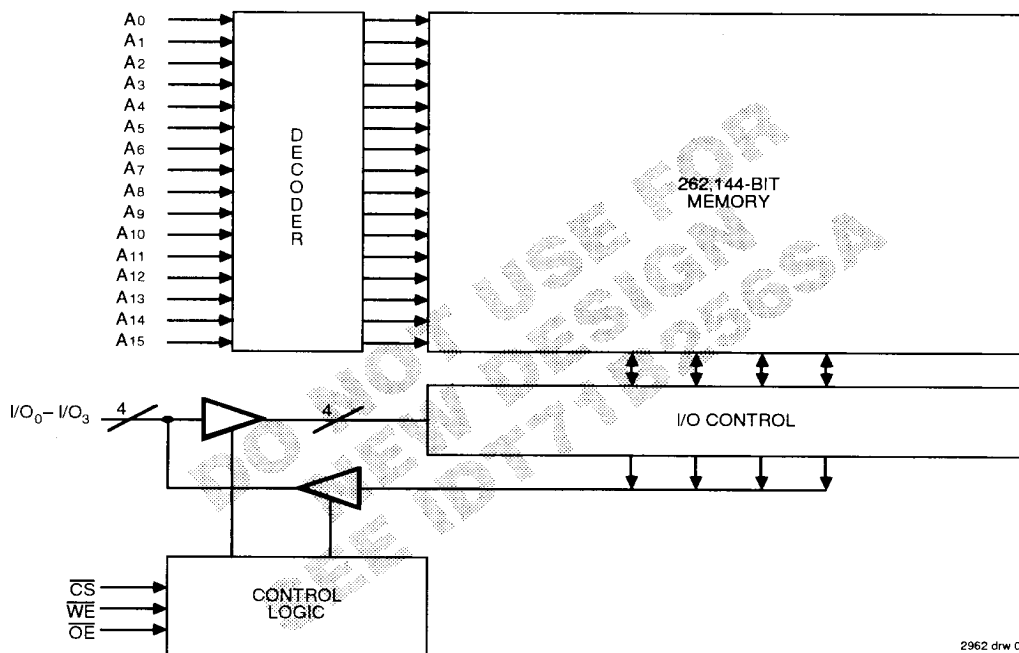
DESCRIPTION:

The IDT71B256 is a 262,144-bit high-speed static RAM organized as 32Kx8. It is fabricated using IDT's high-performance high-reliability BiCMOS technology. This state-of-the-art technology, combined with innovative circuit design techniques, provides a cost-effective solution for high-speed memory needs.

Address access times as fast as 12ns are available. All inputs and outputs of the IDT71B256 are TTL-compatible and operation is from a single 5V supply. Fully static asynchronous circuitry is used, requiring no clocks or refreshing for operation.

The IDT71B256 is packaged in a 28-pin, 300-mil sidebrazed 28-pin, 300 mil plastic DIP and 28-pin, 300-mil SOJ packages.

FUNCTIONAL BLOCK DIAGRAM



2962 drw 01

The IDT logo is a registered trademark of Integrated Device Technology, Inc.

MILITARY AND COMMERCIAL TEMPERATURE RANGES

SEPTEMBER 1992

©1992 Integrated Device Technology, Inc.

7.6 - I

DSC-1073/3

1