

KVR400X72RC3A/512D

512MB 64M x 72-Bit DDR400 CL3

Registered ECC 184-Pin DIMM

DESCRIPTION:

This document describes Kingston's 64M x 72-bit (512MB) DDR400 CL3 SDRAM (Synchronous DRAM) Registered ECC memory module. The components on this module include eighteen 32M x 8-bit (8M x 8-Bit x 4 bank) DDR400 SDRAM in TSOP packages and three 16-bit drivers in TSSOP packages. This 184-pin DIMM uses gold contact fingers and requires +2.6V. The electrical and mechanical specifications are as follows:

FEATURES:

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|---------------------------------|-------------------------------|
| ☑ Clock Cycle Time (tCK) | 5ns (min.) / 10ns (max.) |
| ☑ Row Cycle Time (tRC) | 55ns (min.) |
| ☑ Refresh Row Cycle Time (tRFC) | 70ns (min.) |
| ☑ Row Active Time (tRAS) | 40ns (min.) / 70,000ns (max.) |
| ☑ Single Power Supply of | +2.6V (+/- .1V) |
| ☑ Power (IDD0) | 5.668 W operating |
| ☑ UL Rating | 94 V - 0 |

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.200" (30.48mm).

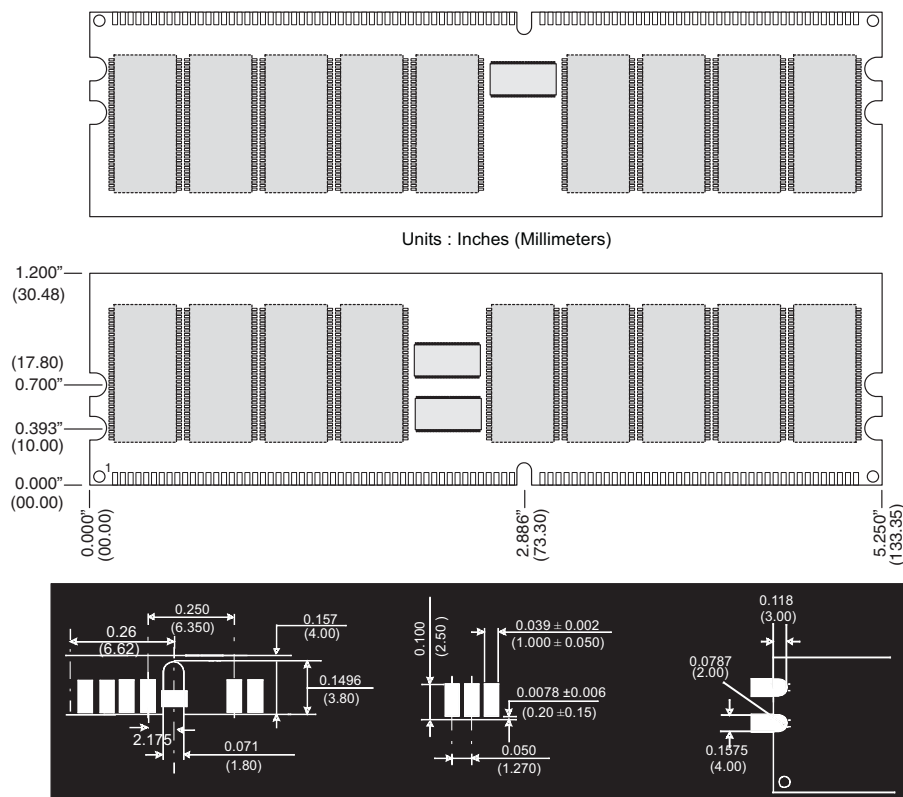


Figure 1: KVR400X72RC3A/512D 184-Pin DIMM