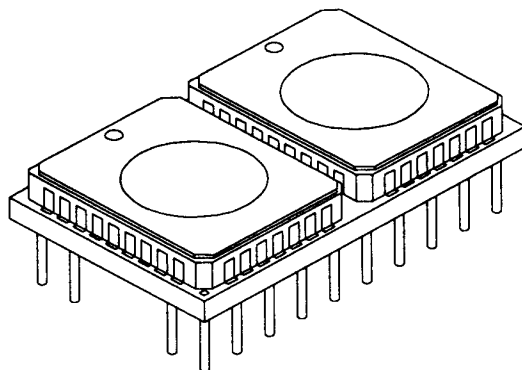


PRELIMINARY

DESCRIPTION:

The DPV64X16A is a 40-pin Pin Grid Array (PGA) consisting of two 64K X 8 UVEPROM devices in ceramic LCC packages surface mounted on a co-fired ceramic substrate with matched thermal coefficients. The LCCs are mounted in a pattern resulting in the smallest possible module outline.

The pins have been arranged around a central 0.3" gap which can accommodate a heat rail, if desired. In this central gap is a cavity containing two 0.1µf decoupling capacitors.

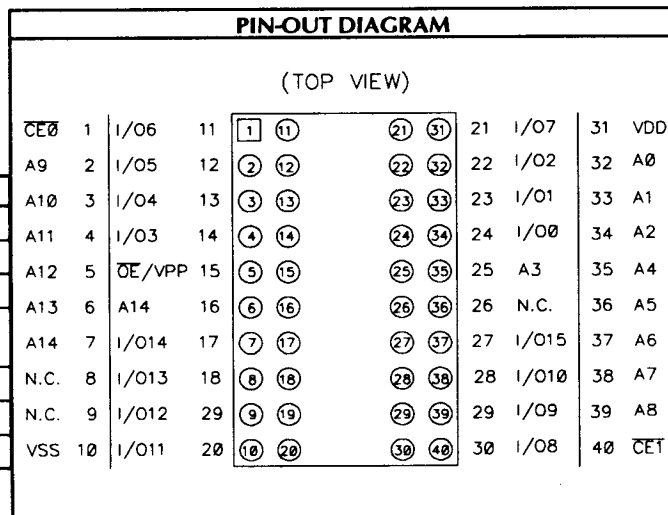
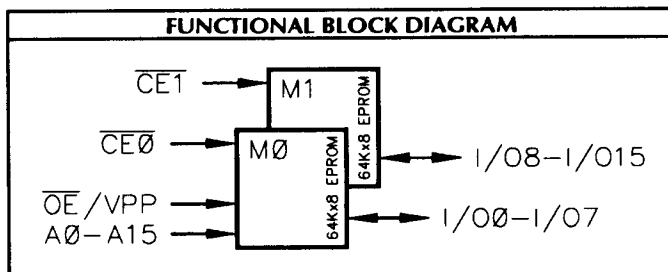


6

FEATURES:

- Organizations Available:
128K X 8 or 64K X 16
- Access Times:
55*, 70, 90, 120, 150, 170, 200, 250ns
- Fully Static Operation - No clock or refresh required
- Programming Voltage 12.5 Vdc
- Simple Programming Requirements
- Three-State Outputs
- High Speed Programming Algorithm (1.0ms Pulses Typ.)
- Common Data Inputs and Outputs
- TTL-compatible Inputs and Outputs
- 40-Pin PGA (Pin Grid Array) Package

* Commercial only.



PIN NAMES	
A0 - A15	Address Inputs
I/O0 - I/O15	Data In/Out
CE0, CE1	Chip Enables
OE / VPP	Output Enable / Programming Voltage
VDD	Power (+5V)
VSS	Ground
N.C.	No Connect

**FOR FURTHER INFORMATION
SEE CHAPTER 10
FOR COMPLETE DATA SHEET**