TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC83230-0011,JTC83230-0011S

TC83230-0011, JTC83230-0011S: Single-Chip CMOS LSI for Calculators with Printers (applicable printer heads: M-31 manufactured by EPSON)

The TC83230-0011, JTC83230-0011S LSI is a single-chip CMOS LSI for use in calculators with printers.

It integrates I/O logic circuits necessary to configure a calculator with 10-digit display, two-memory function, serial printer used to print calculation results, oscillator, and LCD drivers.

QFP80-P-1420-0.80A

Weight: 1.52 g (typ.)

Features

Operational Features

- Print: 11 digits of data. (including decimal point.)
 1 digit of minus sign, operational symbol.
 1-color printing (black).
- Display: 10 digits of data. (including punctuation in each digit.)
 - 1 digit of floating minus sign, memory load, error symbol, grand total memory load, 3 digits of commas.
- Decimal output: Decimal set lock key controls output format. Fixed decimal setting ("0", "1", "2", "3", "4", "6"), full floating decimal, and ADD mode.
- Key-input buffer: 12 words
- Operation methods: Addition and subtraction: By ARITHMETIC operation
 Multiplication and division: By algebraic operation
- Function: Four function, repeat multiplication and division, mixed calculation, square calculation,
 percentage calculation, percent discount and add-on calculation, memory calculation, delta
 percent calculation, add-mode calculation, mark-up/down calculation, total calculation, constant
 calculation, tax calculation

Two-key rollover

· Leading zero suppression

Protection

- (1) In the overflow condition, all key except "C", "C/CE", "CE", "Feed", " \rightarrow " key are inoperative.
- (2) Key chatter protection (at f = 4 MHz)

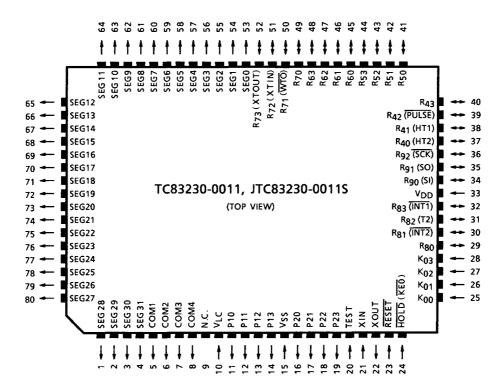
Auto-Clear at Power On

Auto-clear functions by connecting a capacitor to the RESET pin.



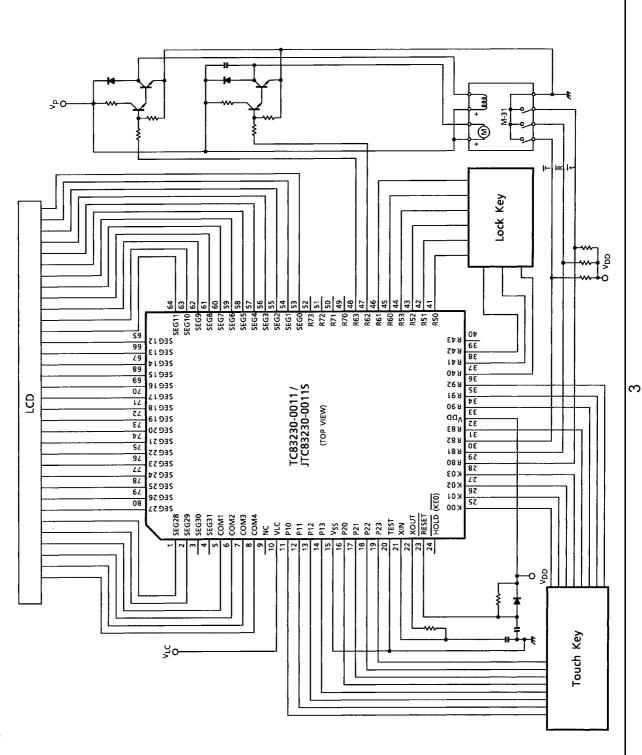
Pin Assignment (top view)

QFP80



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System Block Diagram

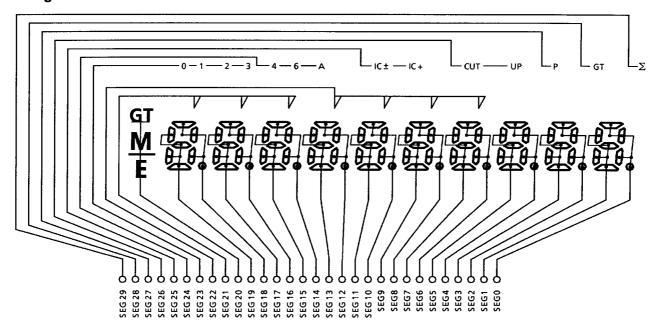


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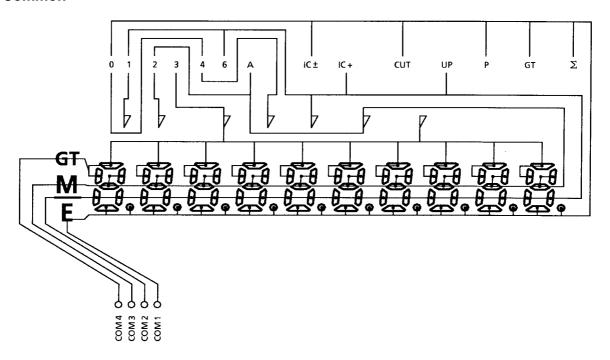


Connection of LCD

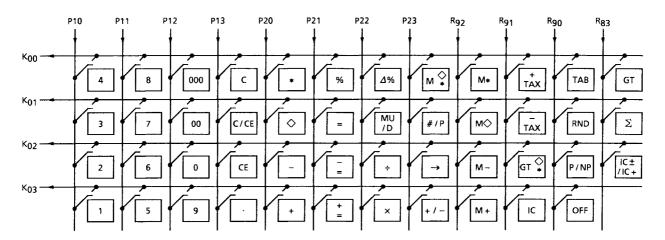
Segment



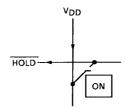
Common

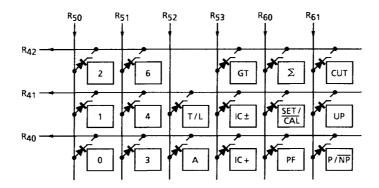


Key Connection



Touch Key





Lock Key



Specification of Calculator

Operation Specifications

(1) Operations depending on key types and modes

Key Name	CAL Mode	Tax Set Mode
Mode switch	[CAL] lock key is on	[SET] lock key is on.
С	Operates as clear key	Clears input data
CE	Operates as clear entry key	Clears input data
C/CE	Operates as clear or clear entry key	Clears input data
OFF	Operates as off key	Unused
Numeral	Numerals Key-inputs numerals	Inputs numerals
•	Key-inputs decimal points	Key-inputs decimal points
*,	Operates as total or sub-total key	Unused
+, - ×, ÷	Operates as four-function key	Unused
=	Operates as = key	Unused
GT [◊]	Operates as GT^\lozenge_* key	Unused
P/NP	Switches print or non-print	Unused
RND	Switches round-off and round-up	Unused
TAB	Switches decimal points	Unused
%	Operates as % key	Unused
Δ%	Operates as delta percentage calculation key	Unused
MU/D	Operates as mark-up/down key	Unused
IC	Operates as item count key	Unused
#/P	Operates as non-add-print key for left-justified printing	Unused
\rightarrow	Operates as right-shift key	Operates as right-shift key
+/-	Operates as sign change key	Unused
M+, M− M*, M◊ M*◊	Operates as memory function key	Unused
+TAX	Operates as +tax key	Unused
-TAX	Operates as -tax key	Unused
+ =	Operates as + key	Unused
- =	Operates as _ key	Unused
Σ	Operates as Σ key	Unused
IC±/IC+	Operates as IC±/IC+ key	Unused
GT	Switches GT-mode or non GT-mode	Unused
PF	Operates as paper feed key	Operates as paper feed key

(2)	Evn	lanation	of f	inction
(4)	EXP.	lanamon	OLI	unchor

If these key are pressed in multiplication/division mode or in constant calculation mode, add or subtract displays data to addition/subtraction registers, then display the result. At this time, in the operation mode multiplicand or divisor do not change.

These keys increment or decrement the item counter. In the following operation mode, the operations are executed, and the results are printed and displayed. At that time, addition or subtraction using the addition/subtraction register is not executed.

1) Percent discount/add-on calculation

$$a \times b\% + a + (ab/100)$$
 $c\% + a + (ac/100)$
 $a \times b\% - a - (ab/100)$
 $c\% - a - (ac/100)$

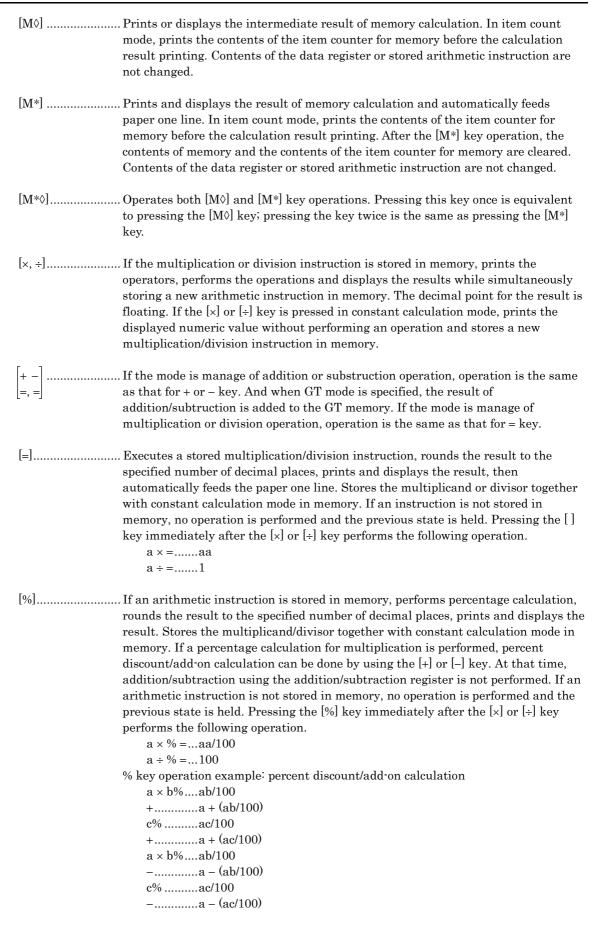
Percent discount/add-on with constants are calculated as above.

Contents of data register or stored arithmetic instruction are not changed.

After this key operation, the contents of the addition/subtraction register are cleared. The contents of the item counter are cleared at the first addition/subtraction in next step. The contents of the data register or stored arithmetic instruction are not changed. When GT mode is specified, the result of addition/subtraction is added to the GT memory.

[M+, M-] If the arithmetic instruction is not stored or if the mode is constant calculation mode, first prints the displays contents after rounding to the specified number of decimal places, performs addition/subtraction using the data in memory, then stores the result in memory. If the multiplication/division instruction is stored, executes the arithmetic instruction, rounds the result to the specified number of decimal places, prints and displays the result, adds/subtracts with the data in memory, then stores the result to memory.

At that time, the multiplicand or divisor is stored together with the mode, constant calculation mode. When this key is pressed immediately after the $[\times]$ or [M+, M-] key, operation is the same as that for the [=] key; that is, adds/subtracts using data in memory. This key operation increments or decrements the item counter for memory.



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[MU/D]...... If a multiplication/division instruction is stored in memory, cancels the data. The decimal point for the result is floating. MU/D key operation example: aMU/Db = a/(1 - (b/100)) - a(prints profit) a/(1 - (b/100))(mark-up) $c = \dots a/(1 - (c/100)) - a$ (prints profit) a/(1 - (c/100))(mark-up) aMU/Db +/- = a/(1 + (b/100)) - a(prints profit) a/(1 + (b/100))(mark-down) $c + /- = \dots a/(1 + (c/100)) - a$ (prints profit) a/(1 + (c/100))(mark-down) [\Delta\%] If a multiplication/division instruction is memorized, cancels the data. Δ %key operation example: $a\Delta\% \ b = \ b - a$ (b-a)/|a|(prints difference) c = c – a (change delta percent) (c-a)/|a|(prints difference) $a\Delta\% \ b +/- = -(b + a)$ (change delta percent) -(b + a)/|a|(prints difference) $c + /- = \dots -(c + a)$ (change delta percent) -(c + a)/|a|(prints difference) [+/-]......Inverts sign of the displayed number at key entry. estimation calculation error, cancels the error. GT memory, but does not change current state. If the key is pressed twice, calls the contents of GT memory and clears them. registers except the memory register, and prints 0.C. [CE] If pressed at key entry, clears only the contents of the display; does not change the stored arithmetic instruction or the contents of the data register. Invalid if pressed after one of the following keys: [C] [\times] [\div] [+] [-] [=] [%] [$\Delta\%$] [M+] [M-] [M \Diamond] [M *] $[M*\lozenge]$ [MU/D] [IC]. The result of pressing the [CE] key after the [#/P] key depends on the state before the keys were pressed. [IC+]..... Selects item count mode. [IC±] IC+.....Counts up by the [+] or [-] key. IC±.....Counts up by the [+] key, down by the [-] key. mode, adds the operation result to the addition/subtraction register and increments the item counter. [GT] In grand total mode, adds the total register to the GT register by the [*] key. [C/CE]...... If pressed at key entry, operates same as the [CE] key. If pressed after one of the following keys, operates same as the [C] key: [C/CE] [x] $[\div]$ [+] [-] [=] [%] $[\Delta\%]$ [M+] [M-] $[M\lozenge]$ [M*] $[M*\lozenge]$ [MU/D] [IC]. The result of pressing the [C/CE] key after the [+/-] or the [#/P] key depends on the state before the keys were pressed.

(3)

[#/P]	register together with the # symbol, b key is pressed after a key except the r	ory, prints the contents of the key entry data ut does not change the current state. If the numerical keys or [+/-] key, does not change rent state. If the key is pressed in clock yed date and time.
TAX+ TAX-	does not express the tax. +TAX, -TAX key operation example: a +TAXa (3/100) a + (a (3/100)) a -TAXa - a /(1 + 3/100) a/(1 + 3/100)	(excluded TAX) xey entry, calculate the tax as a result of
[P/NP]	set. Switches mode in each time when	PRINT mode. At reset, NON-PRINT mode is the [P/NP] key is pressed: displays "print mode". Valid only when the
[RND]	Switches the mode in each time when	f and half-adjust. At reset, half-adjust is set. the [RND] key is pressed: $5/4 \rightarrow \downarrow \rightarrow \uparrow \rightarrow 5/4$ f. Valid only when the [T/ \overline{L}] lock key is set to
[TAB]	each time when the [TAB] key is pres	floating point (F) is set. Switches the mode in sed as follows: $F \rightarrow 0 \rightarrow 1 \rightarrow 2 \rightarrow 3 \rightarrow 4 \rightarrow 6$ field decimal point or add mode. Valid only
Explanation of lo [0, 1, 2, 3][4, 6, A]	Sets the specified decimal point. If no When processing floating point data, When A mode is specified, key-entered key-entered numerical value is used f	the operation result is zero-shifted. d data are multiplied by 1/100 only when the or addition/subtraction or memory pressed during data entry, A mode is invalid.
[CUT, UP]	half-adjusts. When a decimal point is decimal place is (are) half-adjusted, re	n UP mode; when no specification is made, specified, the digit (s) in the subsequent bunded-off, or rounded-up (??). If floating st significant digits which cannot be displayed
[P/NP]	Switches between print and non print all printing except [PF] or [#/P] key. When mode changes from non-print to	mode. When $[P/\overline{NP}]$ lock key is off, disables print, feeds the paper one line.
[IC+][IC±]	Selects item count mode. IC+ Counts up by the [+] or IC± Counts up by the [+] key	
[Σ]	If an operation is performed by the [= mode, adds the operation result to the increments the item counter.	or [%] key in auto accumulation calculation e addition/subtraction register and
[GT]	In grand total mode, adds the total re	gister to the GT register by the [*] key.

	[T/L]	When the $[T/L]$ lock key is on, the $[P/NP]$, $[RND]$, and $[TAB]$ keys are valid. When the $[T/\overline{L}]$ key is off, the $[NP]$, $[CUT]$, $[UP]$, and $[0, 1, 2, 3, 4, 6, A]$ lock keys are valid.
	[SET/CAL]	When the (SET/ \overline{CAL}) lock key is on, prints and express the stored tax rate. When the (SET/ \overline{CAL}) lock key is off, store the expression data to the new tax rate. The result of tax rate is only floating-point, and not concent the decimal-point at this function.
	[PF]	Feed paper.
(4)	ON, OFF key [ON]	If pressed in HOLD mode, cancels HOLD. At that time, cancels all arithmetic instructions and errors. The contents of the memory register and the TAX RATE before HOLD mode are retained; all other registers are cleared. While the [ON] key is pressed, the [OFF] key is invalid.
	[OFF]	Forcibly enters HOLD mode (CPU sleep mode).



Operation Example

			Ke	y			5	5
TAB	4/5	IC	Σ	GT	MOD	Touch	Print	Display
F	4/5	OFF	OFF	OFF	CAL	POWER ON		
							<pf></pf>	
							C	
							<pf></pf>	0.
						1+	1. +	1.
						2-	2	-1.
						◊	-1. ◊	-1.
						*	-1. *	
							<pf></pf>	-1.
						IC	0.	0.
		IC+				1+	1. +	1.
						2-	2	-1.
						♦	002	
							-1. ◊	-1.
						*	002	
							-1. *	
							<pf></pf>	-1.
						IC	0.	0.
		OFF				3×	3. ×	3.
						4÷	4. ÷	12.
						=	4. =	
							3. *	
							<pf></pf>	3.
						5×	5. ×	5.
						6%	6. %	
							0.3 *	
							<pf></pf>	0.3
						+	+	
							5.3 %	
							<pf></pf>	5.3
						2÷	2. ÷	2.
						3%	3. %	
							66.66666666 *	
							<pf></pf>	66.6666666
						2 MU/D	2. M	2.
						3=	3. %	
							=	
							0.06185567 *	
							2.06185567 *	
							<pf></pf>	2.06185567
						2∆%	2	2.

Note 1: <PF>Paper feed

6.	D: (Key				
Display	Print	Touch	MOD	GT	Σ	IC	4/5	TAB
	3. %	3=						
	=							
	1. *							
	50. *							
50.	<pf></pf>							
3.	3. ×	3×	CAL	OFF	Σ	OFF	4/5	F
12.	4. ÷	4÷						
	4. =	=						
	3. +							
3.	<pf></pf>							
5.	5. ×	5×						
	6. %	6%						
	0.3 +							
0.3	<pf></pf>							
	+	+						
	5.3 %							
5.3	<pf></pf>							
2.	2. ÷	2÷						
	3. %	3%						
	66.66666666 +							
66.6666666	<pf></pf>							
2.	2. M	2 MU/D						
	3. %	3=						
	=							
	0.06185567 *							
	2.06185567 +							
2.06185567	<pf></pf>							
2.	2	2∆%						
	3. %	3=						
	=							
	1. *							
	50. +							
50.	<pf></pf>							
	122.0285223 *	*						
122.0285223	<pf></pf>							
2.	2. +	2+		GT				
5.	3. +	3+						
	Т	*						
	5. +							
5.	<pf></pf>							
-3.	3	3-						
-7.	4	4-						

			Ke	у			Deliet	District
TAB	4/5	IC	Σ	GT	MOD	Touch	Print	Display
						5-	5	-12.
						*	T	
							-12 +	
							<pf></pf>	-12.
						GT	Т	
							-7. ◊	-7.
						GT	T	
							-7. *	
							<pf></pf>	-7.
				OFF		M+	М	
							-7. +	м -7.
F	4/5	OFF	Σ	OFF	CAL			
						ΜÔ	М	
							-7. ◊	м -7.
						M*	М	
							-7. *	
							<pf></pf>	-7.
						#/P	-7. ◊	-7.
						2 #/P	#2	2.
						#/P	2. ◊	2.
						0÷	0. ÷	0.
						=	0. =	
							0. *	
							<pf></pf>	E 0.
						С	0. C	
							<pf></pf>	0.
	CUT		OFF		SET		0. %	
							<pf></pf>	0.
						3		3.
					CAL		3. %	
							<pf></pf>	0.
						С	0. C	
							<pf></pf>	0.
					SET		3. %	
							<pf></pf>	3.
					CAL			0.
						1560		1,560.
						+TAX	1560.	
							용	
							46.8 ◊	
							1606.8 *	

5: 1	D: 1			/	Key			
Display	Print	Touch	MOD	GT	Σ	IC	4/5	TAB
1,606.8	<pf></pf>							
	1606.8 ◊	+TAX						
	8							
	48.204 ◊							
	1655.004 *							
1,655.004	<pf></pf>							
1,560.		1560						
1,560.	1560. ×	×						
78,900.		78900						
	78900. =	+TAX						
	123084000. ◊							
	96							
	3692520. ◊							
	126776520. *							
126,776,520.	<pf></pf>							
126,776,520.		=						
5.	-	5						
5. 5.	5. ×	X	CAT	OPP	OFF	OFF	CIIM	
5.	5. =	+TAX =	CAL	OFF	OFF	OFF	CUT	F
	25. *	_						
25.	<pf></pf>							
25.	25. ◊	+TAX						
	23. V %	TIAA						
	0.75 ♦							
	25.75 *							
25.75	<pf></pf>							
25.75	1117	=						
	0. C	С						
0.	<pf></pf>							
1,560.		1560						2
1,560.00	1560.00 +	+						
1,100.		1100						
2,660.00	1100.00 +	+						
	2660.00 ◊	+TAX						
	%							
	79.80 ◊							
	2739.80 *							
2,739.80	<pf></pf>							
	2739.80 ◊	+TAX						F
	8							
	82.194 ◊							
	2821.994 *							
2,821.994	<pf></pf>							

D:	Delet			у	Ke			
Dis	Print	Touch	MOD	GT	Σ	IC	4/5	TAB
		98000000						
9,800		00						
0.	9800000000.	+TAX						
%								
0. ♦	294000000.							
00 *	1,009400000							
E 1,	<pf></pf>							
0. C	0.	С						
	<pf></pf>							
		1560						
		+/-						
0.	-1560.	+TAX						
8								
.8 ◊	-46.8							
.8 *	-1606.8							
	<pf></pf>							
		1560						
	1560.	-TAX						
용								
	-45,436894							
	1514.563106							
1,5	<pf></pf>							
	1514.563106	-TAX	CAL	OFF	OFF	OFF	CUT	F
8	44 11240055							
	-44.11348855							
1,4	1470.449618 <pf></pf>							
3. %			C E/III					
3. 6	<pf></pf>		SET					
	\r_r/	С						
ગ. ક	0		CAL					
	<pf></pf>		CILL					
ગ. %			SET					
	<pf></pf>							
		1234						
4. %	1234.		CAL					
	<pf></pf>							
		98000000						
9,800		00						
	9800000000.	+TAX						
o. *	0.							
E	<pf></pf>							
o. c	0.	С						
	<pf></pf>							



Maximum Ratings $(V_{SS} = 0 V)$

Characteristics	Symbol	Rating	Unit
Supply voltage 1	V_{DD}	-0.3~6	V
Supply voltage (LCD drive)	V _{LC}	-0.3~V _{DD} + 0.3	V
Input voltage	V _{IN}	-0.3~V _{DD} + 0.3	V
Output voltage	V _{OUT}	-0.3~V _{DD} + 0.3	V
Output current	I _{OUT}	3.2	mA
Power dissipation	P_{D}	600	mW
Soldering temperature	T _{sld}	260 (10 s)	°C
Storage temperature	T _{stg}	−55~125	°C
Operating temperature	T _{opr}	0~40	°C

Electrical Characteristics

Recommended Operating Conditions ($V_{SS} = 0 \text{ V}, T_{opr} = 0 \sim 40^{\circ}\text{C}$)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Max	Unit
Operating temperature	T _{opr}	_	_	0	40	°C
		_	NORMAL	4.5		
Supply voltage	V_{DD}	_	SLOW	7.0	5.5	V
			HOLD	2.0		
High-level input voltage (non-schmitt circuit)	V _{IH1}		N >45V	V _{DD} × 0.7	V_{DD}	V
High-level input voltage (schmitt circuit)	V _{IH2}		V _{DD} ≥ 4.5 V	V _{DD} × 0.75	V _{DD}	V
High-level input voltage	V _{IH3}	_	V _{DD} < 4.5 V	V _{DD} × 0.9	V _{DD}	V
Low-level input voltage (non-schmitt circuit)	V _{IL1}		V _{DD} ≧ 4.5 V	0	V _{DD} × 0.3	V
Low-level input voltage (schmitt circuit)	V _{IL2}		י פוט א = ד.ס ע = ד.ס ע	0	V _{DD} × 0.25	V
Low-level input voltage	V _{IL3}	_	V _{DD} < 4.5 V	0	V _{DD} × 0.1	V



DC Characteristics (VSS = 0 V, $T_{opr} = 0~40$ °C)

Characteristics	Symbol	Test Circuit	Terminal	Test Condition	Min	Тур.	Max	Unit
Hysteresis voltage (schmitt circuit)	V _{HS}	_	Hysteresis Input	_	_	0.7	_	V
Input current	I _{IN1}	_	KO port, TEST, RESET, HOLD	V _{DD} = 5.5 V			±2	μА
input current	I _{IN2}	_	Open Drain R port, P port	V _{IN} = 5.5/0 V	_	_	± 2	μΑ
Input resistance	R _{IN1}	_	KO port TEST with Input Resistor	V _{DD} = 5.5 V	30	70	150	kΩ
	R _{IN2}	_	RESET, HOLD	V _{IN} = 5.5/0 V	100	220	450	
Output leakage current	I _{LO1}	_	Sink Open Drain R port	V _{DD} = 5.5 V V _{OUT} = 5.5 V	_	_	2	μА
Output leakage current	I _{LO2}		Source Open Drain R port, P port	$V_{DD} = 5.5 V$ $V_{OUT} = -1.5 V$	_		-2	μΑ
High-level output voltage	V _{OH}	_	Source Open Drain R port, P port	$V_{DD} = 5.5 \text{ V}$ $I_{OH} = -1.6 \text{ mA}$	2.4	_	_	٧
Low-level output voltage	V _{OL}	_	Sink Open Drain R port	$V_{DD} = 5.5 \text{ V}$ $I_{OL} = 1.6 \text{ mA}$	_		0.4	٧
Pull-down resistance	R _{OUT}	_	R port, P port	V _{DD} = 5.5 V V _{IN} = 5.5 V	30	70	150	kΩ
Output resistance	Ros		SEG				35	kΩ
Output resistance	Roc	—	СОМ	V _{DD} = 5 V			3	K22
	V _{O2/3}			$V_{DD} - V_{LC} = 3 V$	3.8	4.0	4.2	
Output voltage	V _{O1/2}	_	SEG/COM	1 ADD - AFC - 2 A	3.3	3.5	3.7	V
	V _{O1/3}				2.8	3.0	3.2	
Supply current (normal)	I _{DD}		_	$V_{DD} = 5.5 \text{ V},$ $V_{LC} = V_{SS}$ $f_{c} = 4 \text{ MHz}$	_	3	6	mA
Supply current (hold)	I _{DDH}	_	_	V _{DD} = 5.5 V	_	0.5	10	μА

Note 2: Typ. values are guaranteed at $T_{opr} = 25^{\circ}C$, $V_{DD} = 5~V$.

Note 3: I_{IN1}: Excepts a current through a internal pull up/down resistor.

Note 4: ROS, ROC: Shows on-resistor at level switching.

Note 5: V_{O2/3}: Shows 2/3 level output voltage at which 1/4 or 1/3 duty LCD drive.

Note 6: $V_{O1/2}$: Shows 1/2 level output voltage at which 1/2 duty or static LCD drive.

Note 7: V_{O1/3}: Shows 1/3 level output voltage at which 1/4 or 1/3 duty LCD drive.

Note 8: I_{DD} , I_{DDH} : Current consumption at $V_{IN} = 5.3 \text{ V}/0.2 \text{ V}$

Should be under that KO port is open and R port voltage level is valid.



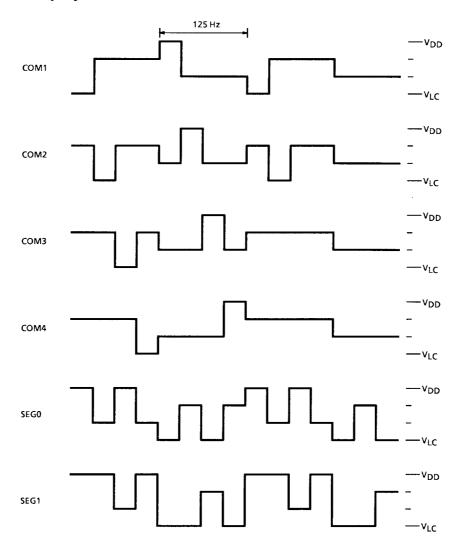
Oscillation Circuit (VSS = 0 V, V_{DD} = 4.5~5.5 V, T_{opr} = 0~40°C)

Recommended Circuit	Test Condition	Min	Тур.	Max	Unit
XIN XOUT	$V_{DD} = 5.0 \text{ V}$ $C = 100 \text{ pF}$ $R = 1 \text{ k}\Omega \pm 2\%$	2.4	4.0	5.6	MHz

AC Characteristics ($V_{SS} = 0 \text{ V}, V_{DD} = 4.5 \sim 6.0 \text{ V}, T_{opr} = 0 \sim 40 ^{\circ}\text{C}$)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Instruction cycle time	t _{CY}		NORMAL	1.9		20	μS
			SLOW	235		267	μο
High-level clock pulse width	twch		External Clock Operation	80	_	_	ns
Low-level clock pulse width	twcL		External Clock Operation				ns
Shift data hold time	tsdh	_		0.5 tcy - 300			ns
High speed timer/counter input frequency	fHT	_	ı	_		f _c	MHz

Waveforms for Display



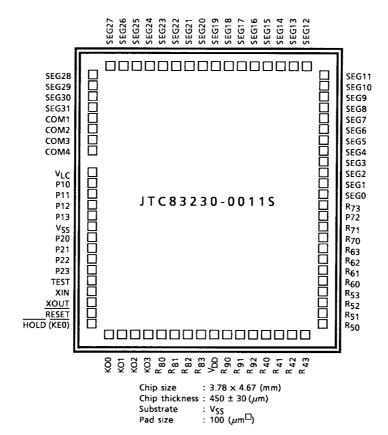
Pad Location Table

(μ**m**)

Name	X Point	Y Point
KO0	-1282	-2074
KO1	-1122	-2074
KO2	-962	-2074
KO3	-802	-2074
R ₈₀	-641	-2074
R ₈₁	-438	-2074
R ₈₂	-278	-2074
R ₈₃	-74	-2074
V_{DD}	86	-2074
R ₉₀	246	-2074
R ₉₁	449	-2074
R ₉₂	610	-2074
R ₄₀	802	-2074
R ₄₁	962	-2074
R ₄₂	1122	-2074
R ₄₃	1282	-2074
R ₅₀	1644	-2011
R ₅₁	1644	-1807
R ₅₂	1644	-1647
R ₅₃	1644	-1444
R ₆₀	1644	-1283
R ₆₁	1644	-1080
R ₆₂	1644	-920
R ₆₃	1644	-716
R ₇₀	1644	-556
R ₇₁	1644	-353
R ₇₂	1644	-193
R ₇₃	1644	62
SEG0	1644	223
SEG1	1644	383
SEG2	1644	543
SEG3	1644	703
SEG4	1644	863
SEG5	1644	1024
SEG6	1644	1184
SEG7	1644	1344
SEG8	1644	1504
SEG9	1644	1664
SEG10	1644	1825
SEG11	1644	1985

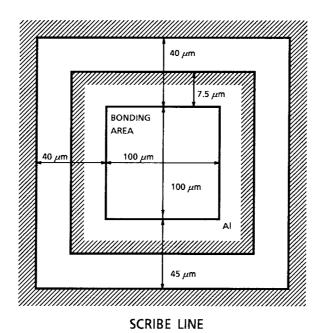
Name	X Point	Y Point
SEG12	1202	2074
SEG13	1042	2074
SEG14	881	2074
SEG15	721	2074
SEG16	561	2074
SEG17	401	2074
SEG18	241	2074
SEG19	80	2074
SEG20	-80	2074
SEG21	-240	2074
SEG22	-400	2074
SEG23	-560	2074
SEG24	-721	2074
SEG25	-881	2074
SEG26	-1041	2074
SEG27	-1201	2074
SEG28	-1644	1961
SEG29	-1644	1801
SEG30	-1644	1641
SEG31	-1644	1481
COM1	-1644	1321
COM2	-1644	1160
COM3	-1644	1000
COM4	-1644	840
V _{LC}	-1644	520
P10	-1644	359
P11	-1644	156
P12	-1644	-4
P13	-1644	-208
V _{SS}	-1644	-368
P20	-1644	-528
P21	-1644	-731
P22	-1644	-892
P23	-1644	-1095
TEST	-1644	-1255
XIN	-1644	-1415
XOUT	-1644	-1651
BRESET	-1644	-1811
BHOLD	-1644	-1971

Chip Layout



Pad Layout

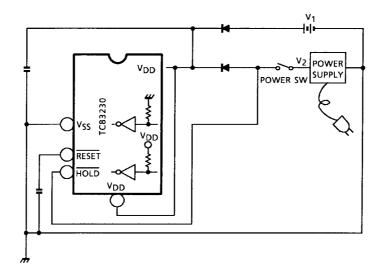
Active Element



Pad pitch 160 (μm)



The Proposal of Outer Circuit for Tax Rate Holding with Back-Up Battery.



Note 9: $V_1 = +3 \text{ V: Battery supply}$

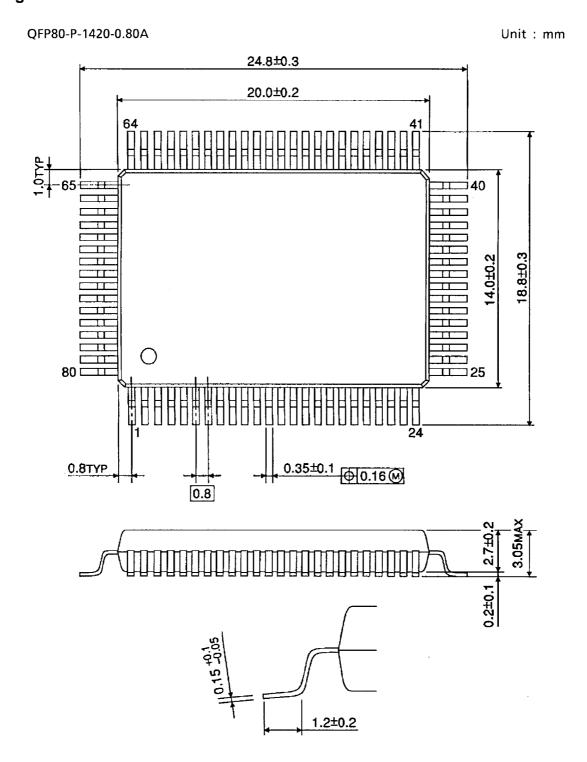
 $V_2 = +5 \text{ V: DC supply}$

 $\left(rac{\overline{HOLD}}{\overline{RESET}}
ight)$ pin is pulled down in the LSI, but normally pulled up to VDD.

- (1) Setting POWER SW to ON, V_2 is supplied to V_{DD} pin, and also to \overline{HOLD} pin. Then calculator operates normally.
- (2) Setting POWER SW from ON to OFF, V_1 is supplied to V_{DD} pin and V_{SS} is supplied to \overline{HOLD} pin. Under this connection, TAX RATE is held.
- (3) Setting POWER SW to ON, V_2 is supplied to V_{DD} pin, and also to \overline{HOLD} pin. Then calculator operates normally with TAX RATE to be held.

Note 10: V₁ (battery) should be supplied to the circuit after V₂ (DC) supply, because of prevention from exhaustion of battery and abnormal operation.

Package Dimensions



Weight: 1.52 g (typ.)

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000707EBA

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