

- Single Output
- TTL Compatible
- 14 Pin DIL Package



Specifications:

- Delay tolerance : $\pm 5\%$ or 2ns
whichever is greater
- Rise time : 4ns max.
- Temp. coefficient : 100 ppm/°C
- Operating Temp. Range : -0°C to 70°C
- Minimum Pulse Width : 40% of total delay
- Supply Voltage : 5Vdc $\pm 5\%$
- Logic 1 Input current : 50 UA max.
- Logic 0 Input current : -2 mA max.
- Logic 1 Output : 2.75V min.
- Logic 0 Output : 0.4V max.
- Logic 1 Fan out : 20/TAP max.
- Logic 0 Fan out : 10/TAP max.
- Power Dissipation : 375 mW max.

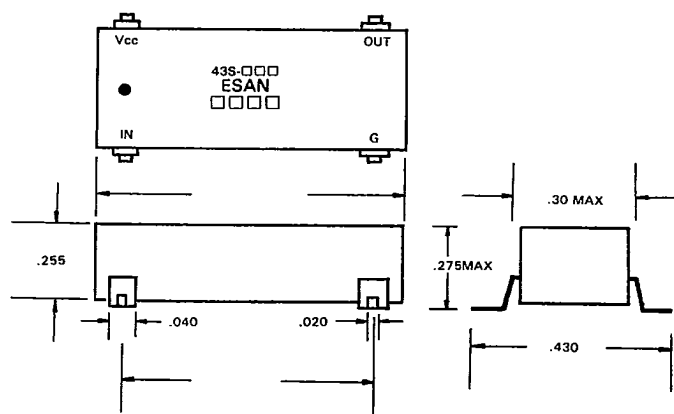
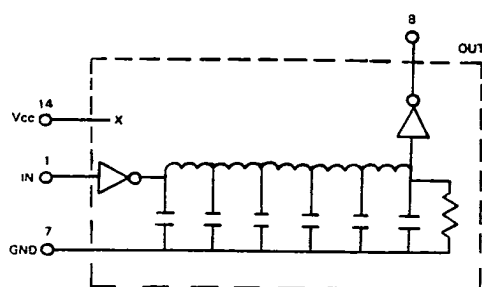
Test Conditions:

- Input pulse voltage : 3.2V
- Input pulse width : 1.2 * total delay
- Input pulse spacing : 2 * total delay
- Input pulse rise time: : 2 ns
- Rise time : 0.75V to 2.4V
- Time delay measured : @1.5V level
- All measurement : @Vcc = 5.0V, TA = 25°C

Features:

- Schottky TTL interface
- Low power schottky available named "43SL- xxx" series
- Both Leading and Trailing edge available under request named "43SB- xxx" series

PART NO.	DELAY TIME (ns)
43S-025	25
43S-050	50
43S-075	75
43S-100	100
43S-125	125
43S-150	150
43S-200	200
43S-250	250
43S-300	300
43S-400	400
43S-500	500



Unit: Inch