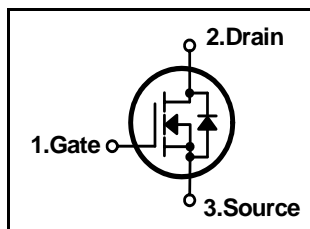


N-Channel MOSFET

Features

- Low $R_{DS(on)}$ (0.04Ω)@ $V_{GS}=10V$
- Low Gate Charge (Typical 27nC)
- Low C_{rss} (Typical 75pF)
- Improved dv/dt Capability
- 100% Avalanche Tested
- Maximum Junction Temperature Range



$BV_{DSS} = 60V$
 $R_{DS(ON)} = 0.04 \text{ ohm}$
 $I_D = 30A$

General Description

This N-channel enhancement mode field-effect power transistor using DI semiconductor's advanced planar stripe, DMOS technology intended for battery operated systems like a DC-DC converter motor control, ups, audio amplifier. Also, especially designed to minimize $r_{ds(on)}$, low gate charge and high rugged avalanche characteristics.

TO-252
(D-Pak)



Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_{DSS}	Drain to Source Voltage	60	V
I_D	Continuous Drain Current(@ $T_C = 25^\circ C$)	24.5	A
	Continuous Drain Current(@ $T_C = 100^\circ C$)	17.3	A
I_{DM}	Drain Current Pulsed (Note 1)	98	A
V_{GS}	Gate to Source Voltage	± 20	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	430	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	7.0	V/ns
P_D	Total Power Dissipation(@ $T_C = 25^\circ C$)	53	W
	Derating Factor above $25^\circ C$	0.35	W/ $^\circ C$
T_{STG}, T_J	Operating Junction Temperature & Storage Temperature	- 55 ~ 175	$^\circ C$
T_L	Maximum Lead Temperature for soldering purpose, 1/8 from Case for 5 seconds.	300	$^\circ C$

Thermal Characteristics

Symbol	Parameter	Value			Units
		Min.	Typ.	Max.	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	-	-	2.85	$^\circ C/W$
$R_{\theta CS}$	Thermal Resistance, Case to Sink	-	0.5	-	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	-	-	62.5	$^\circ C/W$

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Electrical Characteristics (T_C = 25 °C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250uA	60	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature coefficient	I _D = 250uA, referenced to 25 °C	-	0.06	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 60V, V _{GS} = 0V	-	-	1	uA
		V _{DS} = 48V, T _C = 150 °C	-	-	10	uA
I _{GSS}	Gate-Source Leakage, Forward	V _{GS} = 20V, V _{DS} = 0V	-	-	100	nA
	Gate-Source Leakage, Reverse	V _{GS} = -20V, V _{DS} = 0V	-	-	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250uA	2.0	-	4.0	V
R _{DS(ON)}	Static Drain-Source On-state Resistance	V _{GS} = 10 V, I _D = 12.3A	-	0.029	0.04	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = 25V, f = 1MHz	-	930	1210	pF
C _{oss}	Output Capacitance		-	290	380	
C _{rss}	Reverse Transfer Capacitance		-	75	100	
Dynamic Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} = 30V, I _D = 15A, R _G = 50Ω * see fig. 13. (Note 4, 5)	-	15	40	ns
t _r	Rise Time		-	25	60	
t _{d(off)}	Turn-off Delay Time		-	60	130	
t _f	Fall Time		-	40	90	
Q _g	Total Gate Charge	V _{DS} = 48V, V _{GS} = 10V, I _D = 30A * see fig. 12. (Note 4, 5)	-	27	35	nC
Q _{gs}	Gate-Source Charge		-	6.2	-	
Q _{gd}	Gate-Drain Charge(Miller Charge)		-	11.1	-	

Source-Drain Diode Ratings and Characteristics

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit.
I _S	Continuous Source Current	Integral Reverse p-n Junction Diode in the MOSFET	-	-	24.5	A
I _{SM}	Pulsed Source Current		-	-	98	
V _{SD}	Diode Forward Voltage	I _S = 24.5A, V _{GS} = 0V	-	-	1.5	V
t _{rr}	Reverse Recovery Time	I _S = 30A, V _{GS} = 0V, di _F /dt = 100A/us	-	45	-	ns
Q _{rr}	Reverse Recovery Charge		-	65	-	nC

* NOTES

1. Repeativity rating : pulse width limited by junction temperature
2. L = 560uH, I_{AS} = 30A, V_{DD} = 25V, R_G = 0Ω, Starting T_J = 25°C
3. I_{SD} ≤ 30A, di/dt ≤ 300A/us, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
4. Pulse Test : Pulse Width ≤ 300us, Duty Cycle ≤ 2%
5. Essentially independent of operating temperature.

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Fig 1. On-State Characteristics

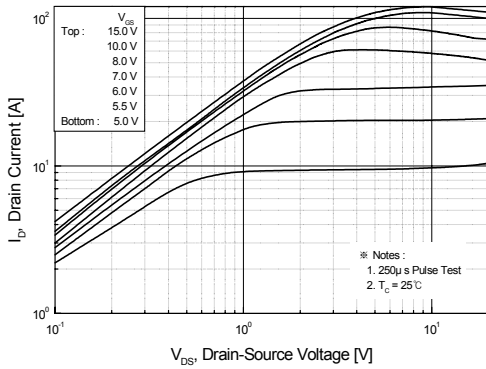


Fig 2. Transfer Characteristics

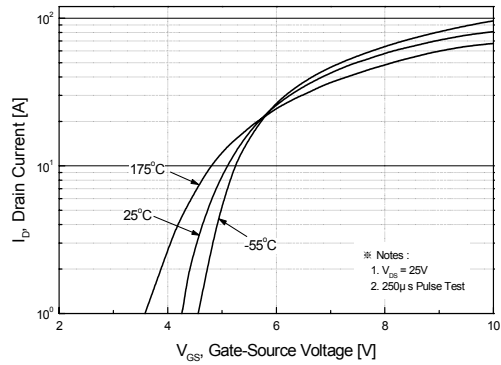


Fig 3. On Resistance Variation vs. Drain Current and Gate Voltage

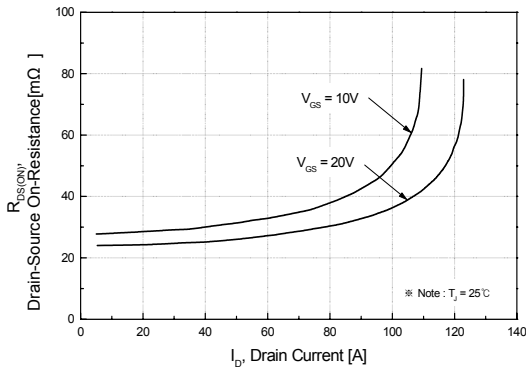


Fig 4. On State Current vs. Allowable Case Temperature

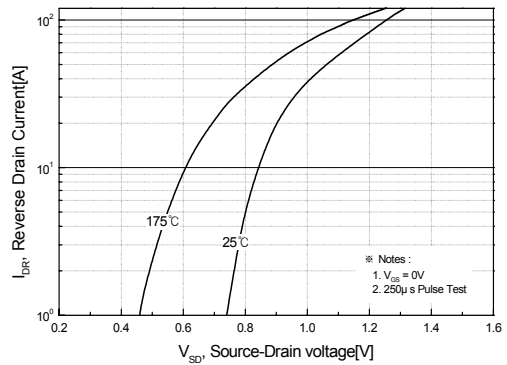


Fig 5. Capacitance Characteristics

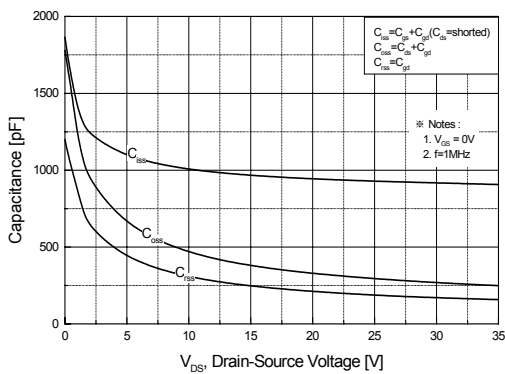
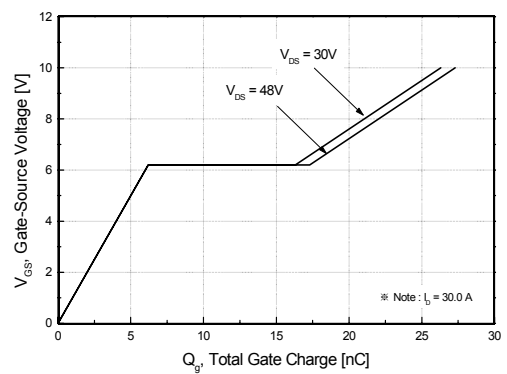


Fig 6. Gate Charge Characteristics



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Fig 7. Breakdown Voltage Variation vs. Junction Temperature

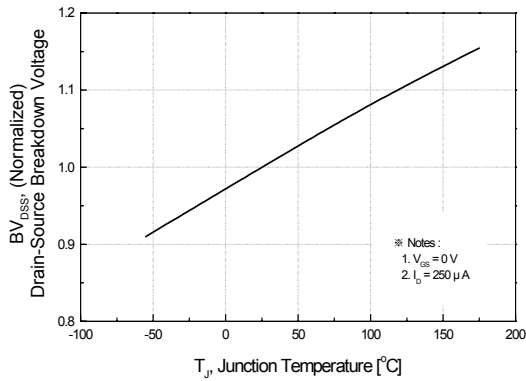


Fig 8. On-Resistance Variation vs. Junction Temperature

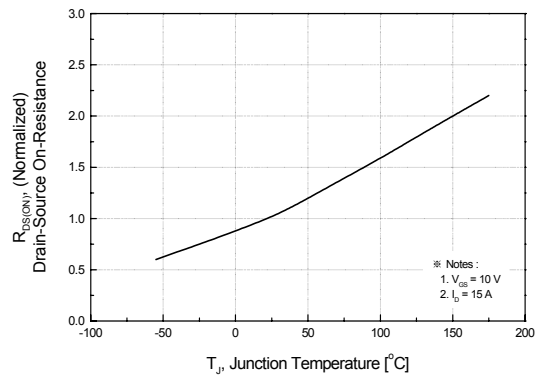


Fig 9. Maximum Safe Operating Area

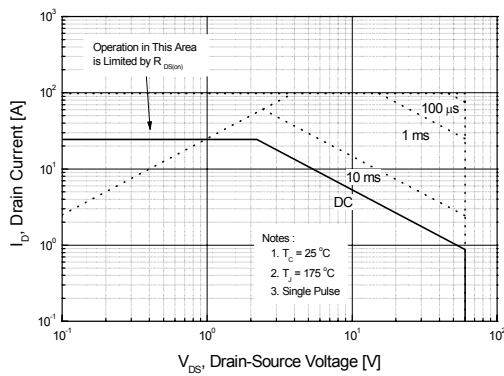


Fig 10. Maximum Drain Current vs. Case Temperature

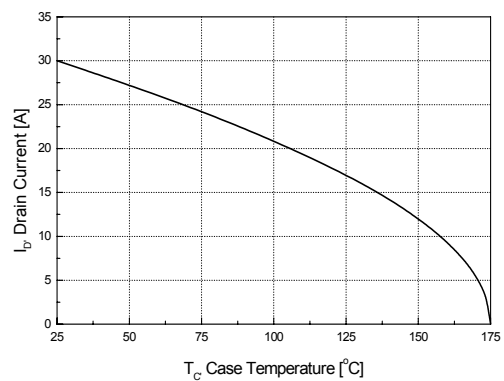
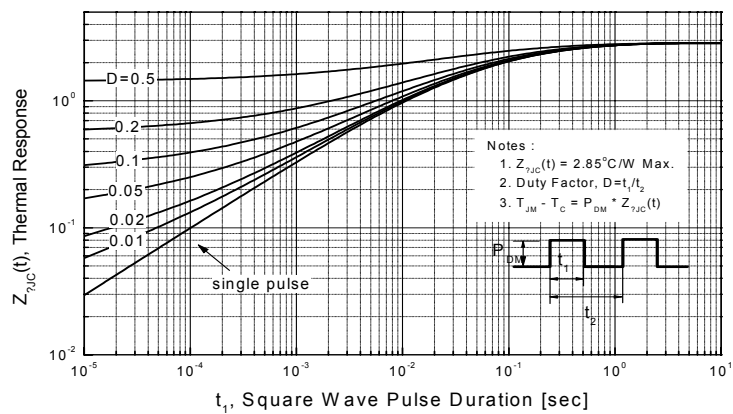


Fig 11. Transient Thermal Response Curve



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Fig. 12. Gate Charge Test Circuit & Waveforms

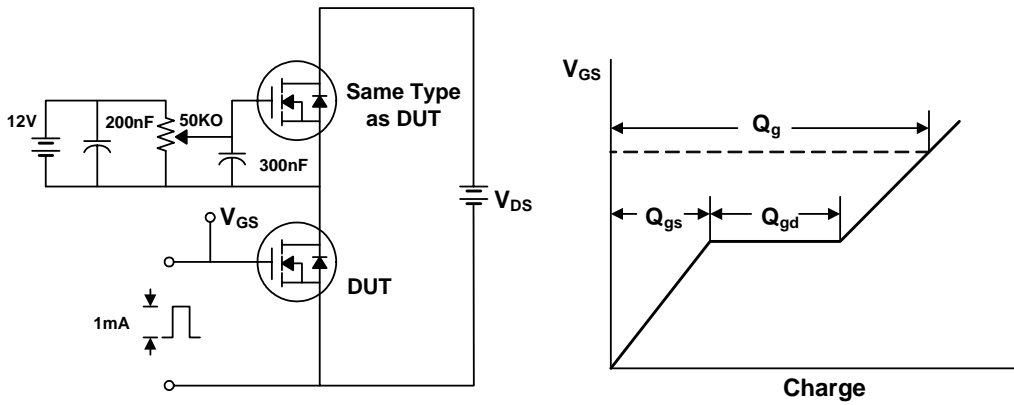


Fig 13. Switching Time Test Circuit & Waveforms

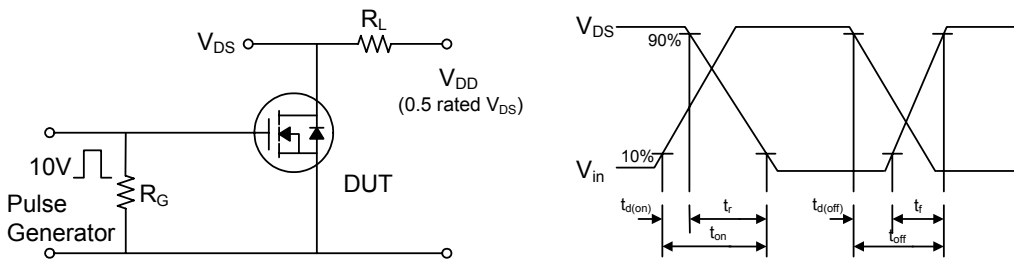
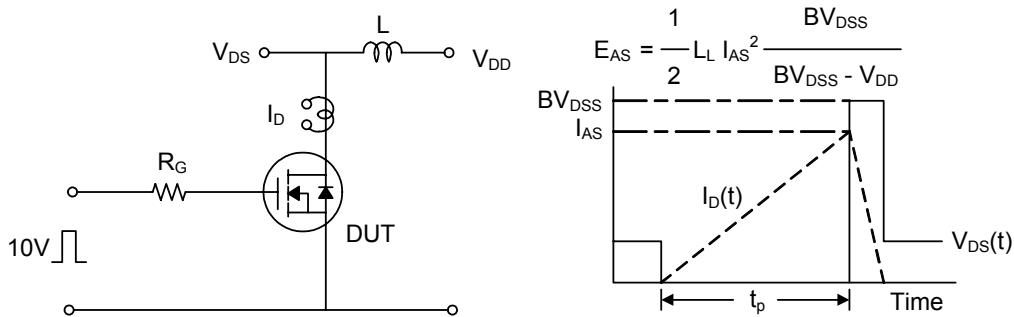
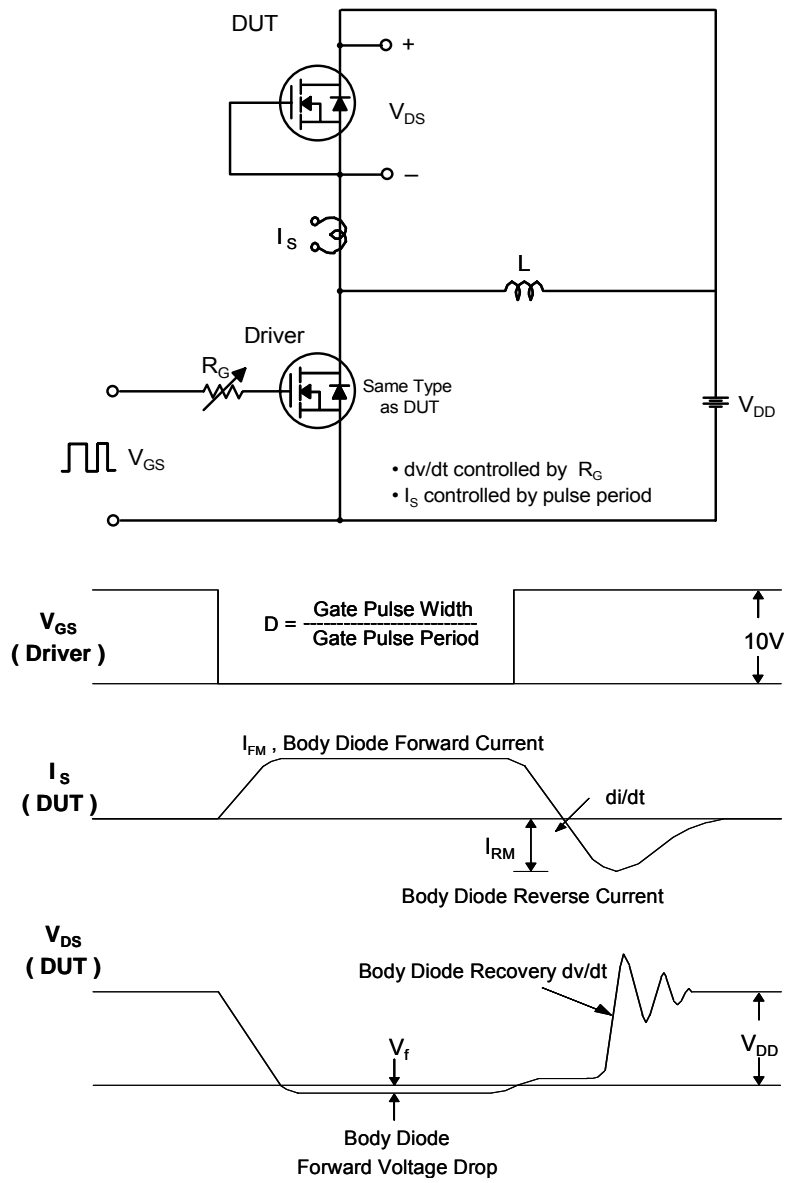


Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms



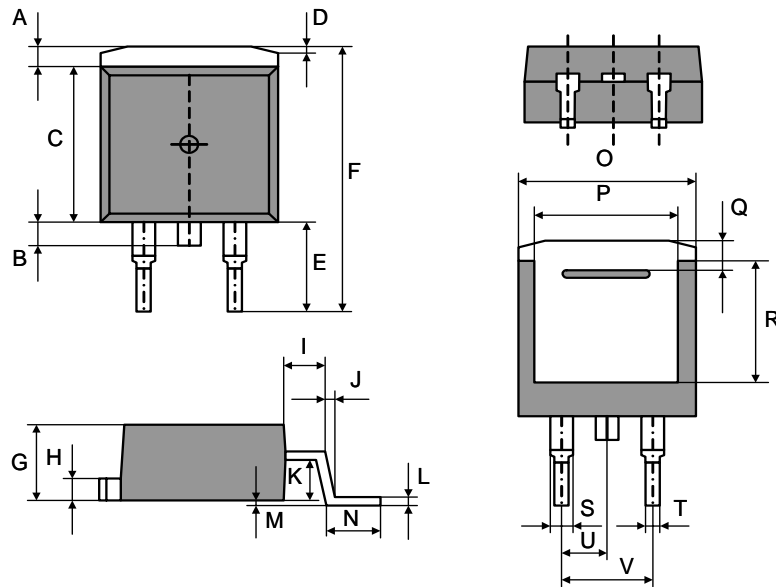
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Fig. 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



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TO-252(D-Pak) Package Dimension



DIMENSION		A	B	C	D	E	F	G	H
mm	Min	5.9	1	0.6	7.1	0.7	2.1	0.79	0.4
	Typ.	6.1	1.2	0.8	7.3	0.8	2.3	0.89	0.5
	Max	6.3	1.4	1	7.5	0.9	2.5	0.99	0.6

DIMENSION		I	J	K	L	M	N	O	
mm	Min	0.92	6.4	5.14	1			4.4	
	Typ.	1.02	6.6	5.34	1.2	0.96	2.3	4.6	
	Max	1.12	6.8	5.64	1.4			4.8	