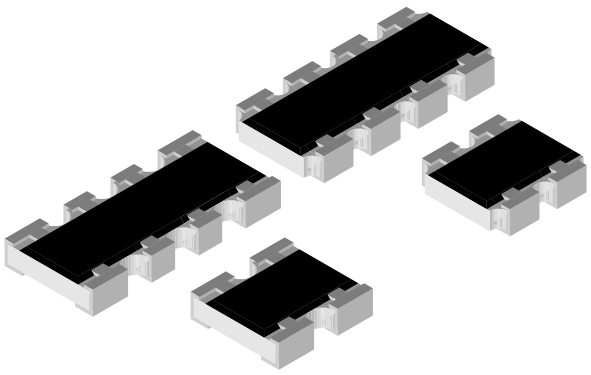
CRA06E and S

Vishay Dale

Thick Film, Resistor Array



FEATURES

- 4, 8 and 10 terminal package
- Single component reduces board space and component counts
- Wrap around termination
- Inner electrode protection
- Flow & Reflow solderable
- Automatic placement capability
- Consult factory for additional schematics, values, etc

STANDARD ELECTRICAL SPECIFICATIONS

MODEL	POWER RATING $P_{70°C}$ W	CIRCUIT	LIMITING ELEMENT VOLTAGE MAX. V≅	TEMPERATURE COEFFICIENT ppm/°C	TOLERANCE %	RESISTANCE RANGE Ω	E-SERIES
CRA06E & S	0.0625	03	50	200	1, 2 , 5	10R-1M0	24-96
				Jumper: Zero-Ohm-Resistor on Request			

- Power rating depends on the max. temperature at the solder point, the component placement density and the substrate material
- Operating temperature Range: - 55°C to + 150°C
- Ask about further value ranges
- Packaging: according to EIA 481

TECHNICAL SPECIFICATIONS

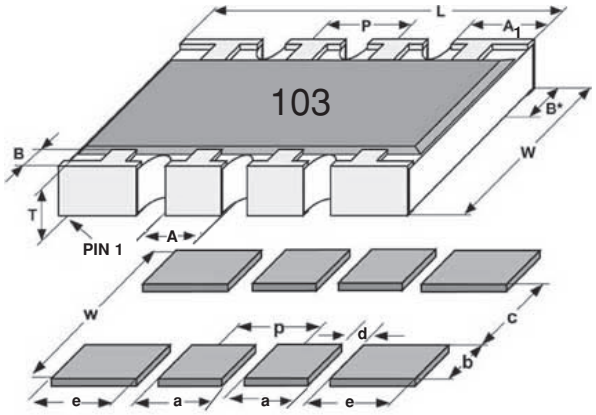
PARAMETER	UNIT	CRA06E & S 03 CIRCUIT
Rated Dissipation at 70°C	W	0.0625
Limiting Element Voltage [1]	V≅	50
Insulation Voltage (1min)	$V_{dc/ac\ peak}$	100
Category Temperature Range	°C	- 55 / + 150
Insulation Resistance	Ω	> 10^{10}

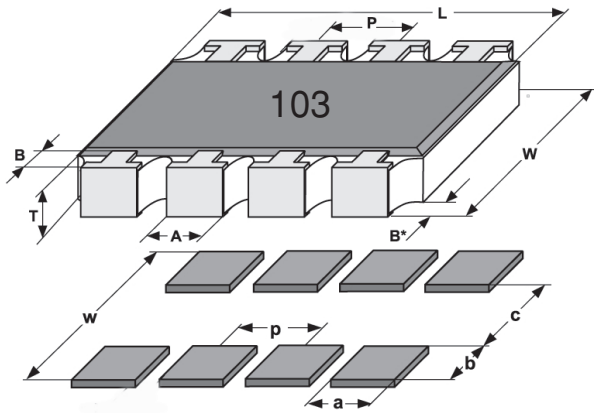
[1] Rated voltage: $\sqrt{P \times R}$

ORDERING INFORMATION

CRA06S	08	03	473	J	RT1
MODEL	TERMINAL COUNT	CIRCUIT TYPE	R-VALUE Ω	TOLERANCE ± %	PACKAGING
CRA06S	04, 08, 10	03	First two digits (three for 1%) are significant. Last digit is the multiplier 473 = 47K 4702 = 47K 100 = 10Ω 10R0 = 10Ω 000 = 0Ω Jumper	F = ± 1% G = ± 2% J = ± 5% Z = 0Ω Jumper	Papertape 5000pcs
CRA06E	08	03			

8-Terminal device S - Version E - Version



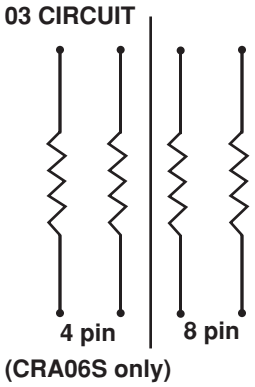


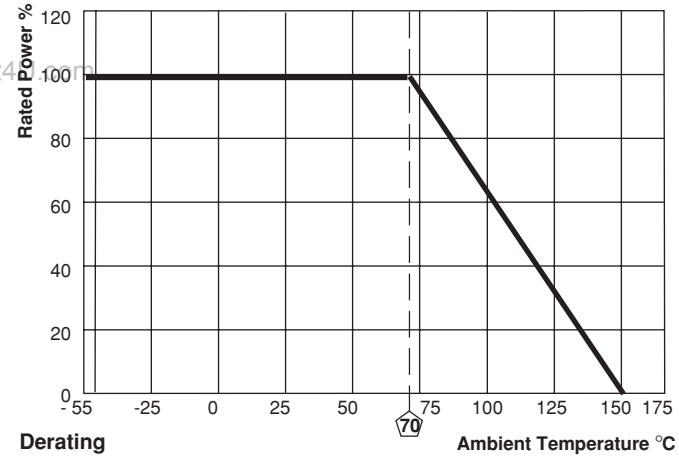
PIN NO#	DIMENSIONS [in millimeters]						
	L	A	B	B*	P	T	W
4	1.6	0.38	0.3	0.3	0.8	0.4	1.5
8	3.2	0.38	0.3	0.3	0.8	0.4	1.5
10	3.2	0.34	0.3	0.2	0.64	0.5	1.6
Tol	± 0.15	± 0.15	± 0.15	± 0.15	± 0.1	± 0.05	± 0.15

S-Version: $A_1 = 0.61 \pm 0.15$, 8 pin and 4 pin, $A_1 = 0.49 \pm 0.15$ for 10 pin

SOLDER PAD DIMENSIONS [in millimeters]								
MODEL	PINS	c	w	d	p	a	b	e
CRA06S	4	0.8	3.1	0.36		0.44	1.15	
CRA06E + S	8	0.8	3.1	0.36	0.8	0.44	1.15	0.63
CRA06S	10	0.8	3.1	0.30	0.64	0.34	1.15	0.45

CIRCUIT SCHEMATIC





PERFORMANCE

TEST	CONDITIONS OF TEST	TEST RESULTS
Endurance Test at 70°C per EIA 575	1000 hour at 70°C, 1.5 hours "ON", 0.5 hours "OFF"	±1.0%
Overload per EIA 575	Short time overload 2.5 x rated continuous working voltage for 5 seconds. Not to exceed 2 x max operating voltage	± 0.5%
Thermal Shock	per EIA 575-3.5	± 0.5%
Moisture Resistance	per EIA 575-3.10	± 1.0%
Resistance to Soldering Heat EIA 575	10 seconds at 260°C solder bath temperature	± 2.0%
High Temperature Exposure	per EIA 575-3.7	± 1.0%
Low Temperature Operations	per EIA-575-3.6	± 0.5%
Solderability & Leaching	EIA 575-3.12	95% Coverage