10 Watts
AEE01-12Vin

Total Power:	**10 Watts**
Input Voltages:	**12V**
No. of Outputs:	**Single and Dual**

Electrical Specs

Input

Input range	9 to 18 VDC
Efficiency	78% typical (3.3V)

Output

Voltage tolerance	±1.0%
Overall regulation	±1% max
Noise/ripple	1% Typ, 2% Max
Transient response	300 usec typical (50% step load change to within 3%Vo)
Temperature Regulation	±0.02 %Vo/°C
Switching frequency	330kHz

Isolation

I/O isolation	1500 VDC Input to Output
	1000 VDC Input to Case
	1000 VDC Output to Case
Isolation Resistance	300 Mohms

Special Features

- 10 W, Dual output
- 2:1 Input range
- 1.0" x 2.0" x 0.33 case
- Industry standard package
- Low Profile
- Low Cost
- Trim and Enable Option on single out module only

Environmental

Operating base temperature range:

-25°C to +100°C

Storage temperature: -40°C to +125°C

MTBF: 3 Million Hours (Belcore TR332)

Safety

UL	UL1950 Recognition
CSA	CSA22.2-950 Recognition
TUV	EN60950 Recognition

America's (USA): 1-888-41-ASTEC   Europe (UK): 44 (1384) 842-211   Asia (HK): 852-2437-9662   ASTEC

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Ordering Information

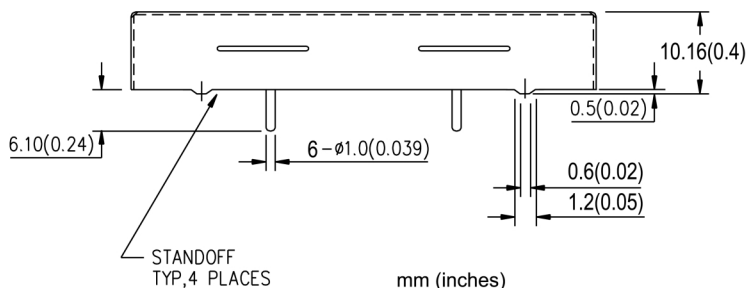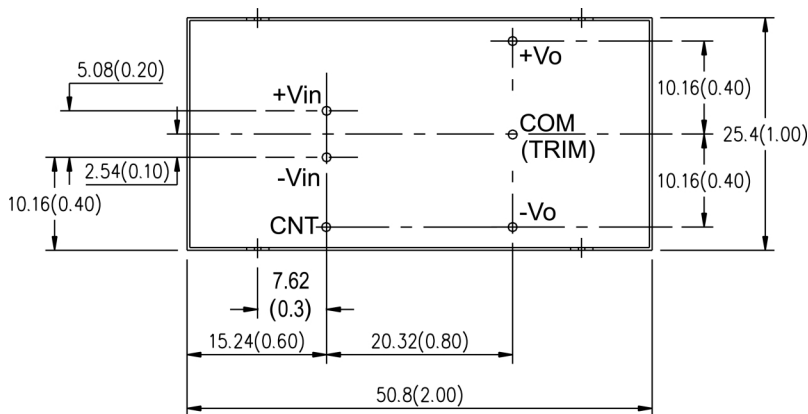| Input Voltage	Output Voltage	Efficiency	Model Number
9-18 V	3.3V @ 2.55 A	78%	AEE02F12
9-18 V	5.0V @ 2 A	81%	AEE02A12
9-18 V	12V @ 0.84 A	83%	AEE01B12
9-18 V	15V @ 0.68 A	84%	AEE01C12
9-18 V	±5V @ ±1.0 A	81%	AEE01AA12
9-18 V	±12V @ ±0.42 A	83%	AEE00BB12
9-18 V	±15V @ ±0.33 A	84%	AEE00CC12

NOTES:

Enable Function
"1" = negative logic enable (std, without "1 or 4" = no enable)
"4" = positive logic enable (std, without "1 or 4" = no enable)
Trim Feature
"9" = Trim feature (std, without "9" no Trim) Available on Single Output devices only

mm (inches)

Notes:

1. 20 MHz bandwidth. An external 0.1 uf ceramic capacitor is recommended to be placed from +V out to comm.
2. All specifications are typical at nominal line, full load, and 25°C unless otherwise noted.
3. All specifications subject to change without notice.
4. Mechanical drawings are for reference only.

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