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 TLCS-900/H1 Cl ·Operating voltage: 3.0 to 3.6 V ·Minimum instruction ex 50 ns (at 20 MHz/ 3.0 t ·Internal ROM: 512 Kby ·Internal RAM: 32 Kbyt Built-in Function ·USB Host controller ·UART/SIO ·SBI ·HSIO ·SPI 	ecution time: o 3.6 V internal) tes es	10MHz PLL 32kHz WDT Input/ Output → 70 Int. CS/WAIT → CS/WAI Bolocks Pulse Output ↓ 8bit TIMER Input Pulse Output ↓ 16bit TIMER (6ch) Pulse Output ↓ 16bit TIMER (2ch)	RAM SBI (2ch)	C→D ^{D+/D-} USBOC USBOC C→D ^{HSCLK/HS} C→D ^{HSCLK/HS} C→D ^{SCLK/SI/S} C→D ^{SCLK/SI/S} C→SDA/SCL C→D ^{SPCLK/SF}
·8-bit timer ·16-bit timer	: 6channels : 2channels	Product Lineur)	**:Under developm
·Key-on wake-up	: 4channels	Part number	ROM	RAM
·Program patch logic	: 8banks	TMP92CD28FG/DFG**	512 Kbytes	32 Kbytes

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Development Systems

Software Products

Toshiba Integrated Development Environment (TIDE)		Real-Time OS (µITRON 3.0)	
C Compiler	Integrated Development Environment	Keai-11ine 05 (µ11K0N 3.0)	
SW96CN0-ZCC: 1 license SW96CN3-ZCC: 10 licenses		SW96RN2-ZCC: Object code can be freely copied SW96RNC-ZCC: Object code can be freely copied. With source code.	

Hardware Products

Part number	Test tool		
rart number	Controller	Emulation pod	
TMP92FD28FG/DFG	BM1040R0A/BM1055R0B	BM92CD28F0A-M15**	

>As to hardware products, additional accessories are also needed.

> For further information about Toshiba microcomputer development systems, please visit http://www.semicon.toshiba.co.jp/mctool/index_e.htm. This product uses the SuperFlash® technology under license of Silicon Storage Technology, Inc. Super Flash® is a registered trademark of Silicon Storage Technology, Inc.

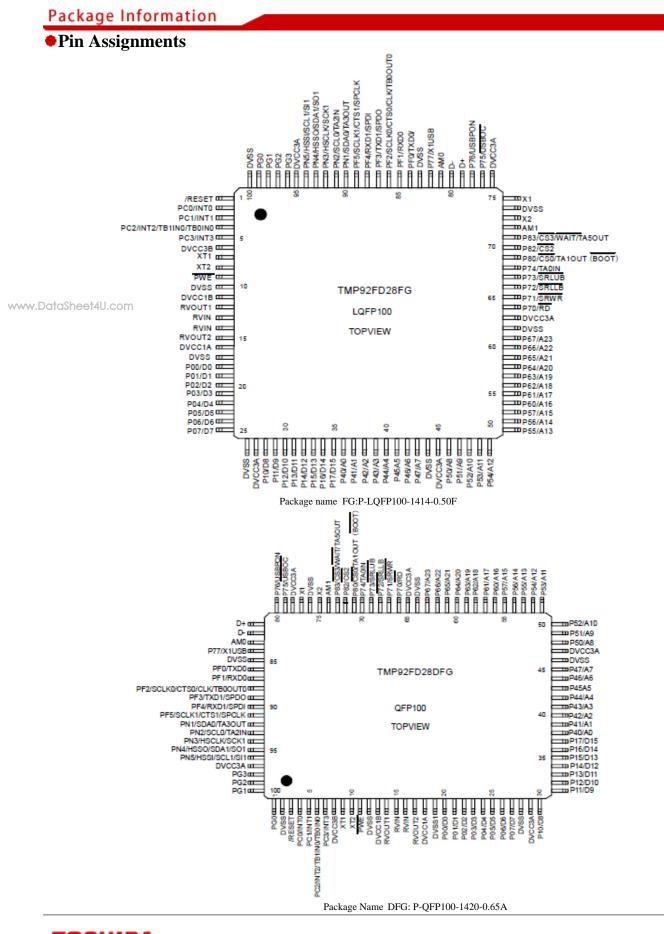
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