CELERITEK

400 SERIES - Gain Blocks

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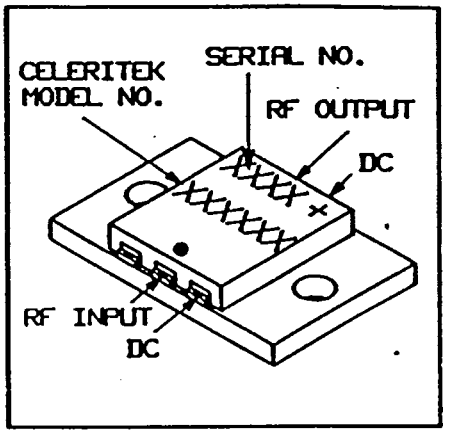
Data Sheet - Jan 88

FEATURES

- Cascadable Gain Blocks
- 0.400 inch Case Length
- Drop-in connectorless Design
- Copper Tungsten Flange for Heat Transfer
- Hermetically Sealed

The 400 series of gain blocks are multistage hybrid amplifiers which can be cascaded to give higher values of gain.



ELECTRICAL SPECIFICATIONS +25°C

MODEL	FREQUENCY RESPONSE (GHz)	GAIN (dB) MIN	GAIN (dB) TYP	GAIN FLATNESS (±dB) MAX	GAIN FLATNESS (±dB) TYP	* NOISE FIGURE (dB) MAX	* NOISE FIGURE (dB) TYP	* POWER 1 dB GAIN COMP. (dBm) MIN	* POWER 1 dB GAIN COMP. (dBm) TYP	POWER SATURATED (dBm) TYP	CURRENT AT +8V (mA) TYP
400-4-0501	.5-4	30.0	33.0	0.5	0.4	4.0	3.5	14.0	16.0	17.0	185
400-8-2001	2-8	19.0	21.0	0.5	0.4	5.0	4.5	19.0	20.0	22.0	120
400-8-2002	2-8	7.5	8.5	0.5	0.4	7.5	6.5	26.0	27.0	29.0	280
400-16-4001	4-16	11.0	12.0	0.5	0.4	7.0	6.5	20.0	21.0	23.0	300
400-18-6001	6-18	13.0	15.0	0.5	0.4	6.0	5.0	12.0	14.0	17.0	120
400-18-6002	6-18	11.0	13.0	0.5	0.4	7.0	6.0	18.0	19.0	21.0	140
400-18-6003	6-16	11.0	12.0	0.7	0.6	7.0	6.5	20.0	21.0	23.0	300

ELECTRICAL SPECIFICATIONS -55°C TO +90°C

MODEL	FREQUENCY RESPONSE (GHz)	GAIN (dB) MIN	GAIN (dB) TYP	GAIN FLATNESS (±dB) MAX	GAIN FLATNESS (±dB) TYP	* NOISE FIGURE (dB) MAX	* NOISE FIGURE (dB) TYP	* POWER 1 dB GAIN COMP. (dBm) MIN	* POWER 1 dB GAIN COMP. (dBm) TYP	POWER SATURATED (dBm) TYP	CURRENT AT +8V (mA) TYP
400-4-0501	.5-4	28.7	31.7	0.7	0.6	4.8	4.3	13.0	15.0	16.0	195
400-8-2001	2-8	17.7	19.7	0.7	0.6	5.8	5.3	18.0	19.0	21.0	130
400-8-2002	2-8	6.8	7.8	0.7	0.6	8.3	7.3	25.0	26.0	28.0	290
400-16-4001	4-16	9.7	10.7	0.7	0.6	7.8	7.3	19.0	20.0	22.0	310
400-18-6001	6-18	11.7	13.7	0.7	0.6	6.8	5.8	11.0	13.0	16.0	130
400-18-6002	6-18	9.7	11.7	0.7	0.6	7.8	6.8	17.0	18.0	20.0	150
400-18-6003	6-18	9.7	11.7	0.9	0.8	7.8	7.3	19.0	20.0	22.0	310

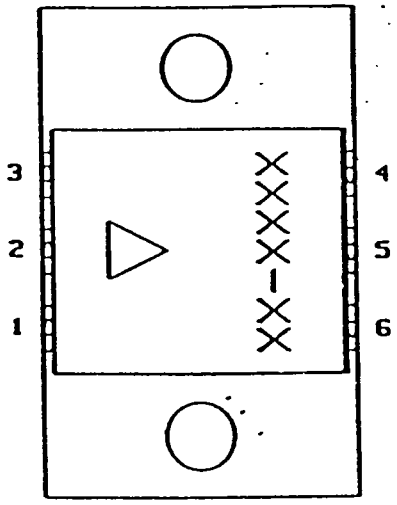
* HIGHER POWER AND LOWER NOISE FIGURES AVAILABLE ON REQUEST

400 CASE ASSEMBLY/TESTING INSTRUCTIONS

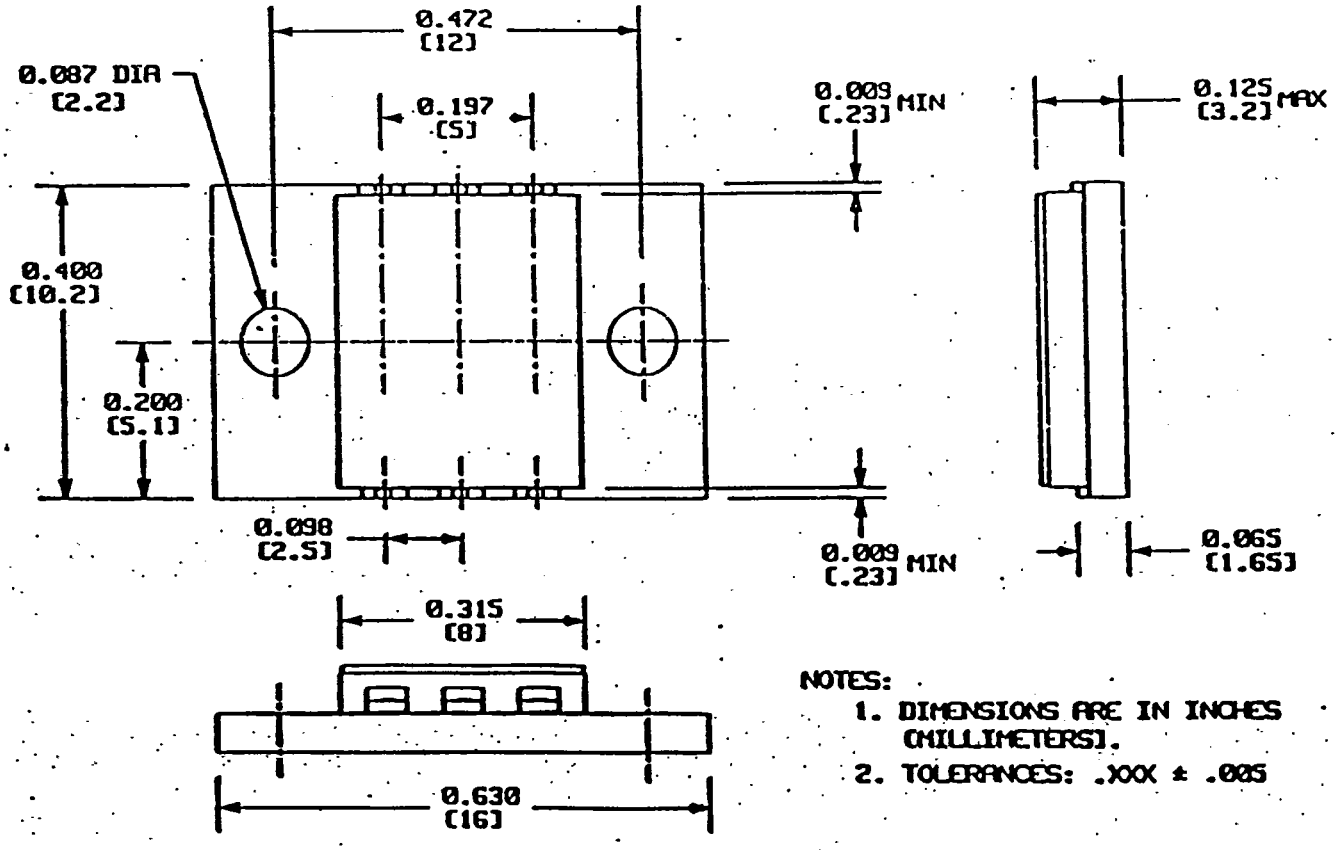
1. Use 1 x 10 mil gold ribbon to interconnect the cases to each other or to the outside world. A gap welder or heated wedge can be used to make the interconnection.
2. Use a regulated power supply when testing the units. DC bias should not exceed 10 volts. Over voltage and reverse bias protection is not included.
3. RF input power should not exceed +20 dBm.

CONNECTION DIAGRAM

1: B+
2: RF in
3: NC
4: NC
5: RF out
6: B+
GND: (CASE FLANGE)



400 CASE DIMENSIONS



NOTES:
1. DIMENSIONS ARE IN INCHES [MILLIMETERS].
2. TOLERANCES: .XXX ± .005

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