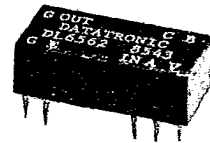


SERIES DL6560 PROGRAMMABLE DIGITAL DELAY LINES

- ECL Interfaced • Digitally Programmable in 8 Delay Steps (3 Bit)
- No External Components Required • 16 Pin DIL

Specifications

- Supply Voltage : -5.2v
- Logic 1 Voltage : -.96v
- Logic 1 Current : .26mA
- Logic 0 Current : .5μA
- Logic 0 Voltage : -1.65v
- Operating Temperature : -30°C to 85°C
- Power Dissipation : 200mW Typ. (No Load)



Input Test Conditions

- Input Pulse-Width : Min. 40% of Total Delay
- Input Pulse Rise : 2NS
- Input Pulse Voltage : -.7v
- Vee Supply Voltage : -5.2VDC
- Vee Supply Current : 40mA Typ.

Truth Table

ADDRESS BIT NO.				Total Delay
Eo	C	B	A	
L	L	L	L	1D
L	L	L	H	1D+T
L	L	H	L	1D+2T
L	L	H	H	1D+3T
L	H	L	L	1D+4T
L	H	L	H	1D+5T
L	H	H	L	1D+6T
L	H	H	H	1D+7T

When Eo is H, no output
 1D = Inherent Delay = 3NS ± 1NS
 T = Multiplier of Incremental Delay
 (Delay change per step)

Part Number	Incremental Delay per step (NS)	Total Delay (NS)
DL6561	1 ± 0.3	10 ± 1
DL6562	2 ± 0.4	17 ± 1
DL6563	3 ± 0.5	24 ± 1.4
DL6564	4 ± 0.5	31 ± 1.5
DL6565	5 ± 0.5	38 ± 1.5
DL6566	10 ± 1	73 ± 2
DL6567	20 ± 1.5	143 ± 4
DL6568	50 ± 2.5	353 ± 10

