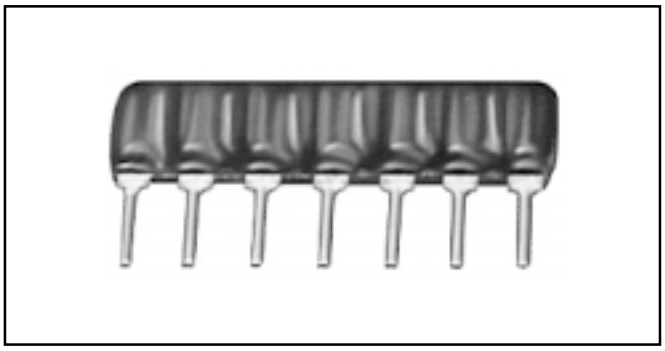
Single-In-Line, Coated, 4 Bits to 8 Bits

R/2R Ladder Networks



APPLICATIONS

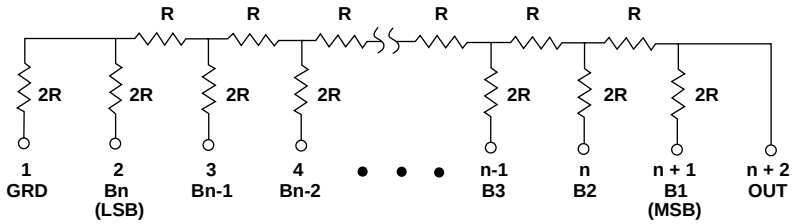
R/2R Ladder networks for D/A and A/D converter with bi-polar or CMOS switches.

ELECTRICAL SPECIFICATIONS

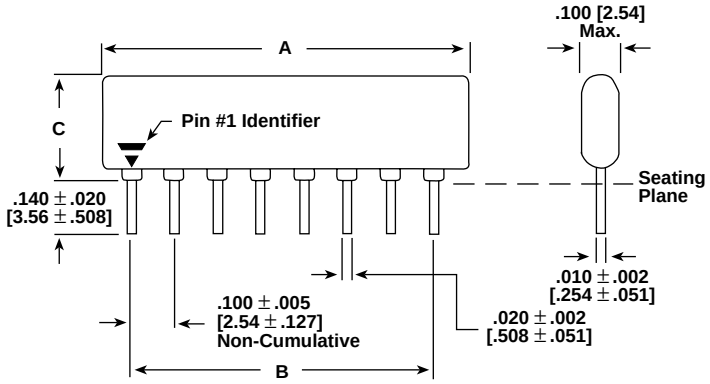
Ladder Network Accuracy on Linearity: ± 1/2 LSB.
Ladder Network Resistance Tolerance: ± 2%.
Temperature Coefficient of Resistance: ± 100PPM/°C.
Operating Temperature Range: - 55°C to + 125°C.
Power Dissipation Rating at + 70°C Ambient: 50mW/element.
Standard Resistance Values (R): 5 kilohms, 10 kilohms, 25 kilohms, 50 kilohms and 100 kilohms.

SCHEMATIC

n Bits:
n = 4 thru 8



DIMENSIONAL CONFIGURATIONS **[Numbers in brackets indicate millimeters]**



NUMBER OF PINS	A (Max.)	B ± .005 [.127]	C (Max.)
6	.590 [14.99]	.500 [12.70]	.350 [8.89]
7	690 [17.53]	.600 [15.24]	.350 [8.89]
8	.790 [20.07]	.700 [17.78]	.350 [8.89]
9	.890 [22.61]	.800 [20.32]	.350 [8.89]
10	.990 [25.15]	.900 [22.86]	.350 [8.89]

HOW TO ORDER

T10S
MODEL

T06S = 6 pin SIP
T07S = 7 pin SIP
T08S = 8 pin SIP
T09S = 9 pin SIP
T10S = 10 pin SIP

08
NUMBER OF BITS

6 pin = 4 Bits
7 pin = 5 Bits
8 pin = 6 Bits
9 pin = 7 Bits
10 pin = 8 Bits

104
RESISTANCE VALUE (Ohms)

First two digits are significant, third digit signifies number of zeros to follow.

EXAMPLE: 104 = R = 100 kilohms.
REFERENCE: 2R = 200 kilohms.