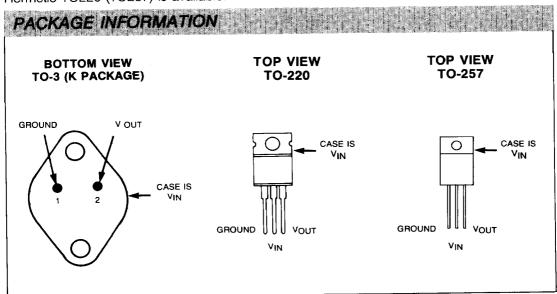
DESCRIPTION

The IP1R17A/IP3R17A and IP1R17/IP3R17 series of fixed three terminal negative regulators are capable of delivering 3 amps of output current, and are available with several convenient output voltages. The A-suffix devices provide 0.01%/V line regulation, 0.5% load regulation, and a ±1% output voltage tolerance at room temperature. Over all specified operating conditions (load, line, power, and temperature), the output voltage is guaranteed not to vary by more than ±3%. Protection features include safe operating area current limiting for the output power transistor, and thermal shutdown. The entire series of regulators is available in a TO-3 package, and the commercial version is also available in a convenient, low cost plastic TO-220 package. For military applications the space saving Hermetic TO220 (TO257) is available.

FEATURES

- 3 Amp output current capability
- ± 1% Output tolerance at room temperature (A suffix)
- 0.01%/V Line regulation
- 0.5% Load regulation
- −5, −5.2, −12, −15 Volt fixed output voltages available
- Short circuit current limit protection
- Safe operating area protection
- Thermal shutdown protection
- Improved version of LM145

4





3 AMP, 3-TERMINAL, FIXED NEGATIVE VOLTAGE REGULATORS

Input Voltage ($V_{OUT} = -5, -5.2, -12, \text{ or } -15V$) 35V

Lead Temperature (Soldering, 10 sec)

300°C

Power Dissipation

Internally Limited

Operating Junction Temperature Range

IP1R17A/IP1R17

00C

-55°C to + 150°C

Storage Temperature Range -65°C to +150°C

IP3R17A/IP3R17 0°C to +125°C

Absolute maximum ratings are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the device should be operated at these limits. The electrical characteristics provide conditions for actual device operation.

ELECTRICAL CHARACTERISTICS

		T		IP1R17A-5/IP3R17A-5			IP1B			
Symbol	Parameter	Conditions (Note 1)		Min	Тур	Max	Min	Тур	Max	Units
	4//10/			-5.05	-5.00	-4.95	-5.15	-5.00	-4.85	V
Vout	Output Voltage	-5mA ≥ IOUT ≥ -3A -8V ≥ VIN ≥ -20V, P ≤ PMAX	•	-5.15		-4.85	-5.25		-4.75	٧
∆Vout	Line Regulation	IOUT = -5mA (Note 2)			3	15		6	30	mV
ΔVIN	Line Hegulation	-7.5V ≥ V _{IN} ≥ -35V	•		6	30	T	12	60	mV
△ VOUT	Load Regulation	-5mA ≥ IOUT ≥ -3A			5	25		10	50	mV
△lout	Load Hegulation	(Note 2)	•		10	50		20	100	mV
IQ	Quiescent Current	IOUT = -5mA	•			5	1		5	mA
△ IQ	Quiescent Current	-5mA ≥ IOUT ≥ -3A	•			10			10	mA
ΔiQ	Change (Load/Line)	IOUT = -5mA, -7.5V ≥ VIN ≥ -35V	•		1	5	1		5	mA
VD	Dropout Voltage	IOUT = -3A, △VOUT = 100mV	•		2.2	3.0		2.2	3.0	v
	Ripple Rejection	IOUT = -1A, f = 120Hz	•	60	80		60	80		dB
-	Thermal Regulation	tpULSE = 20msec, △P = PMAX			0.002	0.01		0.002	0.02	%/W
IPEAK	Peak Output Current (dc)	V _{IN} = -10V	٠	-6.5	-4.5		-6.5	-4.5		А
Isc	Short Circuit	VIN = -10V			-4			-4		Α
'SC	Current	V _{IN} = -35V			-1			-1		Α
en	Output Noise Voltage	10Hz ≤ f ≤ 100kHz			40			40	!	∨ىر
	AVE TC of VOUT									mV
€JC	Thermal Resistance,	K Package			1.5	2.5		1.5	2.5	°C/W
030	Junction to Case	G, T Package			3	4		3	4	°C/W
										°C/W

The • denotes specifications which apply over the full operating junction temperature range. All others apply at TCASE = 25°C unless otherwise specified.

Note 1: Unless otherwise specified, V_{IN} = -10V, and I_{OUT} = -1.5A. Although power dissipation is internally limited, these specifications apply for dissipations up to 30W for the TO-3 package, and for dissipations up to 20W for the TO-220 and TO-257.

Note 2: Load and line regulation are electrically independent and are measured using pulse testing techniques at low duty cycle in order to maintain constant junction temperature. To determine the effects on the output voltage due to device heating refer to the thermal regulation specification.



3 AMP, 3-TERMINAL, FIXED NEGATIVE VOLTAGE REGULATORS

ELECTRICAL CHARACTERISTICS (CONTINUED)

	Parameter			IP1R17A-5.2/IP3R17A-5.2			IP1R17-5.2/IP3R17-5.2			
Symbol		Conditions (Note 1)		Min	Тур	Max	Min	Тур	Max	Units
				-5.25	-5.20	-5.15	-5.35	-5.20	-5.05	٧
Vout	Output Voltage	-5mA ≥ IOUT ≥ -3A -8.2V ≥ VIN ≥ 20V, P ≤ PMAX	•	-5.35		-5.05	-5.45		-4.95	٧
△Vout	Line Regulation	IOUT = -5mA (Note 2)			3	15		6	30	mV
△VIN	Line negulation	-7.7V ≥ V _{IN} ≥ -35V	•		6	30		12	60	mV
△ Vout	Load Regulation	-5mA ≥ IOUT ≥ -3A			5	25		10	50	mV
△lout	Load Regulation	(Note 2)	•		10	50		20	100	mV
IQ	Quiescent Current	IOUT = -5mA	•			5			5	mA
^ 1=	Quiescent Current	-5mA ≥ IOUT ≥ -3A	•		T .	10			10	mA
ΔIQ	Change (Load/Line)	IOUT = -5mA, -7.7V ≥ VIN ≥ -35V	•			5			5	mA
٧D	Dropout Voltage	IOUT = -3A, △VOUT = 100mV	•		2.2	3.0		2.2	3.0	٧
	Ripple Rejection	IOUT = -1A, f = 120Hz	•	60	80		60	80		dB
	Thermal Regulation	tpuLSE = 20msec, △P=PMAX			0.002	0.01		0.002	0.02	%/W
PEAK	Peak Output Current (dc)	VIN = -10V	•	-6.5	-4.5		-6.5	-4.5		Α
100	Short Circuit	VIN = -10V			-4			-4		Α
ISC	Current	VIN = -35V			-1	-		-1		Α
en	Output Noise Voltage	10Hz ≤ f ≤ 100kHz			40			40		μ٧
-	AVE TC of VOUT									mV
0.0	Thermal Resistance,	K Package			1.5	2.5		1.5	2.5	°C/W
⊕ JC	Junction to Case	G, T Package			3	4		3	4	°C/W
			_							°C/W

The • denotes specifications which apply over the full operating junction temperature range. All others apply at TCASE = 25°C unless otherwise specified.

- Note 1: Unless otherwise specified, V_{IN} = -10V, and I_{OUT} = -1.5A. Although power dissipation is internally limited, these specifications apply for dissipations up to 30W for the TO 3 package, and for dissipations up to 20W for the TO 220 and TO-257.
- Note 2: Load and line regulation are electrically independent and are measured using pulse testing techniques at low duty cycle in order to maintain constant junction temperature. To determine the effects on the output voltage due to device heating refer to the thermal regulation specification.

4

3 AMP, 3-TERMINAL, FIXED NEGATIVE VOLTAGE REGULATORS

	Parameter			IP1R17A-12/IP3R17A-12			IP1R1			
Symbol		Conditions (Note 1)		Min	Тур	-11.88	Min -12.36	Typ -12.00	-11.64	Units
				-12.12	-12.00					
Vout	Output Voltage	-5mA ≥ IOUT ≥ -3A -15V ≥ VIN ≥ -27V, P ≤ PMAX	•	-12.36		-11.64	-12.60		-11.40	ν
△Vout	Line Brandadian	IOUT = -5mA (Note 2)			5	30		10	60	mV
ΔVIN	Line Regulation	-14.5V ≥ V _{IN} ≥ -35V	•		10	60		20	120	mV
△ Vout		-5mA ≥ IOUT ≥ -3A			10	60		20	120	mV
△lout	Load Regulation	(Note 2)	•		20	120		40	240	mV
IQ	Quiescent Current	IOUT = -5mA	•			5			5	mA
△ IQ	Quiescent Current	-5mA ≥ IOUT ≥ -3A	•			10			10	mA
	Change (Load/Line)	I _{OUT} = -5mA, -14.5V ≥ V _{IN} ≥-35V	•			5			5	mA
٧D	Dropout Voltage	IOUT = -3A, △VOUT = 250mV	•		2.2	3.0		2.2	3.0	٧
	Ripple Rejection	IOUT = -1A, f = 120Hz	•	52	72		52	72		dB
	Thermal Regulation	tpULSE = 20msec, △P=PMAX			0.002	0.01		0.002	0.02	%/W
IPEAK	Peak Output Current (dc)	V _{1N} = -17V	•	-6.5	-4.5	-	-6.5	-4.5		Α
ISC	Short Circuit	V _{IN} = -17V			-2.5			-2.5		Α
isc	Current	V _{IN} = -35V			-1			-1		Α
en	Output Noise Voltage	10Hz ≤ f ≤ 100kHz			75			75		μV
	AVE TC of VOUT									mV
A	Thermal Resistance,	K Package			1.5	2.5		1.5	2.5	•c/w
o JC	Junction to Case	G, T Package			3	4		3	4	°C/W
				†						°C/W

The • denotes specifications which apply over the full operating junction temperature range. All others apply at T_{CASE} = 25°C unless otherwise specified.

- Note 1: Unless otherwise specified, V_{IN} = -17V, and I_{OUT} = -1.5A. Although power dissipation is internally limited, these specifications apply for dissipations up to 30W for the TO 3 package, and for dissipations up to 20W for the TO 220 and TO-257.
- Note 2: Load and line regulation are electrically independent and are measured using pulse testing techniques at low duty cycle in order to maintain constant junction temperature. To determine the effects on the output voltage due to device heating refer to the thermal regulation specification.



3 AMP, 3-TERMINAL, FIXED NEGATIVE VOLTAGE REGULATORS

ELECTRICAL CHARACTERISTICS (CONTINUED)

				IP1R17A-15/IP3R17A-15			IP1R1			
Symbol	Parameter	Conditions (Note 1)		Min	Тур	Max	Min	Тур	Max	Units
				-15.15	-15.00	-14.85	-15.45	-15.00	-14.55	٧
Vout	Output Voltage	-5mA ≥ IOUT ≥ -3A -18V ≥ V _{IN} ≥ -30V, P < P _{MAX}	•	-15.45		-14.55	-15.75		-14.25	v
△Vout	Line Regulation	IOUT = -5mA (Note 2)			8	40		16	80	mV
$\triangle V_{IN}$	Line negulation	-17.5V ≥ V _{IN} ≥ -35V	•		16	80		32	160	m۷
△ Vout	Load Regulation	-5mA ≥ IOUT ≥ -3A			16	80		32	160	mV
△lout	Load Regulation	(Note 2)	•		32	160		64	320	mV
IQ	Quiescent Current	IOUT = -5mA	•			5			5	mA
	Quiescent Current	-5mA ≥ IOUT ≥ -3A	•		1.00	10			10	mA
∆ IQ	Change (Load/Line)	IOUT = -5mA, -17.5V ≥ VIN ≥ -35V	•			5			5	mA
VD	Dropout Voltage	IOUT = -3A, △VOUT = 300mV	•		2.2	3.0		2.2	3.0	V
	Ripple Rejection	IOUT = -1A, f = 120Hz	•	50	70	_	50	70		dB
	Thermal Regulation	tPULSE = 20msec, △P=PMAX			0.002	0.01		0.002	0.02	%/W
IPEAK	Peak Output Current (dc)	V _{IN} = -20V	•	-6.5	-4.5		-6.5	-4.5		Α
loo	Short Circuit	V _{IN} = -20V			-2.3			-2.3		Α
ISC	Current	V _{IN} = -35V			-1			-1		Α
en	Output Noise Voltage	10Hz ≤ f ≤ 100kHz			90			90		υV
	AVE TC of VOUT									mV
e)c	Thermal Resistance,	K Package			1.5	2.5		1.5	2.5	°C/W
91C	Junction to Case	G, T Package			3	4		3	4	°C/W
										°C/W

The • denotes specifications which apply over the full operating junction temperature range. All others apply at TCASE = 25°C unless otherwise specified.

- Note 1: Unless otherwise specified, V_{IN} = -20V, and I_{OUT} = -1.5A. Although power dissipation is internally limited, these specifications apply for dissipations up to 30W for the TO 3 package, and for dissipations up to 20W for the TO -220 and TO-257
- Note 2: Load and line regulation are electrically independent and are measured using pulse testing techniques at low duty cycle in order to maintain constant junction temperature. To determine the effects on the output voltage due to device heating refer to the thermal regulation specification.

ORDER INFORMATION

Part Number	
IP1R17AK-XX,	IP1R17K-XX
IP1R17AG-XX,	IP1R17G-XX
IP3R17AK-XX,	IP3R17K-XX
IP3R17AT-XX	IP3R17T-XX

Temperature Range
-55°C to +150°C
-55°C to +150°C
0°C to +125°C
0°C to +125°C

Package TO-3 TO-257 TO-3 TO-220

