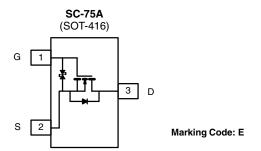




# N-Channel 60 V (D-S) MOSFET

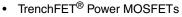
PRODUCT SUMMARY							
V <sub>DS(min.)</sub> (V)	$R_{DS(on)}(\Omega)$	V <sub>GS(th)</sub> (V)	I <sub>D</sub> (mA)				
60	1.25 at V <sub>GS</sub> = 10 V	1 to 2.5	330				



Ordering Information: Si1022R-T1-GE3 (Lead (Pb)-free and Halogen-free)

#### **FEATURES**





• Low On-Resistance: 1.25  $\Omega$ 

Low Threshold: 2.5 V

Low Input Capacitance: 30 pFFast Switching Speed: 25 ns

· Low Input and Output Leakage

Miniature Package

ESD Protected: 2000 V

• Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- · Battery Operated Systems
- · Solid State Relays

#### **BENEFITS**

- · Low Offset Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Error Voltage
- Small Board Area

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)							
Parameter		Symbol	Limit	Unit			
Drain-Source Voltage		V <sub>DS</sub>	60	V			
Gate-Source Voltage		V <sub>GS</sub>	± 20	V			
Ocaliana Paris Commula	T <sub>A</sub> = 25 °C	I_	330	mA			
Continuous Drain Current <sup>a</sup>	T <sub>A</sub> = 85 °C	- I <sub>D</sub>	240				
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	650				
Davier Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	250	mW			
Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C	ט י	130				
Thermal Resistance, Maximum Junction-to-Ambienta		R <sub>thJA</sub>	500	°C/W			
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C			

#### Notes:

a. Surface mounted on FR4 board, power applied for  $t \le 10 \text{ s}$ .



ROHS COMPLIANT HALOGEN FREE

# Vishay Siliconix



<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 10 \mu\text{A}$	60			V		
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 0.25 \text{ mA}$	1		2.5			
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}$			± 150			
Gate-Body Leakage	I <sub>GSS</sub>	T <sub>J</sub> = 85 °C			± 500			
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 5 \text{ V}$			± 20	nA		
		$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}$			10			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	T <sub>J</sub> = 85 °C			100			
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}$	500			mA		
On-State Drain Current		$V_{DS} = 7.5 \text{ V}, V_{GS} = 10 \text{ V}$	800			IIIA		
	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 200 \text{ mA}$			3.0			
Drain-Source On-State Resistance <sup>a</sup>		T <sub>J</sub> = 125 °C			5.0	Ω		
Drain-Source On-State Resistance		$V_{GS} = 10 \text{ V}, I_D = 500 \text{ mA}$			1.25	52		
		T <sub>J</sub> = 125 °C			2.25			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_{D} = 200 \text{ mA}$	100			mS		
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$V_{GS} = 0 \text{ V, } I_{S} = 200 \text{ mA}$			1.3	<b>V</b>		
Dynamic <sup>b</sup>								
Input Capacitance	C <sub>iss</sub>			30				
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		6		pF		
Reverse Transfer Capacitance	C <sub>rss</sub>			2.5				
Gate Charge	Qg	$V_{DS} = 10 \text{ V}, I_D = 250 \text{ mA}, V_{GS} = 4.5 \text{ V}$			0.6	nC		
Switching <sup>b, c</sup>								
Turn-On Time	t <sub>(on)</sub>	$V_{DD} = 30 \text{ V}, R_{L} = 150 \Omega,$			25	ns		
Turn-Off Time	t <sub>(off)</sub>	$I_D = 200 \text{ mA}, V_{GEN} = 10 \text{ V}, R_g = 10 \Omega$			35	110		

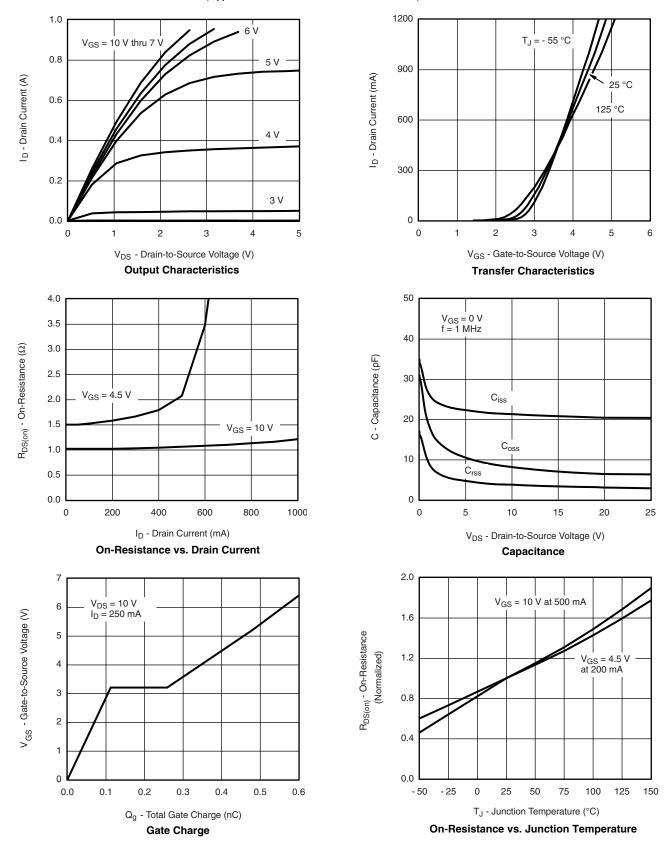
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.
- b. For DESIGN AID ONLY, not subject to production testing.
- c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



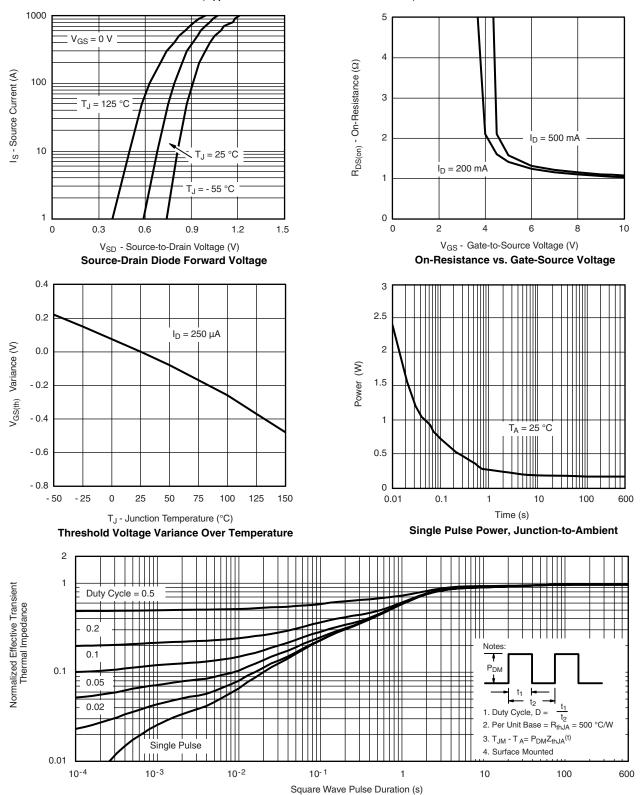
## **TYPICAL CHARACTERISTICS** ( $T_A = 25$ °C, unless otherwise noted)



## Vishay Siliconix



### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



#### Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?71331">www.vishay.com/ppg?71331</a>.



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