Silicon Controlled Rectifiers

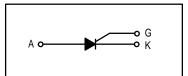
Reverse Blocking Triode Thyristors

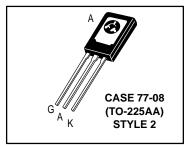
. . . PNPN devices designed for high volume consumer applications such as temperature, light, and speed control; process and remote control, and warning systems where reliability of operation is important.

- · Passivated Surface for Reliability and Uniformity
- Power Rated at Economical Prices
- · Practical Level Triggering and Holding Characteristics
- Flat, Rugged, Thermopad Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Recommended Electrical Replacement for C106

2N6240

SCRs 4 AMPERES RMS 400 VOLTS





MAXIMUM RATINGS ($T_J = 25^{\circ}C$ unless otherwise noted.)

Rating	Symbol	Value	Unit
*Repetitive Peak Forward and Reverse Blocking Voltage(1) (1/2 Sine Wave)	V _{DRM} or		Volts
$(R_{GK} = 1000 \text{ ohms}, T_{C} = -40 \text{ to } +110^{\circ}\text{C})$	VRRM	400	
*Non-repetitive Peak Reverse Blocking Voltage (1/2 Sine Wave, R _{GK} = 1000 ohms,	VRSM		Volts
$T_{C} = -40^{\circ} \text{ to } +110^{\circ}\text{C}$		450	
*Average On–State Current	I _{T(AV)}		Amps
$(T_C = -40 \text{ to } + 90^{\circ}\text{C})$	(,	2.6	
$(T_C = +100^{\circ}C)$		1.6	
*Surge On–State Current	ITSM		Amps
$(1/2 \text{ Sine Wave, } 60 \text{ Hz, T}_{C} = +90^{\circ}\text{C})$		25	
$(1/2 \text{ Sine Wave, } 1.5 \text{ ms, } T_C = +90^{\circ}C)$		35	
Circuit Fusing	I ² t	2.6	A ² s
(t = 8.3 ms)			
*Peak Gate Power	P _{GM}	0.5	Watts
(Pulse Width = 10 μ s, T _C = 90°C)			

^{*}Indicates JEDEC Registered Data.

(continued)



^{1.} V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

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MAXIMUM RATINGS — continued ($T_J = 25$ °C unless otherwise noted.)

Rating	Symbol	Value	Unit
*Average Gate Power (t = 8.3 ms, T _C = 90°C)	P _{G(AV)}	0.1	Watt
Peak Forward Gate Current	I _{GM}	0.2	Amp
Peak Reverse Gate Voltage	[∨] RGM	6	Volts
*Operating Junction Temperature Range	ТJ	-40 to +110	°C
*Storage Temperature Range	T _{stg}	-40 to +150	°C
Mounting Torque(1)	_	6	in. lb.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Min	Max	Unit
*Thermal Resistance, Junction to Case	$R_{ heta JC}$	_	3	°C/W
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	_	75	°C/W

^{*}Indicates JEDEC Registered Data.

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ and $R_{GK} = 1000$ ohms unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
*Peak Forward or Reverse Blocking Current (VAK = Rated VDRM or VRRM) TC = 25°C TC = 110°C	IDRM, IRRM	_	_	10 200	μΑ μΑ
*Peak Forward "On" Voltage (I _{TM} = 8.2 A Peak, Pulse Width = 1 to 2 ms, 2% Duty Cycle)	V _{TM}	_	_	2.2	Volts
Gate Trigger Current (Continuous dc) ⁽²⁾ (V _{AK} = 12 Vdc, R _L = 24 Ohms) *(V _{AK} = 12 Vdc, R _L = 24 Ohms, T _C = -40°C)	lGT	_	_	200 500	μΑ
Gate Trigger Voltage (Continuous dc) (Source Voltage = 12 V, R _S = 50 Ohms) *(V _{AK} = 12 Vdc, R _L = 24 Ohms, T _C = -40°C)	VGT	_	_	1	Volts
Gate Non-Trigger Voltage (V _{AK} = Rated V _{DRM} , R _L = 100 Ohms, T _C = 110°C)	V _{GD}	0.2	_	_	Volts
Holding Current $(V_{AK} = 12 \text{ Vdc}, I_{GT} = 2 \text{ mA})$ $T_{C} = 25^{\circ}\text{C}$ *(Initiating On–State Current = 200 mA) $T_{C} = -40^{\circ}\text{C}$	lн	_	_	5 10	mA
*Total Turn–On Time (Source Voltage = 12 V, R_S = 6 k Ohms) (I_{TM} = 8.2 A, I_{GT} = 2 mA, Rated V_{DRM}) (Rise Time = 20 ns, Pulse Width = 10 μ s)	^t gt	_	2	_	μs
Forward Voltage Application Rate $(V_D = Rated V_{DRM}, T_C = 110^{\circ}C)$	dv/dt	_	10	_	V/µs

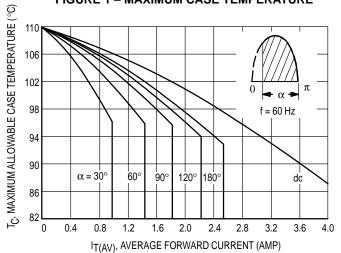
^{*}Indicates JEDEC Registered Data.

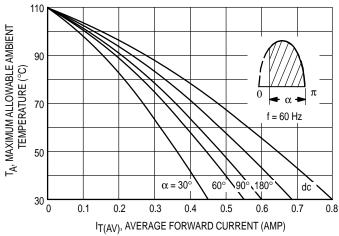
2. Measurement does not include $\ensuremath{R_{GK}}$ current.

^{1.} Torque rating applies with use of compression washer (B52200F006 or equivalent). Mounting torque in excess of 6 in. lb. does not appreciably lower case-to-sink thermal resistance. Anode lead and heatsink contact pad are common. (See AN–209 B) For soldering purposes (either terminal connection or device mounting), soldering temperatures shall not exceed +200°C. For optimum results an activated flux (oxide removing) is recommended.

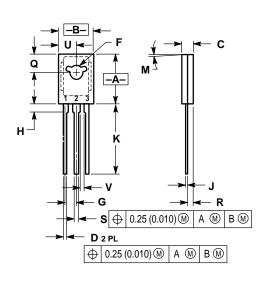


FIGURE 2 - MAXIMUM AMBIENT TEMPERATURE





PACKAGE DIMENSIONS



STYLE 2: PIN 1. CATHODE 2. ANODE 3. GATE NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.425	0.435	10.80	11.04	
В	0.295	0.305	7.50	7.74	
C	0.095	0.105	2.42	2.66	
D	0.020	0.026	0.51	0.66	
F	0.115	0.130	2.93	3.30	
G	0.094	0.094 BSC		BSC	
Н	0.050	0.095	1.27	2.41	
7	0.015	0.025	0.39	0.63	
K	0.575	0.655	14.61 5°	16.63	
M	5°	5° TYP		TYP	
ø	0.148	0.158	3.76	4.01	
R	0.045	0.055	1.15	1.39	
S	0.025	0.035	0.64	0.88	
5	0.145	0.155	3.69	3.93	
٧	0.040		1.02		

CASE 77-08 (TO-225AA)

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