

SP6T High Power 2.5V Antenna Switch

Quad-Band GSM850/GSM900/DCS/PCS

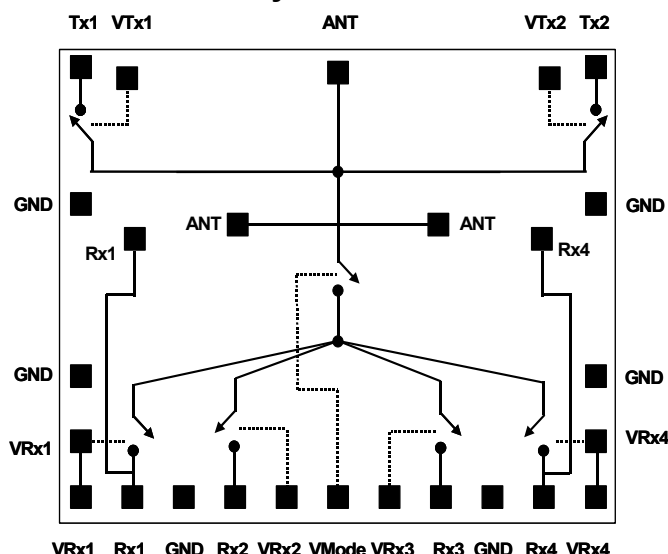
Features:

- Unpackaged PHEMT GaAs MMIC Die
- High Linearity, Asymmetric Design
- Excellent Harmonic Performance
 - -79 dBc 2nd Harmonic at 1GHz / +35dBm
 - -90 dBc 2nd Harmonic at 2GHz / +33 dBm
- High Tx to Rx Isolation: -40dB typ
- Low Insertion Loss:
 - Tx Path 0.35dB typ at 1 GHz
 - Rx Path 0.7 dB typ at 1 GHz
- Low Control Voltage Operation to <2.0V
- Very Low Control Current: <100 μ A Typ
- Flexible Bonding Options

Applications:

- Quad Band for GSM Handset Antenna Switch Modules (ASM) and Front End Modules (FEM)
- Dual and Tri-Band GSM Capable with unused ports

Schematic Layout:



Description:

TriQuint's TQ4M4006 is a high power antenna switch die in a single pole six throw (SP6T) configuration. The die utilizes TriQuint's PHEMT MMIC switch process to provide optimized performance for use in GSM Quad Band Modules. PHEMT Switches are a very low DC current replacement for classic PIN diode based switches. The die is ideally suited to applications where the antenna of a GSM handset is to be routed to the two separate Tx ports and up to four separate Rx inputs. Use of 2 or 3 of the Rx ports allows the same switch die to be used in dual and tri-band GSM applications. The TQ4M4006 finds applications in Antenna Switch modules combined with Tx Low Pass Filters and Rx SAW Filters. It is also suitable for use in Front End PA Modules that integrate PA, CMOS controller/decoder, and filters together. Robust operation is available with stable harmonic performance down less than 2.0V of control voltage and over a wide operating power range.

Electrical Performance: Ta = 25°C, Zo=50 Ohms, Vcontrol = 0V / 2.5V¹

Parameter	Test Conditions	Units	Min	Typ	Max
Tx Insertion Loss	GSM850/900	dB		0.35	
Tx Insertion Loss	DCS/PCS	dB		0.45	
Rx Insertion Loss	GSM850/900	dB		0.7	
Rx Insertion Loss	DCS/PCS	dB		0.9	
Isolation Rx to Rx	GSM850/950, PCS/DCS ; Rx On	dB		-30	
Isolation Tx to Rx	GSM850/950, PCS/DCS ; Tx On	dB		-40	
Isolation Tx to Tx	GSM850/950 PCS/DCS ; Tx On	dB		-32	
2 nd Harmonic	GSM850/900, Pin = +35dBm	dBc		-79	
2 nd Harmonic	DCS/PCS, Pin = +33 dBm	dBc		-90	
3 rd Harmonic	GSM850/900, Pin = +35dBm	dBc		-79	
3 rd Harmonic	DCS/PCS, Pin = +33 dBm	dBc		-80	

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Electrical Performance (continued): Ta = 25°C, Zo=50 Ohms, Vcontrol = 0V / 2.5V¹

Parameter	Test Conditions	Units	Min	Typ	Max
IIP3	Two tones; +20dBm each; 1MHz spacing; 1950 MHz	dBm		+55	
Return Loss	0.5 to 2.0GHz	dB		-20	
Leakage Current	-	μA			100
Trise, TFall	10% to 90% RF, 90% to 10% RF	μS			1
Ton, Toff	50% control to 90% RF, and 50% control to 10% RF	μS			1

Truth Table: ^{2, 3}

VTx1	VTx2	Vmode	VRx1	VRx2	VRx3	VRx4	ANT-Tx1	ANT-Tx2	ANT-Rx1	ANT-Rx2	ANT-Rx3	ANT-Rx4
1	0	0	0	0	0	0	On	Off	Off	Off	Off	Off
0	1	0	0	0	0	0	Off	On	Off	Off	Off	Off
0	0	1	1	0	0	0	Off	Off	On	Off	Off	Off
0	0	1	0	1	0	0	Off	Off	Off	On	Off	Off
0	0	1	0	0	1	0	Off	Off	Off	Off	On	Off
0	0	1	0	0	0	1	Off	Off	Off	Off	Off	On

Absolute Maximum Ratings⁴:

Parameter	Absolute Maximum
Max Input Power	+38dBm
Control Voltage	+/-5V
Operating Temp	-40°C to +85°C
Storage Temp	-65°C to +150°C

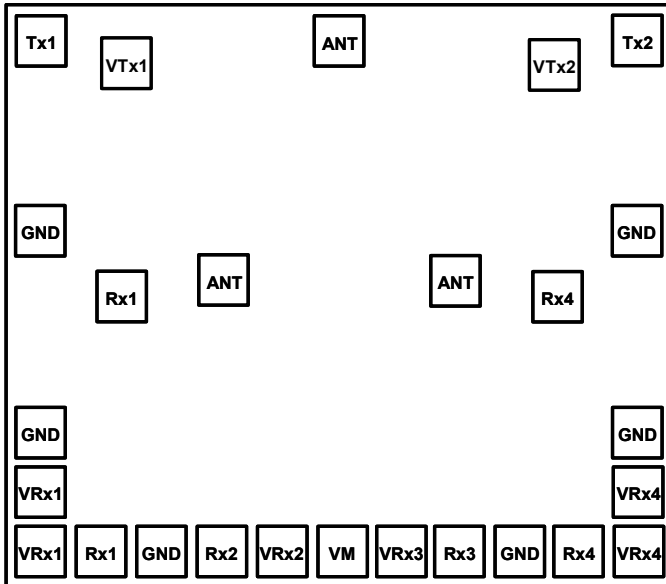
Notes:

1. External DC blocking capacitors are required at all RF ports
2. State 1 = +2.0V to +5.0V, State 0 = 0V to +0.2V
3. Differential voltage from State 1 to State 2 must be a minimum of 2.0V
4. Exceeding any parameter either individually or in combination may cause permanent damage.

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Die Layout:



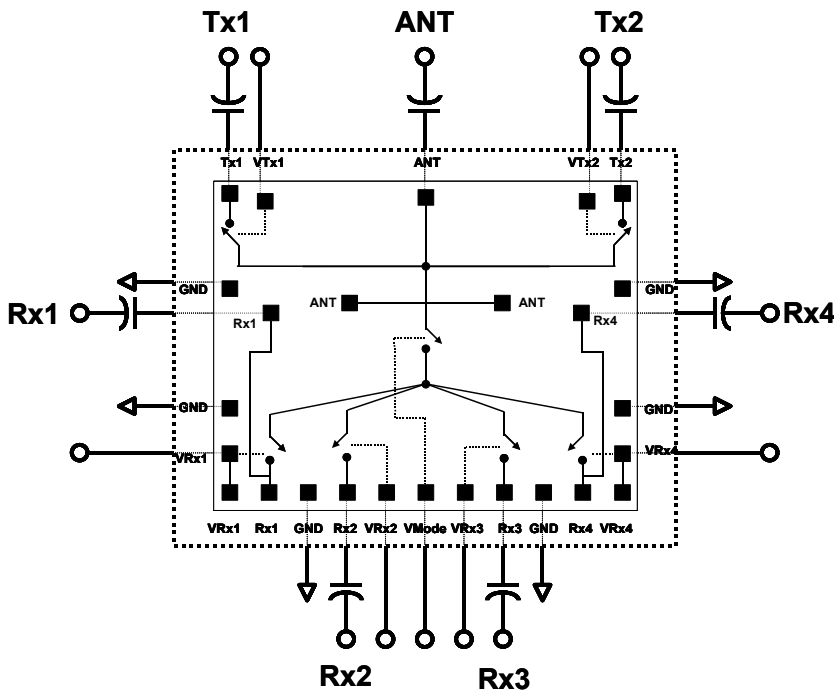
Pad Number	Pad Name	Description
1	Tx1	Transmit RF Port 1
2	VTx1	Control Tx1
3	ANT	Main RF ANT Port
4	VTx2	Control Tx2
5	Tx2	Transmit RF Port 2
6	GND	Ground
7	Rx4	Redundant Receive RF Port 4
8	ANT	Redundant RF ANT
9	GND	Ground
10	VRx4	Redundant Control
11	VRx4	Control Rx4
12	Rx4	Receive RF Port 4
13	GND	Ground
14	Rx3	Receive RF Port 3
15	VRx3	Control Rx3
16	VM	Control TX/RX Mode
17	VRx2	Control Rx2
18	Rx2	Receive RF Port 2
19	GND	Ground
20	Rx1	Receive RF Port 1
21	VRx1	Control Rx1
22	VRx1	Redundant Control
23	GND	Ground
24	ANT	Redundant RF ANT
25	Rx1	Redundant Receive RF Port 1
26	GND	Ground

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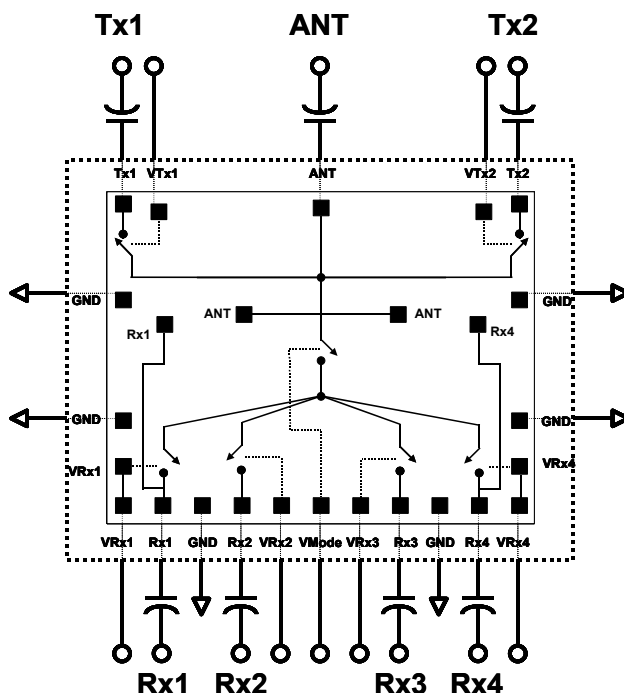
Flexible Bonding Configurations:

External DC Blocking Capacitors are required on all RF ports.
Three Alternate Antenna Bonding Pads



Rx1, Rx4, VRx1 and VRx4
Bonded from Sides

(as used on TriQuint
Application Board)



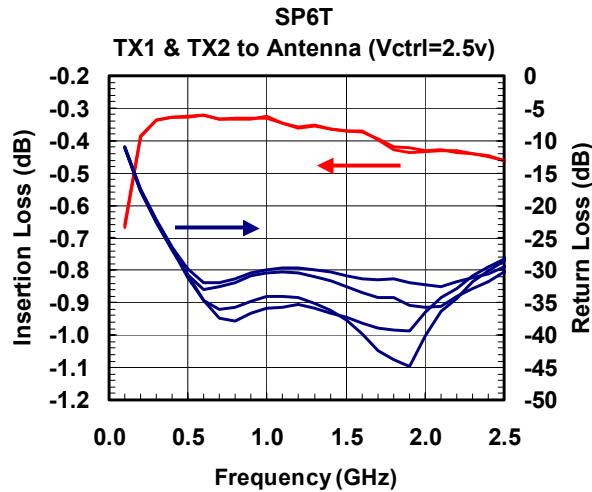
All Rx Control Lines and Rx Ports
Bonded off Lower Edge

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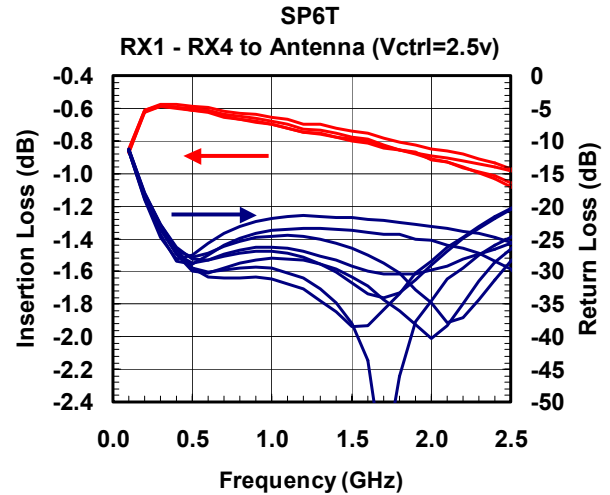
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Typical Performance Curves:

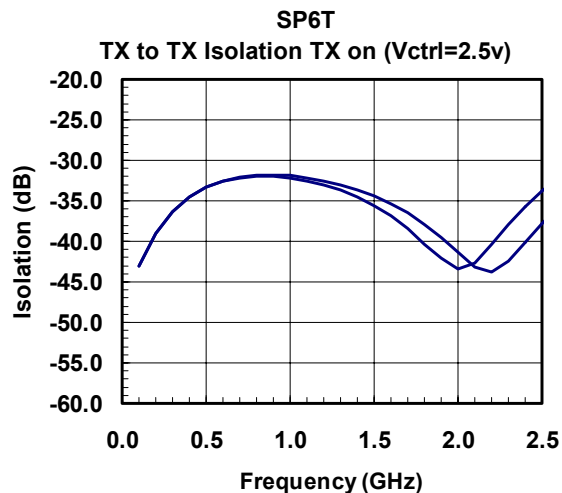
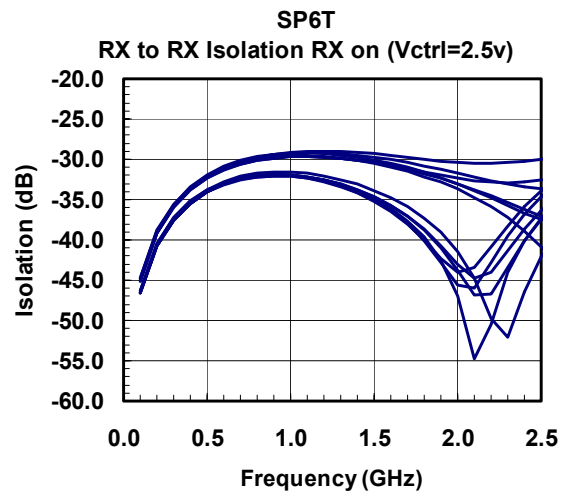
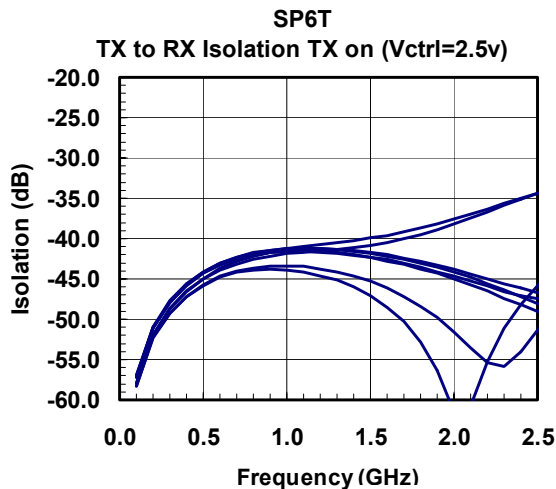
Tx Insertion Loss and Return Loss



Rx Insertion Loss and Return Loss



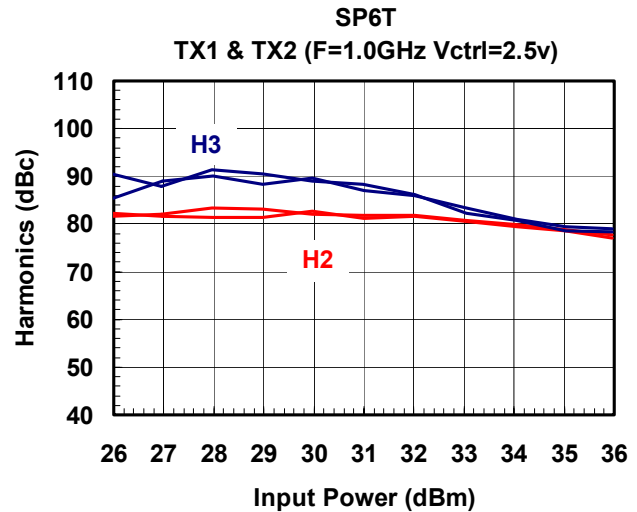
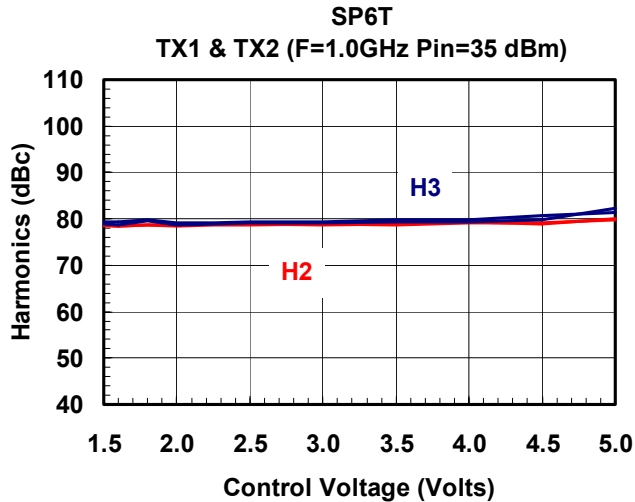
Isolation Curves



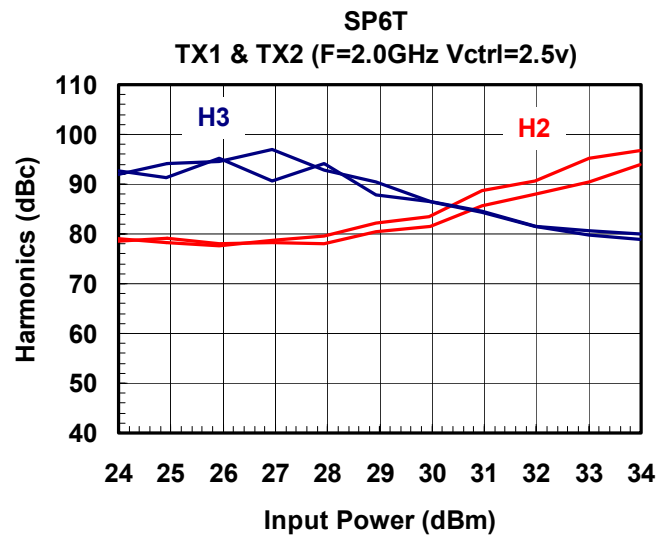
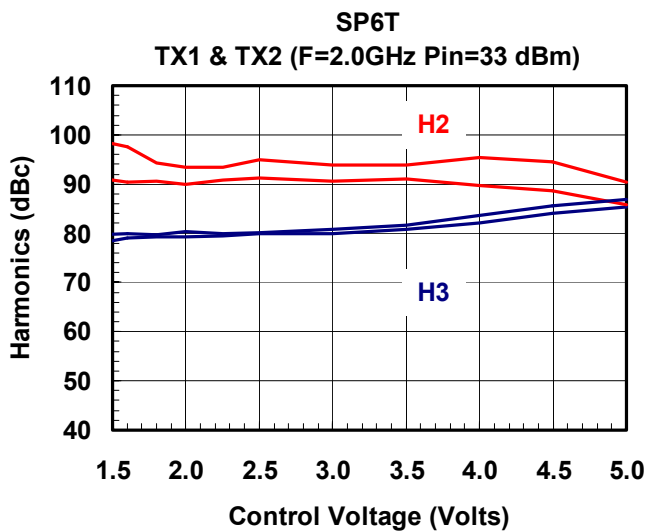
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1GHz 2nd and 3rd Harmonics



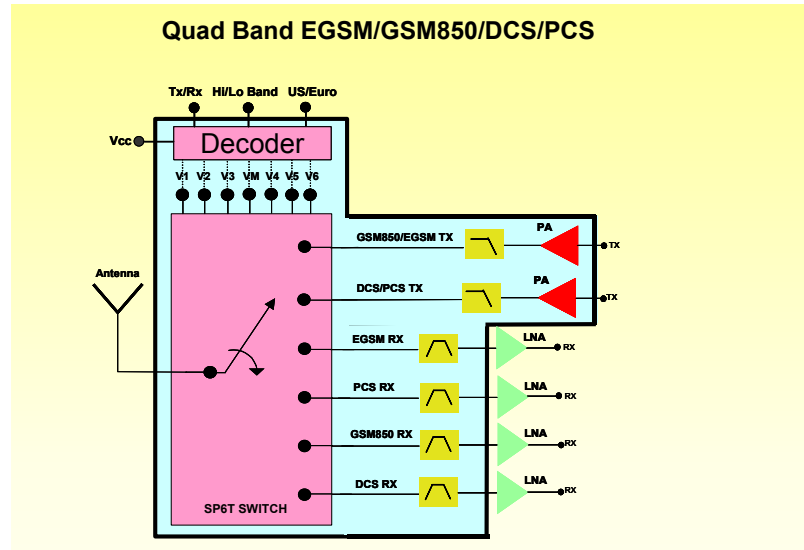
2 GHz 2nd and 3rd Harmonics



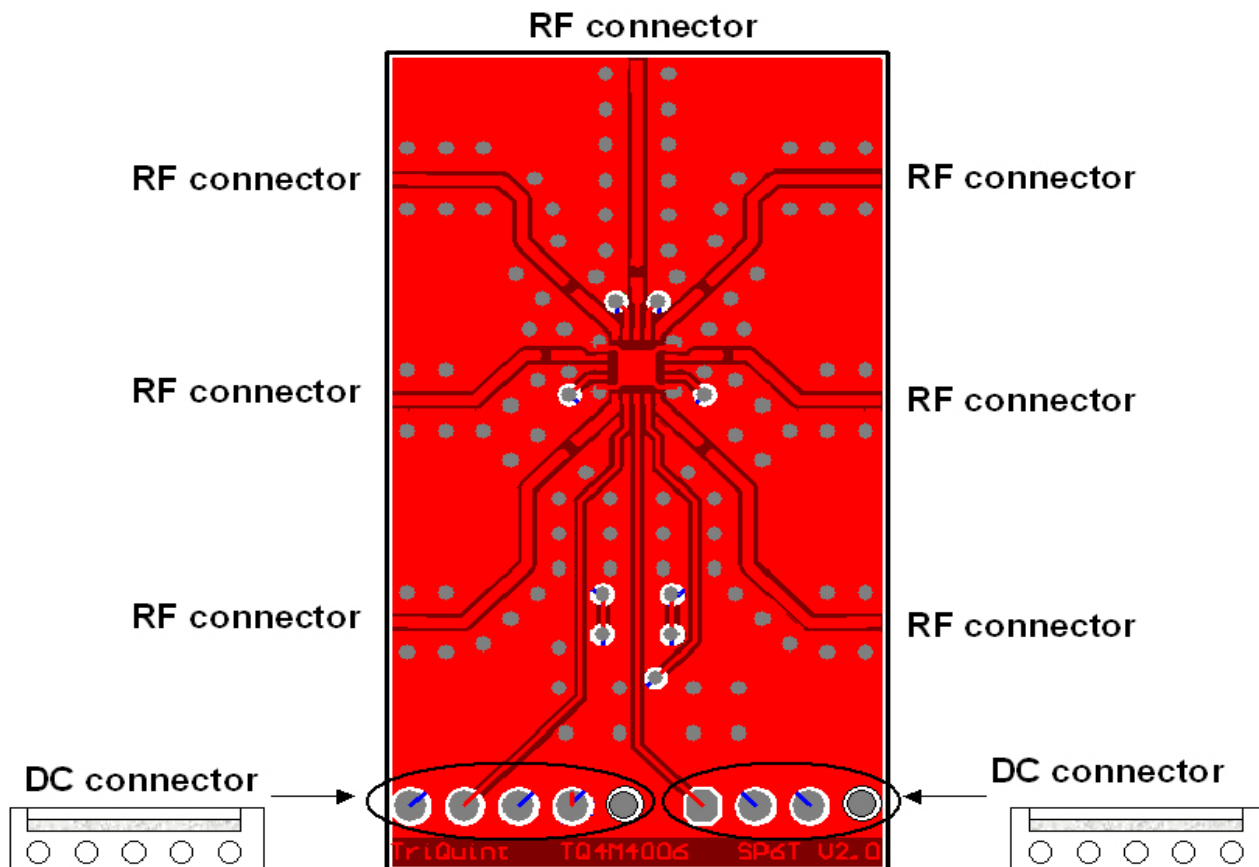
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Application Example



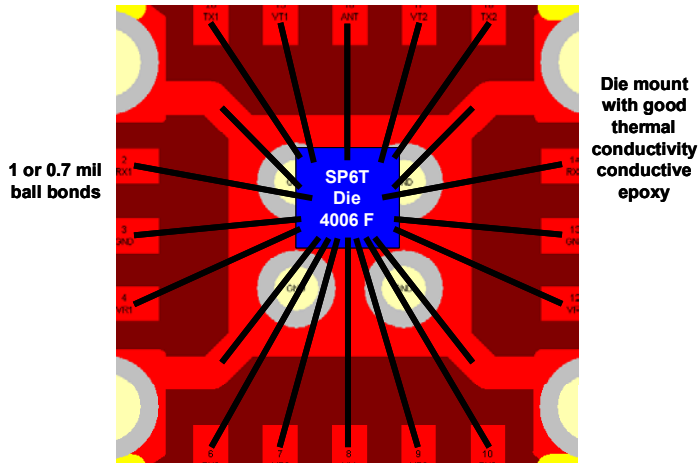
Application Board



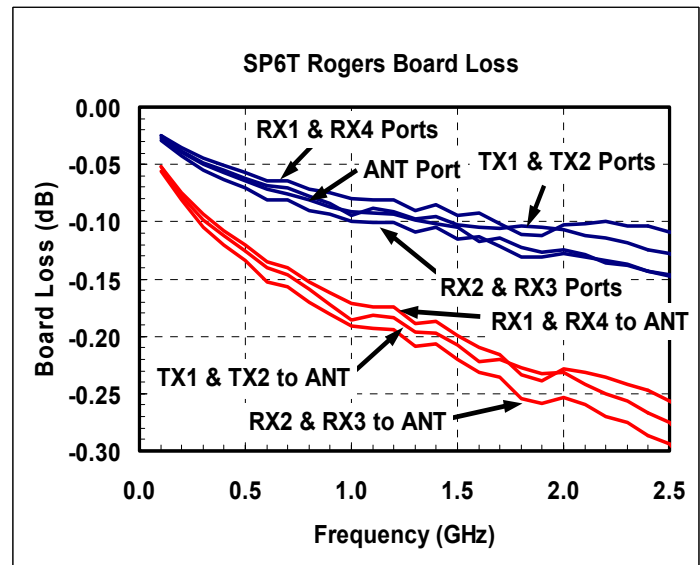
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Bonding Configuration:



Application Board Loss De-Embedding Curve:



Part Ordering Information:

Type	Marking	Package
TQ4M4006	(N/A)	None- Bare Die

For latest specifications, additional product information, worldwide sales and distribution locations, and information about TriQuint:

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