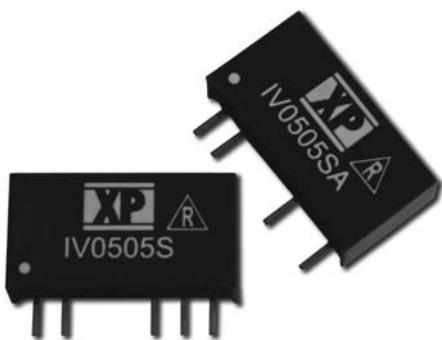


**IV Series**

- Single or Dual Output
- SIP Package
- DIP Package
- 3000 VDC Isolation
- Short Circuit Protection
- -40 °C to +85 °C Operation
- Industry Standard Pinout

**Specification****Input**

Input Voltage Range	• Nominal ±10%
Input Current (no load)	• See table
Input Reflected Ripple	• 200 mV pk-pk
Input Reverse Voltage Protection	• None

**Output**

Output Voltage	• See table
Minimum Load	• None
Line Regulation	• 1.2%/1% $\Delta$ Vin
Load Regulation	• ±8% 20-100% load change
Setpoint Accuracy	• ±5%
Ripple & Noise	• 100 mV pk-pk
Temperature Coefficient	• 0.02%/°C
Short Circuit Protection	• 1 s max
Maximum Capacitive Load	• ±220 $\mu$ F

**General**

Efficiency	• See table
Isolation Voltage	• 3000 VDC minimum
Isolation Resistance	• 10 <sup>9</sup> $\Omega$
Isolation Capacitance	• 60pF typical
Switching Frequency	• 80 kHz typical

**Environmental**

Operating Temperature	• -40 to +85 °C
Storage Temperature	• -40 to +125 °C
Case Temperature	• 100 °C max
Cooling	• Free-air convection

**Notes**

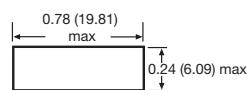
1. For dual output, delete suffix 'A', and split current equally between rails.
2. For DIP package, replace 'S' in part number with 'D'.
3. All dimensions in inches (mm).

Input Voltage	No Load Input Current	Output Voltage	Output Current	Efficiency	Model Number <sup>[1,2]</sup>
5 VDC	22 mA	5.0 V	200 mA	78%	IV0505SA†
	22 mA	9.0 V	112 mA	78%	IV0509SA
	23 mA	12.0 V	84 mA	78%	IV0512SA†
	25 mA	15.0 V	66 mA	80%	IV0515SA†
	30 mA	24.0 V	42 mA	80%	IV0524SA
12 VDC	16 mA	5.0 V	200 mA	70%	IV1205SA†
	15 mA	9.0 V	112 mA	80%	IV1209SA
	20 mA	12.0 V	84 mA	75%	IV1212SA†
	15 mA	15.0 V	66 mA	80%	IV1215SA†
	15 mA	24.0 V	42 mA	80%	IV1224SA
24 VDC	10 mA	5.0 V	200 mA	78%	IV2405SA
	8 mA	9.0 V	112 mA	75%	IV2409SA
	8 mA	12.0 V	84 mA	75%	IV2412SA
	8 mA	15.0 V	66 mA	78%	IV2415SA
	9 mA	24.0 V	42 mA	78%	IV2424SA

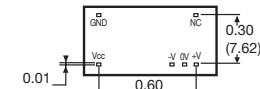
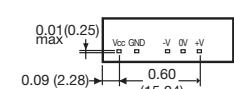
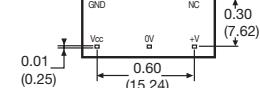
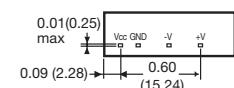
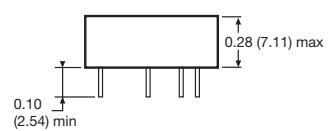
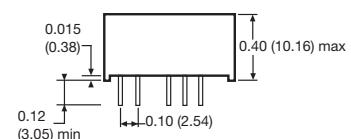
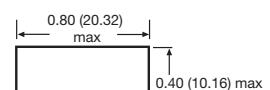
† Available from Farnell InOne. See pages 236-237

**Mechanical Details**

SIP Package



DIP Package



Single Output

Single Output

Dual Output

Dual Output