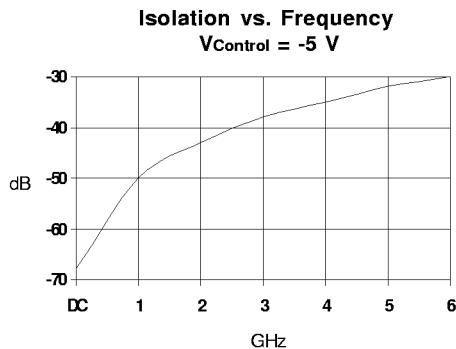


Product Description

Stanford Microdevices' SSW-124 is a high performance Gallium Arsenide Field Effect Transistor MMIC switch housed in a low-cost surface-mountable 8-pin ceramic package.

This single-pole, double-throw, non-reflective switch consumes less than 50uA and operates at -5V and 0V for control bias. Its high isolation and low insertion loss, makes it ideal for T/R switching in analog and digital wireless communication systems.

The die is fabricated using 0.5 micron FET process with gold metallization and silicon nitride passivation to achieve excellent performance and reliability.



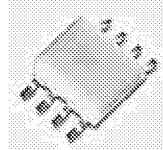
Electrical Specifications at Ta = 25°C

Symbol	Parameters: Test Conditions		Units	Min.	Typ.	Max.
Ins	Insertion Loss	f = 0.05-2.0 GHz	dB		0.7	1.1
		f = 2.00-4.0 GHz	dB		1.0	1.5
		f = 4.00-6.0 GHz	dB		1.4	
Isol	Isolation	f = 0.05-2.0 GHz	dB	40	50	
		f = 2.00-4.0 GHz	dB	30	40	
		f = 4.00-6.0 GHz	dB	25	30	
VSW Ron	Input & Output VSW R (on or low loss state)	f = 0.05-2.0 GHz			1.15	
		f = 2.00-4.0 GHz			1.25	
		f = 4.00-6.0 GHz			1.50	
VSW Roff	Input & Output VSW R (off or isolated state)	f = 0.05-1.0 GHz			1.15	
		f = 1.00-2.0 GHz			1.25	
		f = 2.00-4.0 GHz			1.50	
P1dB	Output Power at 1dB Compression f = 0.5-6.0 GHz	V = -5V	dBm		+26	
		V = -8V	dBm		+29	
TOIP	Third Order Intercept Point f = 0.5-6.0 GHz	V = -5V	dB		+45	
		V = -8V	dB		+48	
Id	Device Current		uA		40	
Isw	Switching Speed 50% control to 10% / 90% RF		nsec		3	

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SSW-124

DC-6 GHz High Isolation GaAs MMIC SPDT Switch



Product Features

- High Isolation: 42dB at 2GHz, 30dB at 6GHz
- Low DC Power Consumption
- Non-reflective
- Broadband Performance - True DC Operation
- Low Cost Surface-Mountable Ceramic Package

Applications

- Analog/Digital Wireless System
- Spread Spectrum
- GPS

SSW-124 DC-6 GHz Absorptive SPDT GaAs Switch

Absolute Maximum Ratings

RF Input Power	2W Max>500MHz
Control Voltage	-10V
Operating Temperature	-45C to +85C
Storage Temperature	-65C to +150C
Thermal Resistance	20 deg C/W

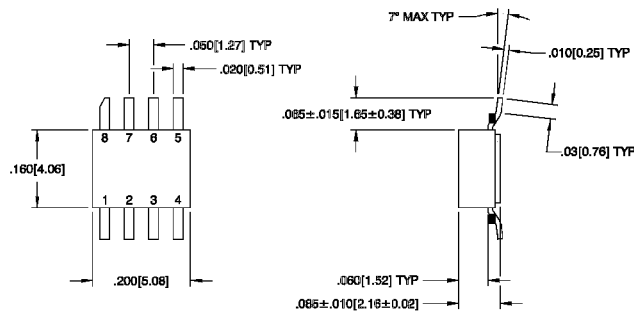
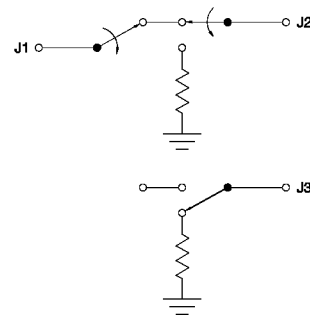
Truth Table

V1	V2	J1-J2	J1-J3
0	-5	Low Loss	Isolation
-5	0	Isolation	Low Loss

Pin Out

Pin	Function
1	GND
2	J1
3	GND
4	GND
5	J2
6	V1
7	V2
8	J3

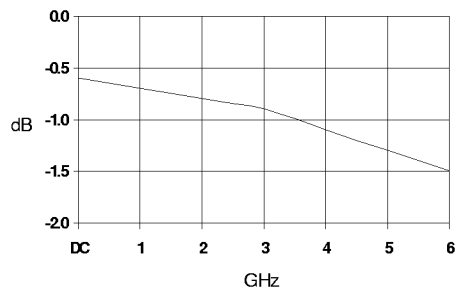
Switch Schematic



Pin numbers shown for reference only, not marked on part

Insertion Loss vs. Frequency

V_{Control} = -5 V



On Port Input/Output VSWR vs. Frequency

V_{Control} = -5 V

