TOSHIBA Transistor Silicon PNP Epitaxial Type

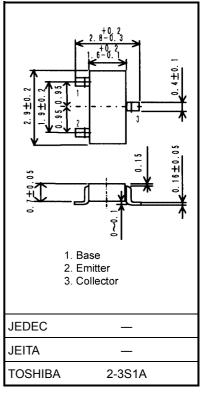
2SA2058

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain: $h_{FE} = 200$ to 500 (I_C = -0.2 A)
- Low collector-emitter saturation voltage: V_{CE} (sat) = -0.19 V (max)
- High-speed switching: $t_f = 25 \text{ ns}$ (typ.)

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	-20	V	
Collector-emitter voltage		V _{CEO}	-10	V	
Emitter-base voltage		V _{EBO}	-7	V	
Collector current	DC	Ι _C	-1.5	A	
	Pulse	I _{CP}	-2.5		
Base current		Ι _Β	-150	mA	
Collector power dissipation	DC	P _C	500	mW	
	t = 10 s	(Note)	750		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 0.01 g (typ.)

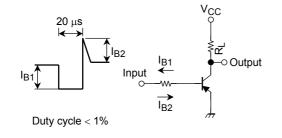
Note: Mounted on FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current		I _{CBO}	$V_{CB}=-20~V,~I_{E}=0$			-100	nA	
Emitter cut-off current		I _{EBO}	$V_{EB} = -7 \text{ V}, \text{ I}_{C} = 0$			-100	nA	
Collector-emitter breakdown voltage		V (BR) CEO	$I_{C} = -10 \text{ mA}, I_{B} = 0$	-10			V	
DC current gain		h _{FE} (1)	$V_{CE} = -2 \text{ V}, \text{ I}_{C} = -0.2 \text{ A}$	200		500		
		h _{FE} (2)	$V_{CE} = -2 \text{ V}, \text{ I}_{C} = -0.6 \text{ A}$	125				
Collector-emitter saturation voltage		V _{CE (sat)}	$I_{C} = -0.6 \text{ A}, I_{B} = -20 \text{ mA}$			-0.19	V	
Base-emitter saturation voltage		V _{BE (sat)}	$I_{C} = -0.6 \text{ A}, I_{B} = -20 \text{ mA}$			-1.10	V	
Collector output capacitance		C _{ob}	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1 \text{ MHz}$		12		pF	
Switching time	Rise time	tr	See Figure 1 circuit diagram.		50		ns	
	Storage time	t _{stg}	$V_{CC} \simeq -6 \text{ V}, \text{ R}_{L} = 10 \Omega$		115			
	Fall time	t _f	-I _{B1} = I _{B2} = -20 mA		25			

Unit: mm

Marking



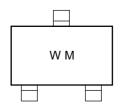
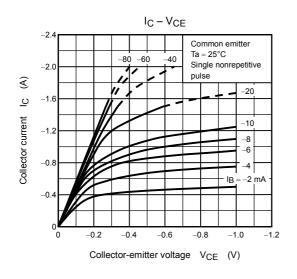
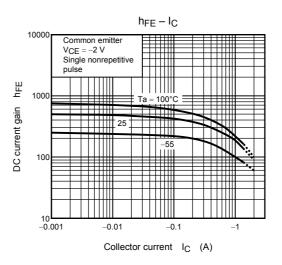
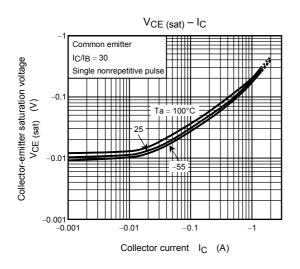


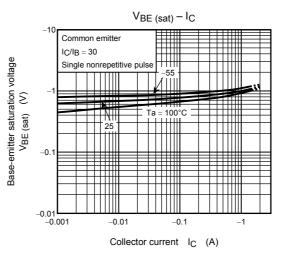
Figure 1 Switching Time Test Circuit & Timing Chart

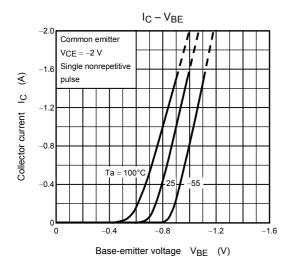
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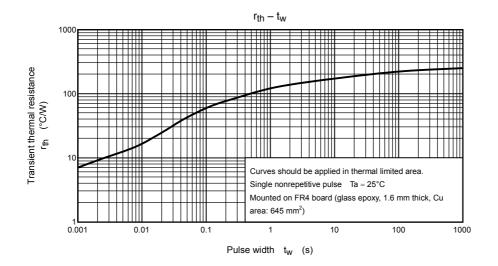












Safe Operating Area -10 IC max (pulsed) 10 ms♦ 1 ms IC max (continuous) € NI 100 ms 🜢 <u>ں</u> DC operation Collector current 10 s♦* (Ta = 25°C) Single nonrepetitive pulse Ta = 25°C
-0.1 Note that the curves for the devices aren't mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²). These characteristic curves must be derated linearly with increase in temperature.
-0.01 ++++ max VCEO -10 -100 Collector-emitter voltage V_{CE} (V)

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