

NE556**LINEAR INTEGRATED CIRCUIT****DUAL TIMER**

The NE556 series dual monolithic timing circuits are a highly stable controller capable of producing accurate time delays or oscillation.

The NE556 is a dual NE555. Timing is provided an external resistor and capacitor for each timing function.

The two timers operate independently of each other, sharing only V_{cc} and ground.

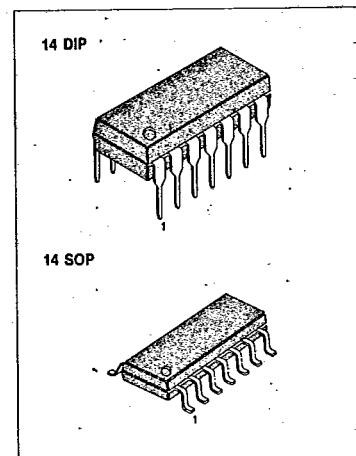
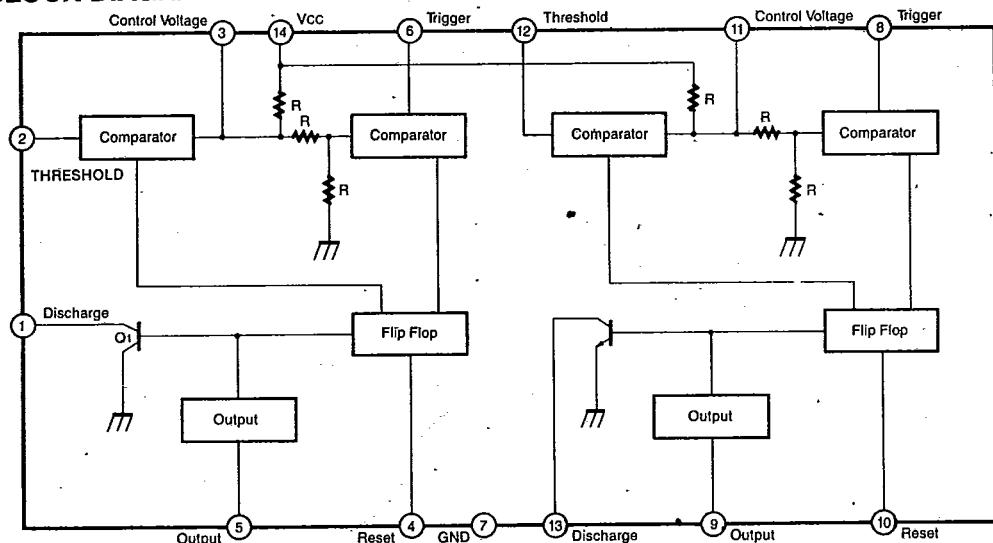
The circuits may be triggered and reset on falling waveforms. The output structures may sink or source 200mA.

FEATURES

- Direct replacement for NE555
- Replace two NE555 timers
- Operates in both astable and monostable modes
- High output current
- TTL compatible
- Timing from microsecond to hours
- Adjustable duty cycle
- Temperature stability of 0.005% per °C

APPLICATIONS

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| <ul style="list-style-type: none"> • Precision timing • Pulse shaping • Pulse width modulation • Frequency division • Traffic light control | <ul style="list-style-type: none"> • Sequential timing • Pulse generator • Time delay generator • Touch tone encoder • Tone burst generator |
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BLOCK DIAGRAM

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ORDERING INFORMATION

Device	Package	Operating Temperature
NE556IN	14 DIP	-40 ~ +85°C
NE556ID	14 SOP	
NE556CN	14 DIP	0 ~ +70°C
NE556CD	14 SOP	



SAMSUNG SEMICONDUCTOR

NE556

LINEAR INTEGRATED CIRCUIT

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

Characteristic	Symbol	Value	Unit
Supply Voltage	V_{CC}	18	V
Lead Temperature (soldering 10 sec)	T_{lead}	300	$^\circ\text{C}$
Power Dissipation	P_D	600	mW
Operating Temperature NE556I NE556C	T_{opr}	-40 ~ +85 0 ~ +70	$^\circ\text{C}$
Storage Temperature	T_{stg}	-65 ~ +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS

(V_{CC} = +5V to +15V, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Supply Voltage	V_{CC}		4.5		16	V
*1 Supply Current (Two timers) (low state)	I_{CC}	$V_{CC} = 5\text{V}, R_L = \infty$ $V_{CC} = 15\text{V}, R_L = \infty$		6 20	12 30	mA mA
*2 Timing Error (monostable)	MT_1	$R_A = 2\text{K}\Omega$ to $100\text{K}\Omega$ $C = 0.1\mu\text{F}$ $T = 1.1R_C$		0.75 50 0.1		% % %/V
Initial Accuracy						
Drift with Temperature						
Drift with Supply Voltage						
Control Voltage	V_C	$V_{CC} = 15\text{V}$	9.0	10.0	11.0	V
		$V_{CC} = 5\text{V}$	2.6	3.33	4.0	V
Threshold Voltage	V_{TH}	$V_{CC} = 15\text{V}$		10.0		V
		$V_{CC} = 5\text{V}$		3.33		V
*3 Threshold Current	I_{TH}			30	250	nA
Trigger Voltage	V_{TR}	$V_{CC} = 15\text{V}$	4.5	5.0	5.6	V
		$V_{CC} = 5\text{V}$	1.1	1.67	2.2	V
Trigger Current	I_{TR}	$V_T = 0\text{V}$		0.5	2.0	μA
*5 Reset Voltage	V_{RE}		0.4	0.7	1.0	V
Reset Current	I_{RE}			0.1	0.6	mA
Output Voltage Low	V_{OL}	$V_{CC} = 15\text{V}$ $I_{sink} = 10\text{mA}$ $I_{sink} = 50\text{mA}$ $I_{sink} = 100\text{mA}$ $I_{sink} = 200\text{mA}$ $V_{CC} = 5\text{V}$ $I_{sink} = 8\text{mA}$ $I_{sink} = 5\text{mA}$		0.1 0.4 2.0 2.5 0.25 0.15	0.25 0.75 2.75 0.35 0.25	V V V V V V



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ELECTRICAL CHARACTERISTICS

(V_{CC} = +5V to +15V, unless otherwise specified)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage (high)	V _{OH}	V _{CC} = 15V I _{source} = 200mA I _{source} = 100mA	12.75	12.5 13.3		V V
		V _{CC} = 5V I _{source} = 100mA	2.75	3.3		V
Rise Time of Output	T _r			100		nsec
Fall Time of Output	T _f			100		nsec
Discharge Leakage Current	I _D			20	100	nA
*4 Matching Characteristics						
Initial Accuracy	M _{CH}			1.0	2.0	% ppm/°C
Drift with Temperature				10		%/°C
Drift with Supply Voltage				0.2	0.5	%/V
*2 Timing Error (astable)						
Initial Accuracy	MT ₂	R _A , R _B = 1kΩ to 100kΩ		2.25		% ppm/°C
Drift with Temperature		C = 0.1μF		150		%/°C
Drift with Supply Voltage		V _{CC} = 15V		0.3		%/V

Notes:

- Supply current when output is high is typically 1.0mA less at V_{CC} = 5V.
- Tested at V_{CC} = 5V and V_{CC} = 15V
- This will determine the maximum value of R_A + R_B for 15V operation.
The maximum total R = 20MΩ, and for 5V operation the maximum total R = 6.6MΩ.
- Matching characteristic refer to the difference between performance characteristics of each timer section in the monostable mode.
- As reset voltage lowers, timing is inhibited and then the output goes low.



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