## VTS-1 Process Photodiodes

13, \_\_\_ VTS

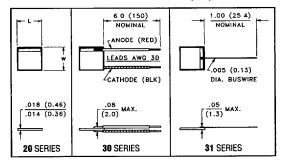
E G & G VACTEC

T-41-51

## PRODUCT DESCRIPTION

Large area planar silicon photodiodes primarily intended for use in the photovoltaic mode. These devices have low series resistance, moderate shunt resistance and high open circuit voltage at nominal light levels for use in power conversion and battery charging applications. Cells have solderable contacts and are available with or without flexible flying leads. Devices with leads are acrylic (plastic) coated.

## PACKAGE DIMENSIONS inch (mm)



## ABSOLUTE MAXIMUM RATINGS

Storage Temperature:

-40°C to 150°C Series 20, 31

-40°C to 105°C Series 30 Operating Temperature:

> -40°C to 125°C Series 20, 31 -40°C to 105°C Series 30

CASE 44A

ANODE (ACTIVE) SURFACE SHOWN CATHODE IS BACKSIDE

DIMENSIONS VTS\_\_13 .200 (5.08) L

.400 (10.16) .200 (5.08) .0322 (212)

.400 (10.16) .1442 (932)

VTS\_\_20

ELECTRO-OPTICAL CHARACTERISTICS @ 25°C (See also VTS-1 curves, page 75)

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**ACTIVE AREA** 

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	VTS13			VTS20						UNITS
			Min	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	0.4110
Isc	Short Circuit Current	H = 100 fc, 2850 K	100	140		460	600					μΑ
TC Isc	Isc Temp. Coefficient	2850 K		.20			.20					%/℃
Isc	Short Circuit Current	H = 100 mW/cm <sup>2</sup>		4.5			19					mA
Voc	Open Circuit Voltage	H = 100 fc, 2850 K		.45			.45					٧
Voc	Open Circuit Voltage	H = 100 mW/cm <sup>2</sup>		.57			.57					٧
TC Voc	Voc Temp. Coefficient	2850 K		-2.0			-2.0					%/℃
lo	Dark Current	$H = 0, V_R = 500 \text{ mV}$		0.5	5		1	50				μΑ
RsH	Shunt Resistance	H = 0, V = 10 mV		.44			.22					MΩ
TC RsH	R <sub>SH</sub> Temp. Coefficient	H = 0, V = 10 mV		-8.0			-8.0					%/℃
CJ	Junction Capacitance	H = 0, V = 0 V		5.5			2.3					nF
λrange	Spectral Application Range		400		1050	400		1050				nm
$\lambda_{p}$	Spectral Response - Peak			875			875					nm
SR	Sensitivity	@ Peak		.51			.51					A/W

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