

STANDARD MICROSYSTEMS  
CORPORATIONT-75-49  
**COM90C56**  
PRELIMINARY

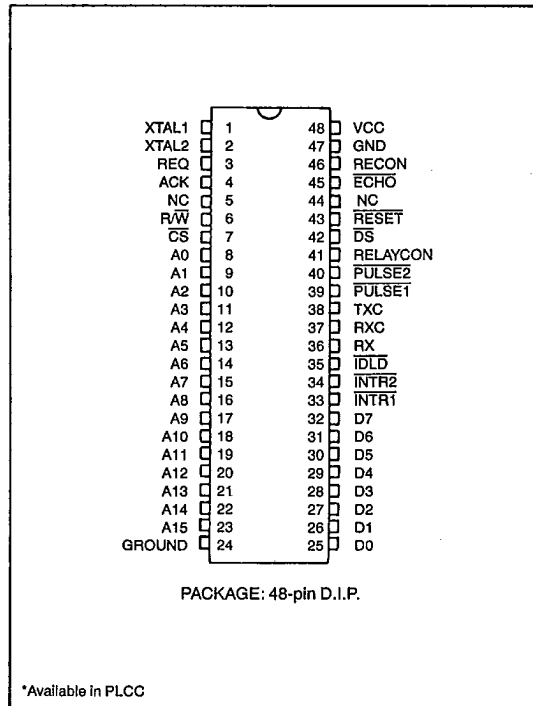
## Enhanced Local Area Network Controller ELANC

SECTION III

### FEATURES

- ☐ 5.0/2.5 M bit data rates
- ☐ 100% compatible with COM9026 (in slow mode) ARCNET local area network controller
- ☐ 64 K byte shared buffer memory
- ☐ Handles variable length data packets (up to 2 K long)
- ☐ Supports up to 255 nodes per network segment
- ☐ Allows 8/16 bit word per sync to enhance line efficiency
- ☐ Supports event scheduling via buffer descriptors
- ☐ On chip network diagnostics
- ☐ Duplicate ID detection/prevention
- ☐ Supports group broadcast messages
- ☐ Provides the hooks for broadband systems (modem)
- ☐ Internal loopback capability for self test
- ☐ On board oscillator
- ☐ Low power CMOS technology
- ☐ 48 pin D.I.P. plastic package or PLCC
- ☐ Single +5v Supply
- ☐ Compatible with HYC9058, HYC9068, HYC9078
- ☐ RAM buffer test capability

### PIN CONFIGURATION\*



Pin configuration subject to change, contact factory for details.

### GENERAL DESCRIPTION

The ELANC is a general purpose communications adapter designed to provide high speed intercommunication between a number of intelligent electrical machines. Data is carried over a variable media (twisted pair, coax, or fiber optics) in variable size packets up to 2048 bytes long at

speeds of up to 5.0 Mbps. The interconnection of several nodes through their associated ELANCs forms an enhanced local area network. Each node has a unique ID number from 1 to 255 to distinguish it from other nodes on the same network.

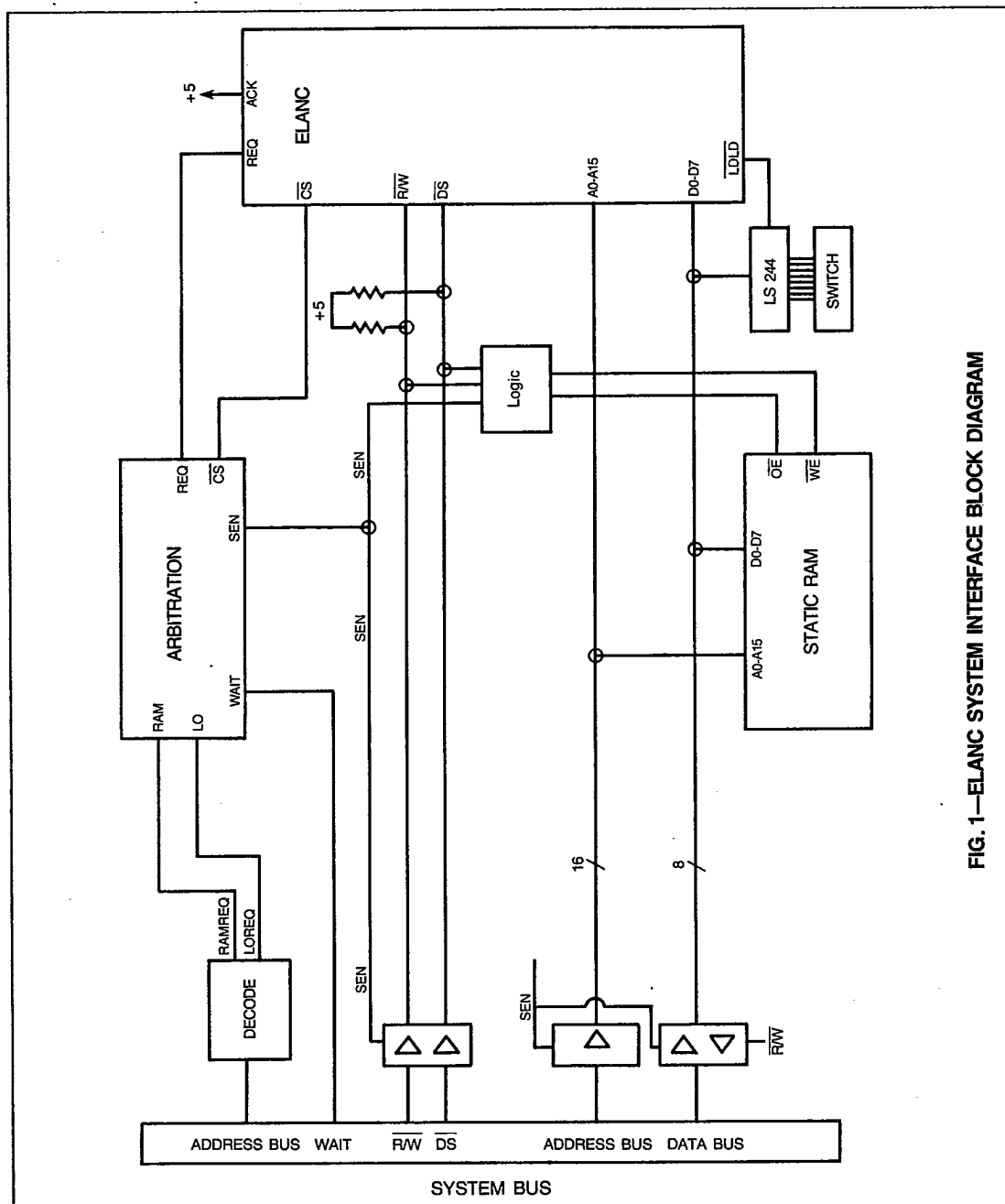


FIG. 1—ELANC SYSTEM INTERFACE BLOCK DIAGRAM

NOTE: For an updated data sheet please fill out the reply card in the back of this catalog or call SMC at (516) 273-3100.

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