TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (L^2 - π -MOSIV)

2SJ312

DC-DC Converter, Relay Drive and Motor Drive Applications

• 4 V gate drive

 $\begin{array}{ll} \bullet & \text{Low drain-source ON resistance} & : R_{DS \; (ON)} = 80 \text{ m}\Omega \; (\text{typ.}) \\ \bullet & \text{High forward transfer admittance} & : | Y_{fs}| = 8.0 \; S \; (\text{typ.}) \\ \end{array}$

• Low leakage current $: I_{DSS} = -100 \,\mu\text{A} \,(\text{max}) \,(V_{DS} = -60 \,\text{V})$

• Enhancement-mode : $V_{th} = -0.8 \sim -2.0 \text{ V (V}_{DS} = -10 \text{ V, I}_{D} = -1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	-60	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	-60	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	-14	Α	
	Pulse(Note 1)	I_{DP}	- 56	^	
Drain power dissipation (Tc = 25°C)		P_{D}	40	W	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Thermal Characteristics

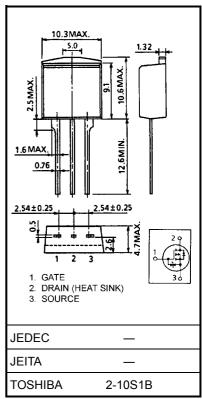
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	83.3	°C/W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

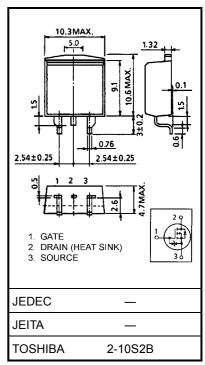
This transistor is an electrostatic sensitive device.

Please handle with caution.

Unit: mm



Weight: 1.5 g (typ.)



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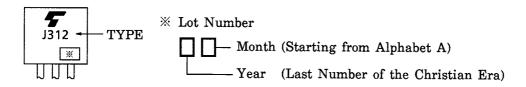
Electrical Characteristics (Ta = 25°C)

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ	
Drain cut-off cur	rent	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V	1	_	-100	μA	
Drain-source bro	eakdown voltage	V _{(BR)DSS}	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-60	_	_	V	
Gate threshold v	oltage	V_{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.8	_	-2.0	V	
Drain-source ON resistance		R _{DS (ON)}	V _{GS} = -4 V, I _D = -5 A	_	130	190	mΩ	
			V _{GS} = -10 V, I _D = -7 A	_	80	120		
Forward transfer	admittance	Y _{fs}	V _{DS} = -10 V, I _D = -7 A	5.0	8.0	_	S	
Input capacitanc	е	C _{iss}		_	1200	_		
Reverse transfer capacitance		C _{rss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	220	_	pF	
Output capacitance		Coss		-	550	_		
Switching time	Rise time	t _r	$V_{GS} \stackrel{0V}{\longrightarrow} I_{D} = -7A$ $V_{CS} \stackrel{0V}{\longrightarrow} V_{OUT}$ $R_{L} = 4.3\Omega$ $V_{DD} = -30V$ $Duty \le 1\%, \ t_{W} = 10\mu s$	_	20	_		
	Turn-on time	t _{on}		1	30	_	ns	
	Fall time	t _f		ı	25	_	. 113	
	Turn-off time	t _{off}		_	100	_		
Total gate charge (Gate-source plus gate-drain)		Qg		_	45	_		
Gate-source charge		Q_{gs}	$V_{DD} \approx -48 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -14 \text{ A}$		30	_	nC -	
Gate-drain ("miller") charge		Q_{gd}			15			

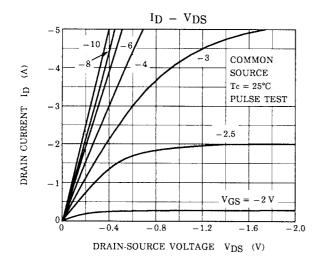
Source-Drain Ratings and Characteristics (Ta = 25°C)

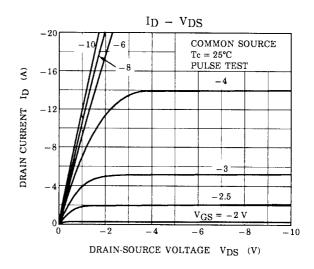
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	-	_	_	-14	Α
Pulse drain reverse current (Note 1)	I _{DRP}	-	_	_	-56	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = -14 A, V _{GS} = 0 V	_	_	1.7	V
Reverse recovery time	t _{rr}	I _{DR} = -14 A, V _{GS} = 0 V		110	_	ns
Reverse recovery charge	Q _{rr}	dI_{DR} / $dt = 50 A / \mu s$		0.18	_	μC

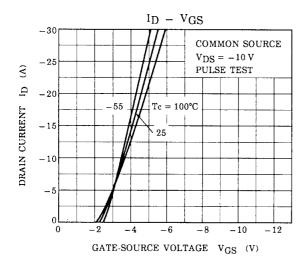
Marking

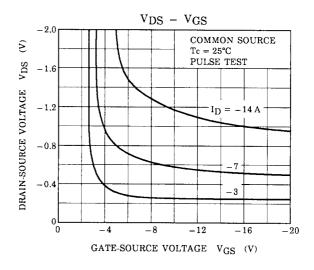


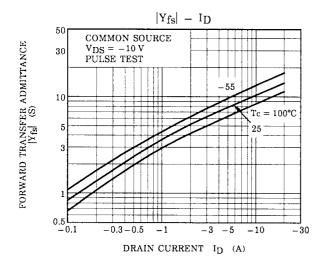
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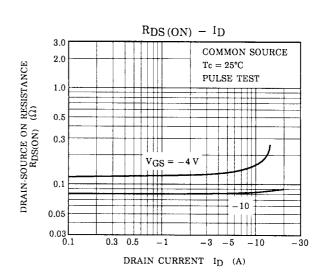




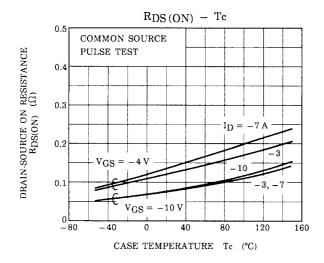


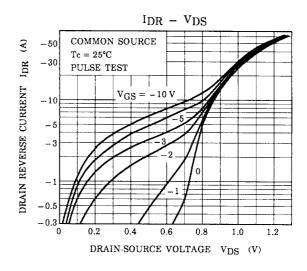


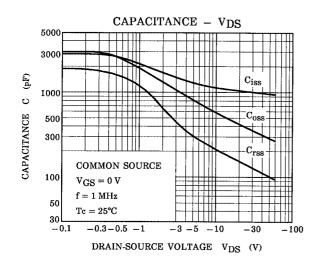


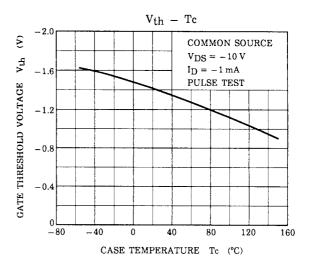


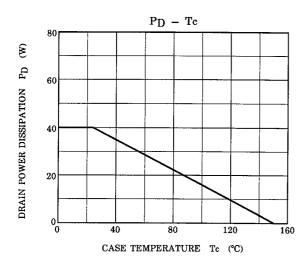
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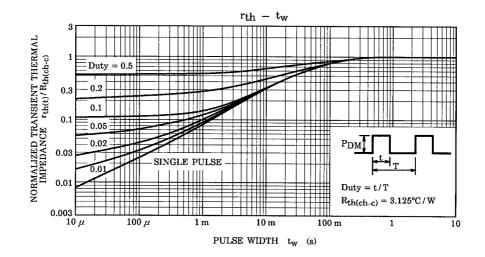


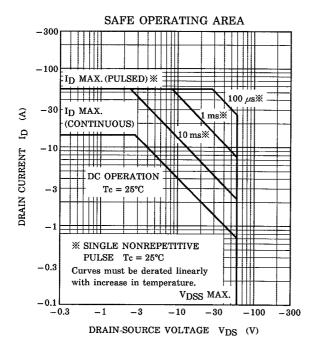












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