



At 25°C free air temperature:

		NJ72 Process				
Static Electrical Characteristics		Min	Typ	Max	Unit	Test Conditions
Gate Source Breakdown Voltage	$V_{(BR)GSS}$	- 25	- 40		V	$I_G = - 1 \mu A, V_{DS} = \emptyset V$
Reverse Gate Leakage Current	$I_{GSS}$		- 10	- 100	pA	$V_{GS} = - 15 V, V_{DS} = \emptyset V$
Drain Saturation Current (Pulsed)	$I_{DSS}$	5		90	mA	$V_{DS} = 15 V, V_{GS} = \emptyset V$
Gate Source Cutoff Voltage	$V_{GS(OFF)}$	- 1		- 5.5	V	$V_{DS} = 15 V, I_D = 1 nA$

Dynamic Electrical Characteristics

Forward Transconductance	$g_{fs}$		22		mS	$V_{DS} = 15 V, V_{GS} = \emptyset V$	$f = 1 \text{ kHz}$
Drain Source ON Resistance	$r_{ds(on)}$		40		$\Omega$	$I_D = 1 \text{ mA}, V_{GS} = \emptyset V$	$f = 1 \text{ kHz}$
Input Capacitance	$C_{iss}$		6.5		pF	$V_{DS} = \emptyset V, V_{GS} = - 10 V$	$f = 1 \text{ MHz}$
Feedback Capacitance	$C_{rss}$		2.5		pF	$V_{DS} = \emptyset V, V_{GS} = - 10 V$	$f = 1 \text{ MHz}$



InterFET Corporation

1000 N. Shiloh Road, Garland, TX 75042  
(972) 487-1287 FAX (972) 276-3375

[www.interfet.co](http://www.interfet.co)



