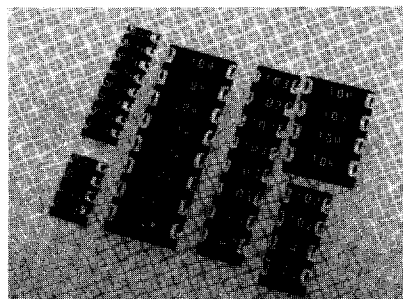


CHIP RESISTOR ARRAY

K 0 A SPEER ELECTRONICS 56 E D ■ 5046087 0000991 67T ■ KOAST-62-05



FLAT CHIP RESISTOR ARRAY

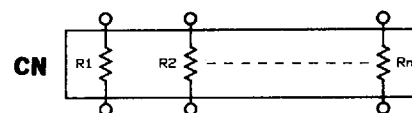
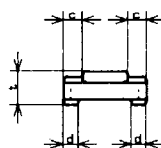
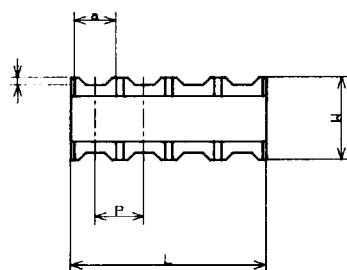
- Manufactured to Type RM Standards
- All Elements Marked with Resistance Value
- Less Board Space than Individual Chips
- Available in 2, 4, & 8 Isolated Elements

DIMENSIONS

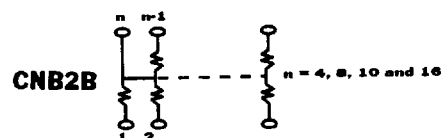
SIZE CODE	DIMENSIONS							
	L	W	C	d Max	t	a	b	p
1J	$\frac{(0.031 \pm 0.008)}{(0.8 \pm 0.2)} \times N$	$\frac{0.063 \pm 0.008}{1.6 \pm 0.2}$	$\frac{0.012 \pm 0.008}{0.3 \pm 0.2}$	$\frac{0.016 \pm 0.006}{0.4 \pm 0.15}$	$\frac{0.016 \pm 0.004}{0.4 \pm 0.1}$	$\frac{0.016 \pm 0.004}{0.40 \pm 0.15}$	$\frac{0.008 \pm 0.004}{0.2 \pm 0.1}$	$\frac{0.031 \pm 0.008}{0.8 \pm 0.2}$
2A	$\frac{(0.050 \pm 0.008)}{(1.27 \pm 0.2)} \times N$	$\frac{0.80 \pm 0.008}{2.0 \pm 0.2}$	$\frac{0.016 \pm 0.012}{0.4 \pm 0.3}$	$\frac{0.022 \pm 0.006}{0.55 \pm 0.15}$	$\frac{0.020 \pm 0.004}{0.5 \pm 0.1}$	$\frac{0.028 \pm 0.006}{0.70 \pm 0.15}$	$\frac{0.006 \pm 0.004}{0.15 \pm 0.10}$	$\frac{0.050 \pm 0.008}{1.27 \pm 0.20}$
2B	$\frac{(0.50 \pm 0.008)}{(1.27 \pm 0.2)} \times N$	$\frac{0.126 \pm 0.008}{3.2 \pm 0.2}$	$\frac{0.020 \pm 0.012}{0.5 \pm 0.3}$	$\frac{0.022 \pm 0.006}{0.55 \pm 0.15}$	$\frac{0.024 \pm 0.004}{0.6 \pm 0.1}$	$\frac{0.028 \pm 0.006}{0.70 \pm 0.15}$	$\frac{0.006 \pm 0.004}{0.15 \pm 0.10}$	$\frac{0.050 \pm 0.008}{1.27 \pm 0.20}$

* CONTROLLING DIMENSIONS IN MILLIMETERS N = 2, 4 or 8

CIRCUIT CONSTRUCTION



$R1 = R2 = \dots = Rn (2, 4, 8)$



STANDARD APPLICATIONS

TYPE	*POWER RATING @70°C	TCR (ppm/°C)	RESISTANCE RANGE (E-24)	RESISTANCE TOLERANCE	MAXIMUM WORKING VOLTAGE	MAXIMUM OVERLOAD VOLTAGE	OPERATING TEMPERATURE RANGE
CN1J	63mW	± 300	10Ω - 1MΩ	J (±5%)	50V	100V	-55°C - +125°C
CN2A	100mW				100V	200V	
CN2B	125mW				200V	400V	
CNB2B	63mW				50V	100V	

* Per Element

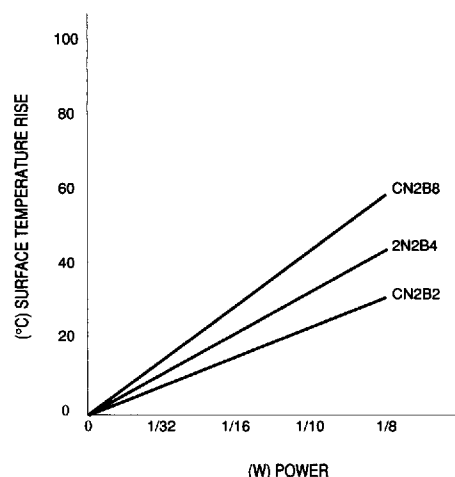
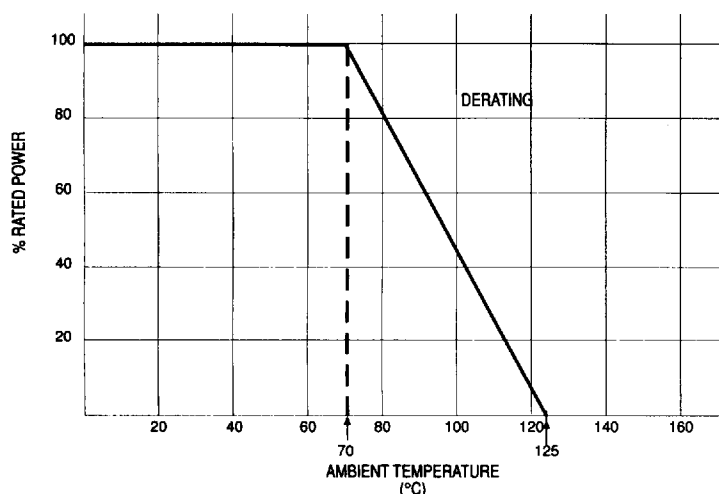
ORDERING & SPECIFYING

CN	2A	4	TE	101	J
TYPE	CODE SIZE	NUMBER OF RESISTORS	PACKAGING	NOMINAL RESISTANCE	TOLERANCE
CN *CNB	(See Above) 1J 2A *2B	2, 4 or 8 * 4, 8, 10 or 16	*TE: Embossed Plastic Tape T: Paper Tape		

*INDICATES CNB SIZE, # OF RESISTORS AND PACKAGING

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ENVIRONMENTAL SPECIFICATIONS



PARAMETER	MAXIMUM Δ R	TEST METHOD
Thermal Shock	± (0.3% + 0.05 Ω)	MIL - STD - 202F, Method 107D -55°C ~ +125°C, 5 Cycles
Low Temperature Operation	± (0.3% + 0.05 Ω)	MIL - R - 55342D π 4.7.4 1 Hour @ -55°C Followed by 45 minutes of RCWV
High Temperature Exposure	± (0.75% + 0.05 Ω)	MIL - R - 55342D π 4.7.6 100 Hours at 125°C
Short Time Overload	± (0.30% + 0.05 Ω)	MIL - R - 55342D π 4.7.5 2.5 x RCWV for 5 Seconds
Resistance to Solder Heat	± (0.30% + 0.05 Ω)	MIL - R - 55342D π 4.7.7 260°C for 10 Seconds
Terminal Strength - Push	± (0.75% + 0.05 Ω)	1.2 Kg for 1 Minute
Terminal Strength - Bend	± (0.50% + 0.05 Ω)	5 mm Deflection in Either Direction for 10 Seconds
Moisture Resistance	± 1.0%	MIL - STD - 202F, Method 106E 10 Cycles, 240 Hours
Life	± 1.5%	MIL - STD - 202F, Method 108A 70°C, 1000 Hours @ RCWV, 1 1/2 Hour ON, 1/2 Hour OFF
Pulse	± 2.5%	2.5 x RCWV, Not Exceeding Maximum Overload Voltage 1 Second ON, 25 Seconds OFF 10,000 Cycles
Temperature Cycling	± 1.0%	30 Minutes at -55°C; 15 Minutes at +25°C; 30 Minutes at +125°C, 15 Minutes at +25°C, 5 Cycles
MINIMUM		
Terminal Adhesion	15 Grams, Minimum	Axial Pull, One Terminal at a Time
Dielectric Withstanding Voltage		
1J	100V	
2A	200V	
2B	400V	
Insulation Resistance	10,000 Meg Ohm,	