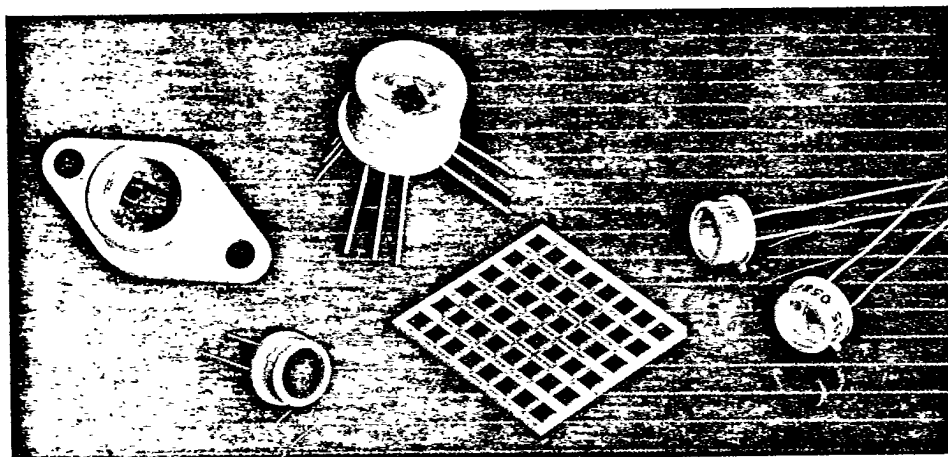


TECHNICAL DATA SHEET

Lead Selenide Detectors



FEATURES

- Spectral Response 1 μm to 6.0 μm
- Superior D*
- Long Term Stability
- Rugged, Compact
- Fast Delivery, Low Cost

Description

Infrared Industries, Inc. Lead Selenide (PbSe) detectors are thin film photoconductors which are chemically deposited onto a quartz substrate. Photolithographic techniques are used to define the active areas. Gold electrodes provide noise-free contact between the lead-out wires and the PbSe film.

The basic detecting element is the sensitized film on a substrate with two electrical leads attached for connection to the outside world. For many instrumentation applications this basic component is adequate, while for others incasement in packages such as TO style is required. These metal packages, which are similar to other semiconductor devices, are rugged and easily handled in next order assembly operations.

Standard active area sizes are typically one, two, or three mm squares; however, Infrared Industries Inc. can provide sizes ranging from 75 microns x 75 microns to 10 millimeter x 10 millimeter in single element detectors. Table 2 shows active area sizes available from stock and the packages

available. Page 3 shows the mechanical drawings of the stock packages. Infrared Industries, Inc. PbSe detectors exhibit excellent stability over a period of many years.

Performance

As a circuit component, the sensor behaves like a variable resistor when connected to a bias supply. Photons induce changes in conductivity which modulate the current flowing through the detector. Changes in circuit voltage or current are then amplified as required by the application.

Background radiation begins to limit signal-to-noise ratio values below -50 degrees C. The effect becomes more pronounced as the temperature is lowered so that at -196 degrees C (liquid nitrogen) peak detectivity is no greater than the value obtained at -78 degrees C (dry ice). This effect can be partially eliminated by attenuating the background energy with cold apertures and/or cold optical filters to restrict the field-of-view and to observe only those wavelengths of interest.

High performance room temperature

formulations and cooling by thermoelectric methods have become very important since they enable greater detecting capability without the logistical problem of gas supplies. Table 3 shows performance characteristics for stock package TE coolers.

Biasing Circuits

Since PbSe is passive until energized, a bias is used. There are three basic methods for biasing. Figure 1: (a) biasing with a matched series load resistor to a constant voltage source; (b) constant current biasing with a low impedance current sensitive termination; (c) constant voltage operation, with a low resistance (relative to the detector) current sensitive termination. The circuit in Figure 1(a) will yield good results and is quite simple; a series circuit with a low impedance voltage supply, a resistor, and the detector. The input of the preamplifier is connected across the detector terminals to allow grounding of one detector lead. The load resistor adds a noise generator which, in some instances, prevents sensor-limited performance and must be considered in the design.

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The constant current configuration (Figure 1b), uses a low impedance current sensitive termination and provides an output signal that is a direct function of the PbSe sensor's output independent of its dark resistance value. The power dissipated in the sensor is a direct function of its dark resistance, which decreases with increasing temperature. Thus, the I^2R heating effect of this configuration will not result in thermal runaway.

Furthermore, there will be a tendency for this bias approach to stabilize

sensor temperature. The constant current approach results in linear performance of the amplifier's input circuit. The constant current method of Figure 1b uses the inverting terminal of an operational amplifier as the current sensitive point. The main disadvantage of current mode biasing is the perfect tracking of variations caused by small detector temperature changes. This can be corrected by introducing temperature compensation elements to the constant current, I_{bias} .

The constant voltage technique (Figure 1c), has some advantages. Constant voltage across the sensor terminals provides excellent thermal compensation; that is, a more constant output when variations in sensor temperature occur. The circuit of (Figure 1c) is good for small active area sensors since only small V_{bias} values can be used with operational amplifiers. It is suggested that amplifiers having operational voltages of ± 22.5 volts be used instead of the traditional ± 15 volts. The V_{bias} value can then be as high as 12 volts.

TABLE I

STOCK DETECTOR PERFORMANCE CHART
PbSe

PARAMETER	OPERATING CONDITIONS					UNITS
Ambient Temperature.	Package 298°K	298	298	298	298	°K
Element Temperature	Package 298°K	298	253	243	203	°K
		25°C	-20°C	-30°C	-70°C	
D* (D-Star) Typical	500°K, 750Hz, 1Hz	2.8x10E8	1.0x10E9	1.8x10E9	4.0x10E9	cmHz 1/2w-1
D* (D-Star) Typical	pk,750Hz,1Hz	2.5x10E9	7.5x10E9	1.3x10E10	2.0x10E10	cmHz 1/2w-1
Spectral Response		1-4.8	1-5.0	1-5.2	1-5.6	Micron
Resistance (Dark)		0.05-10	0.2-15	0.25-20	0.4-50	Megohms
Time Constant		0.5-10	3-50	5-60	10-100	µSec
Responsivity (Minimum)	pk,750Hz,1Hz 1mm x 1mm 2mm x 2mm 3mm x 3mm	6000 3000 2000	9000 5000 3000	13000 8000 5000	50000 25000 15000	Volts/Watt
Package Type		Plate TO Type	TO Type	TO Type	TO Type	

D* MAY BE UP TO 30% LESS THAN STATED VALUE UNLESS SPECIFIED BY CUSTOMER ORDER.

TABLE II

STOCK DETECTORS

ELEMENT SIZE (mm's)	SIZE NO.	SERIES NUMBER				
		4000	5000	5600C	5700C	5900
		PACKAGE NUMBER				
1 x 1	3	Plate	TO-5	TO-37	TO-37	TO-3(S)
		4003	5035	5637C TO-8 5638C	5737C	5933C
2 x 2	5	Plate	TO-5	TO-37	TO-37	TO-3(S)
		4005	5055	5657C TO-8 5658C	5757C	5953C
3 x 3	6	Plate	TO-5	TO-37	TO-37	TO-3(S)
		4006	5065	5667C TO-8 5668C	5767C	5963C

"C" AT THE END OF SERIES NUMBER DENOTES CALIBRATED THERMISTOR.

TABLE III

THERMO-ELECTRIC COOLERS (TYPICAL)

Cooler Type	Delta T Max (°C) dry N ₂	Q max (watts) (amps)	I max (volts)	V max
Single stage	64	1.01	2.0	0.8
Two stage	88	0.35	1.7	0.9
Three stage	101	0.72	1.4	3.6

Thermoelectric coolers are solid state, vibration free, noise free heat pumps. "Q" max is the net heat load which completely suppresses ΔT at the maximum applied power (I max @ V max). Example: The three stage cooler produces 101° C ΔT in dry nitrogen @ 1.4 amps @ 3.6 volts. It will take 0.72 watts to completely suppress that ΔT to zero. This suppression is linear so that 10% of Q max (0.072) applied to the cold plate would suppress ΔT 10% thus producing 90.9° C ΔT for that heat load condition. "Q" is dependent on detector size, leads, thermistors, etc. Consult the factory. TE Cooler data is based on hot side temperature of 27°C.

COMMON CIRCUITS FOR LEAD SELENIDE

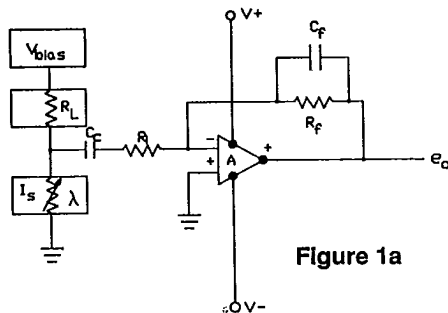


Figure 1a

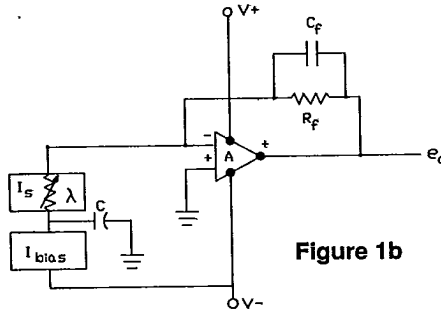


Figure 1b

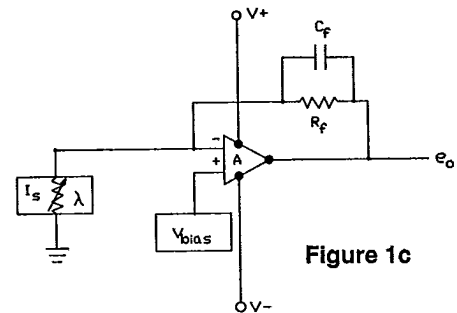
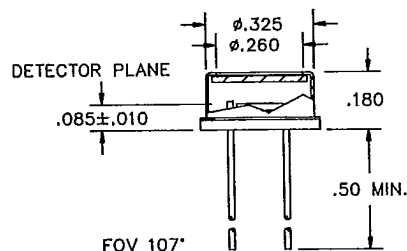


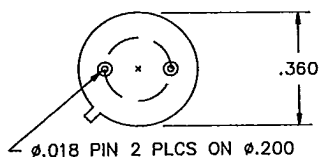
Figure 1c

STOCK PACKAGES

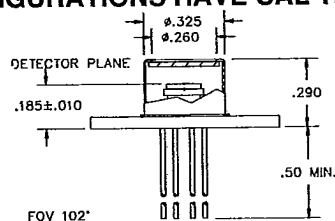
HERMETIC SEAL, UNCOOLED (5000)



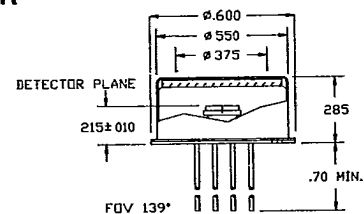
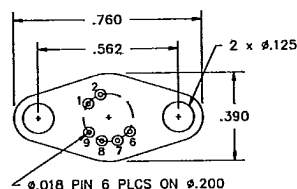
TO-5



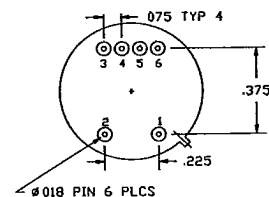
HERMETIC SEAL, 1 STAGE, TE COOLED (5600) BELOW PIN CONFIGURATIONS HAVE CAL THERMISTOR



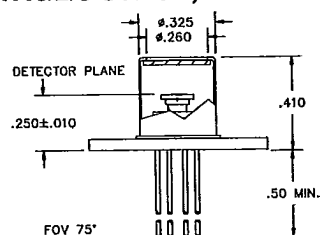
TO-37



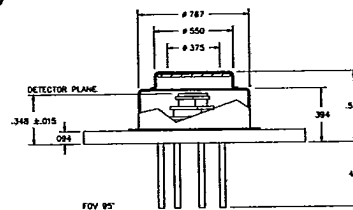
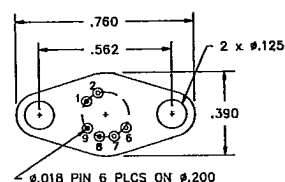
TO-8



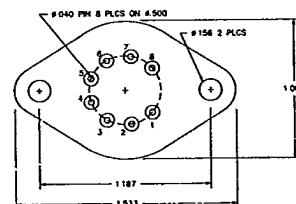
HERMETIC SEAL, 2 STAGE/3 STAGE, TE COOLERS (5700/5900)



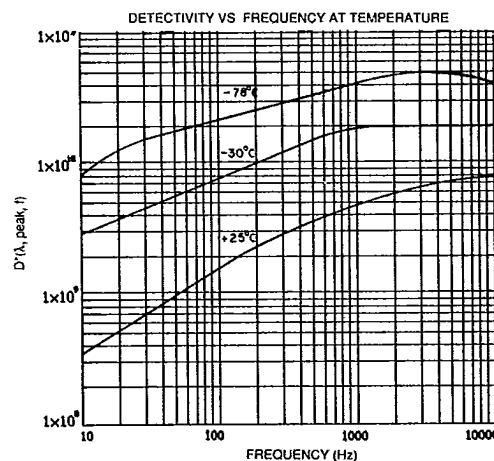
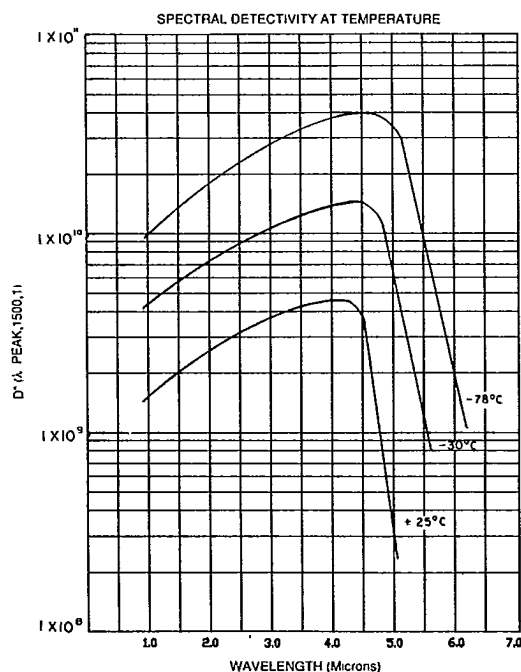
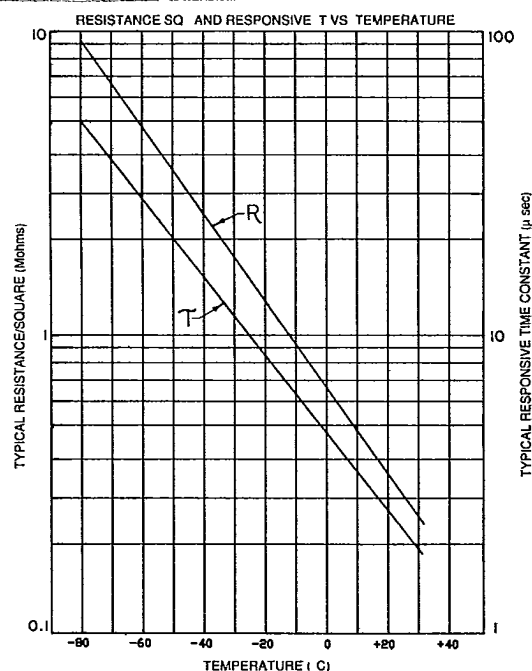
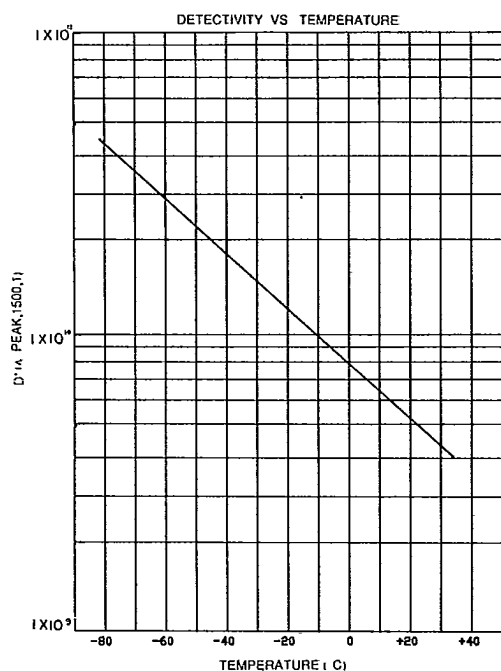
TO-37



TO-3



TECHNICAL DATA SHEET

TYPICAL PERFORMANCE CURVES (2 π STERADIAN FOV, 298°K BACKGROUND)

Custom detectors are available.

Infrared Industries, Inc. also manufactures bias supplies, preamplifiers, TE controllers, and test equipment which can be used in conjunction with lead selenide detectors.