

#### **General Description**

The MAX4173 low-cost, precision, high-side currentsense amplifier is available in a tiny SOT23-6 package. It features a voltage output that eliminates the need for gain-setting resistors and it is ideal for today's notebook computers, cell phones, and other systems where current monitoring is critical. High-side current monitoring is especially useful in battery-powered systems, since it does not interfere with the ground path of the battery charger. The input common-mode range of 0 to +28V is independent of the supply voltage and ensures that the current-sense feedback remains viable even when connected to a battery in deep discharge. The MAX4173's wide 1.7MHz bandwidth makes it suitable for use inside battery charger control loops.

The combination of three gain versions and a userselectable external sense resistor sets the full-scale current reading. This feature offers a high level of integration, resulting in a simple and compact currentsense solution.

The MAX4173 operates from a single +3V to +28V supply, typically draws only 420µA of supply current over the extended operating temperature range (-40°C to +85°C), and is offered in the space-saving SOT23 package.

#### **Applications**

Notebook Computers

Portable/Battery-Powered Systems

Smart Battery Packs/Chargers

Cell Phones

Power-Management Systems

General System/Board-Level Current Monitoring

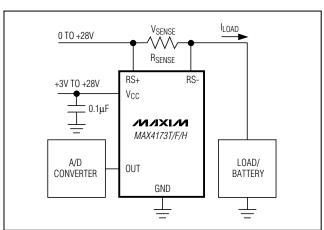
PA Bias Control

Precision Current Sources

#### **Features**

- **♦ Low-Cost, Compact Current-Sense Solution**
- ♦ High Accuracy +2V to +28V Common-Mode Range, Functional Down to 0V, Independent of Supply Voltage
- **♦ Three Gain Versions Available** 
  - +20V/V (MAX4173T)
  - +50V/V (MAX4173F)
  - +100V/V (MAX4173H)
- **♦** ±0.5% Full-Scale Accuracy
- ♦ ±3mV Input Offset Voltage (MAX4173T)
- ♦ Wide 1.7MHz Bandwidth (MAX4173T)
- ♦ 420µA Supply Current
- ♦ Available in Space-Saving SOT23 Package

### **Typical Operating Circuit**



### **Ordering Information**

PART	GAIN (V/V)	TEMP RANGE	PIN-PACKAGE	SOT TOP MARK
MAX4173TEUT+T	20	-40°C to +85°C	6 SOT23	AABN
MAX4173TESA+	20	-40°C to +85°C	8 SO	_
MAX4173FEUT+T	50	-40°C to +85°C	6 SOT23	AABO
MAX4173FESA+	50	-40°C to +85°C	8 SO	_
MAX4173HEUT+T	100	-40°C to +85°C	6 SOT23	AABP
MAX4173HESA+	100	-40°C to +85°C	8 SO	_

<sup>+</sup>Denotes a lead(Pb)-free/RoHS-compliant package.

Pin Configurations appear at end of data sheet.

T = Tape and reel.

#### **ABSOLUTE MAXIMUM RATINGS**

V <sub>CC</sub> , RS+, RS- to GND	0.3V to +30V
OUT to GND	$-0.3V$ to $(V_{CC} + 0.3V)$
Output Short-Circuit to VCC or GND	Continuous
Differential Input Voltage (V <sub>RS+</sub> - V <sub>RS-</sub> )	±0.3V
Current into Any Pin	±20mA

Continuous Power Dissipation ( $T_A = +70^{\circ}C$ )	
8-Pin SO (derate 5.88mW/°C above +70°C)	471mW
SOT23-6 (derate 8.7mW/°C above +70°C)	696mW
Operating Temperature Range	40°C to +85°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C
Soldering Temperature (reflow)	+260°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **ELECTRICAL CHARACTERISTICS**

 $(V_{RS+} = 0 \text{ to } +28V, V_{CC} = +3V \text{ to } +28V, V_{SENSE} = 0V, T_A = T_{MIN} \text{ to } T_{MAX}, R_{LOAD} = \infty \text{ unless otherwise noted.}$  Typical values are at  $T_A = +25^{\circ}C$ .) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS	
Operating Voltage Range	Vcc	Guaranteed by PSR test		3		28	V	
Common-Mode Input Range	V <sub>CMR</sub>	(Note 2)		0		28	V	
Common-Mode Rejection	CMR	V <sub>RS+</sub> > +2.0V			90		dB	
Supply Current	Icc	$V_{RS+} > +2.0V, V_{CC} > 12V$			0.42	1.0	mA	
Leakage Current	I <sub>RS+</sub> , I <sub>RS-</sub>	$V_{CC} = 0V, V_{RS+} = 28V$			0.3	3	μΑ	
	Inc	V <sub>RS+</sub> > +2.0V		0		50		
Input Dies Current	I <sub>RS+</sub>	V <sub>RS+</sub> ≤ +2.0V		-350		50	[ <b>,</b> [	
Input Bias Current	lee	V <sub>RS+</sub> > +2.0V		0		100	μΑ	
	I <sub>RS</sub> -	V <sub>RS+</sub> ≤ +2.0V		-700		100		
Full-Scale Sense Voltage	VSENSE	V <sub>SENSE</sub> = V <sub>RS+</sub> - V <sub>RS-</sub>			150		mV	
		V <sub>SENSE</sub> = +100mV, V <sub>CC</sub> =	$+12V$ , $V_{RS+} = +12V$		±0.5	5.75	5	
		$V_{SENSE} = +100$ mV, $V_{CC} = +12$ V, $V_{RS+} = +12$ V, $T_A = +25$ °C			0.5	3.25	%	
Total OUT Voltage Error (Note 3)		V <sub>SENSE</sub> = +100mV, V <sub>CC</sub> = +28V, V <sub>RS+</sub> = +28V			0.5	5.75		
(Note 3)		V <sub>SENSE</sub> = +100mV, V <sub>CC</sub> = +12V, V <sub>RS+</sub> = +0.1V			-9	±24		
		$V_{CC} = +12V, V_{RS+} = +12V,$ (Note 4)	, V <sub>SENSE</sub> = +6.25mV		±7.5			
		MAX4173T, V <sub>CC</sub> = +3.0V, V V <sub>SENSE</sub> = 250mV	/ <sub>RS+</sub> = 28V,		0.8	1.2		
Out High Voltage (Note 5)	(VCC - VOH)	MAX4173F, V <sub>CC</sub> = +7.5V, V <sub>RS+</sub> = 28V, V <sub>SENSE</sub> = 250mV			0.8	1.2	V	
		MAX4173H, V <sub>CC</sub> = +15V, V V <sub>SENSE</sub> = 250mV	/ <sub>RS+</sub> = 28V,		0.8	1.2		
OUT Law Valtage	V <sub>OL</sub>	MAX4173TEUT, VCC = +5V, V <sub>RS+</sub> = 0.89V,	T <sub>A</sub> = +25°C		1.2	5	mV	
OUT Low Voltage	VOL	$V_{SENSE} = 0mV$	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$			40	IIIV	

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#### **ELECTRICAL CHARACTERISTICS (continued)**

 $(V_{RS+} = 0 \text{ to } +28V, V_{CC} = +3V \text{ to } +28V, V_{SENSE} = 0V, T_A = T_{MIN} \text{ to } T_{MAX}, R_{LOAD} = \infty \text{ unless otherwise noted.}$  Typical values are at  $T_A = +25^{\circ}C.$ ) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
		V <sub>RS+</sub> = +12V,	MAX4173T, VSENSE = +100mV		1.7		
Bandwidth	BW		MAX4173F, VSENSE = +100mV		1.4		MHz
Dariuwiutii	DVV	VCC = +12V, $C_{LOAD} = 5pF$	MAX4173H, VSENSE = +100mV		1.2		
			V <sub>SENSE</sub> = +6.25mV (Note 4)		0.6		
		MAX4173T			20		
Gain	Av	MAX4173F			50		V/V
		MAX4173H			100		
Gain Accuracy	ΔΑγ	MAX4173T/F VSENSE = +10mV to +150mV, VCC = VRS+ = 12V	T <sub>A</sub> = +25°C		0.5	±2.5	- %
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$			4.0	
	ΔΑγ	MAX4173H V <sub>SENSE</sub> = +10mV to +100mV, V <sub>CC</sub> = V <sub>RS+</sub> = 12V	T <sub>A</sub> = +25°C		0.5	±2.5	
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$			4.0	
Input Offset Voltage	Vos	MAX4173TEUT,	$T_A = +25^{\circ}C$		0.3	±3	mV
(Note 6)	VUS	$V_{RS+} = 12V$	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$			±5	1110
OUT Settling Time to 1% of		$V_{CC} = +12V$ , $V_{RS+} = 12V$ , $C_{LOAD} = 5pF$	$V_{SENSE} = +6.25$ mV to +100mV		400		- ns
Final Value			V <sub>SENSE</sub> = +100mV to +6.25mV		800		
OUT Output Resistance	Rout				12		kΩ
Power-Supply Rejection		MAX4173T, V <sub>SENSE</sub> = 80mV, V <sub>RS+</sub> ≥ +2V		60	84		
	PSR	MAX4173F, V <sub>SENSE</sub> = 32mV, V <sub>RS+</sub> ≥ +2V		60	91		dB
		MAX4173H, V <sub>SENSE</sub> = 16mV, V <sub>RS+</sub> ≥ +2V		60	95		
Power-Up Time to 1% of Final Value		V <sub>SENSE</sub> = +100mV, C <sub>LOAD</sub> = 5pF			10		μs
Saturation Recovery Time		V <sub>CC</sub> = +12V, V <sub>RS+</sub> = 12V (Note 7)			10		μs

**Note 1:** All devices are 100% production tested at  $T_A = +25$ °C. All temperature limits are guaranteed by design.

Note 2: Guaranteed by Total Output Voltage Error Test.

Note 3: Total OUT Voltage Error is the sum of gain and offset voltage errors.

Note 4: +6.25mV = 1/16 of +100mV full-scale voltage.

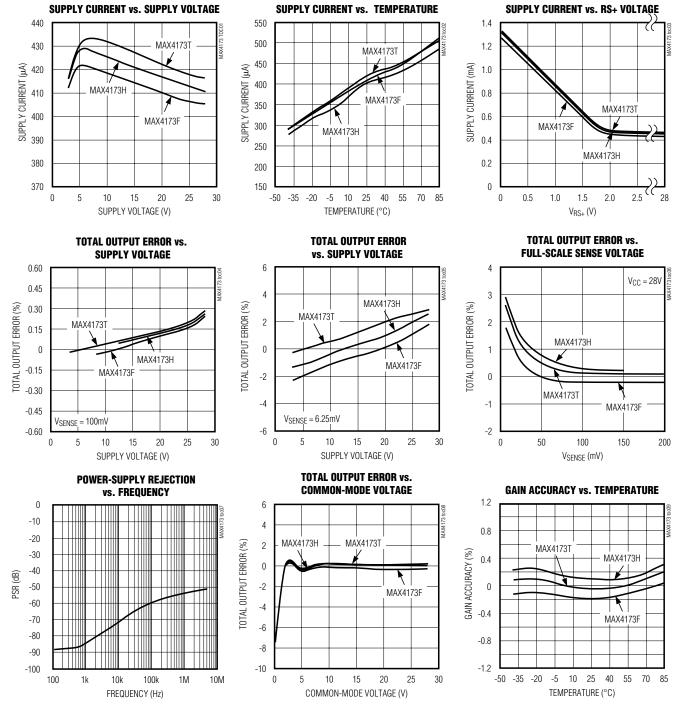
**Note 5:** V<sub>SENSE</sub> such that output stage is in saturation.

Note 6: VOS is extrapolated from the Gain Accuracy tests.

Note 7: The device does not experience phase reversal when overdriven.

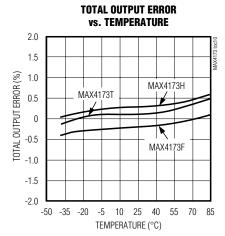
### **Typical Operating Characteristics**

(VCC = +12V, VRS+ = +12V, VSENSE = +100mV, TA = +25°C, unless otherwise noted.)

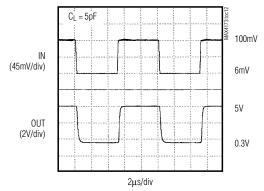


## Typical Operating Characteristics (continued)

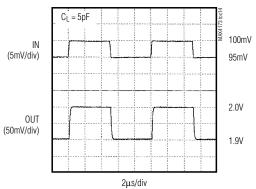
(VCC = +12V, VRS+ = +12V, VSENSE = +100mV, TA = +25°C, unless otherwise noted.)



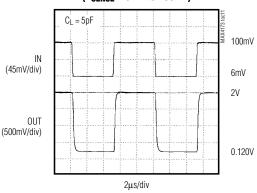
MAX4173F LARGE-SIGNAL TRANSIENT RESPONSE (VSENSE = 6mV to 100mV)



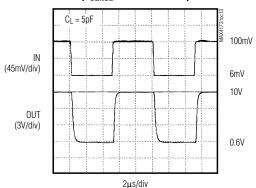
MAX4173T SMALL-SIGNAL TRANSIENT RESPONSE (VSENSE = 95mV TO 100mV)



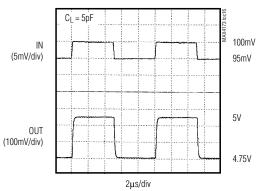
MAX4173T LARGE-SIGNAL TRANSIENT RESPONSE (VSENSE = 6mV to 100mV)



MAX4173H LARGE-SIGNAL TRANSIENT RESPONSE (VSENSE = 6mV to 100mV)



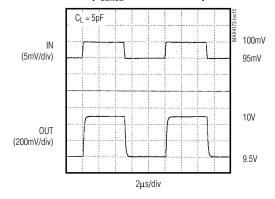
MAX4173F SMALL-SIGNAL TRANSIENT RESPONSE (VSENSE = 95mV TO 100mV)



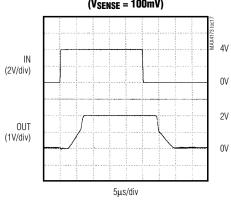
## Typical Operating Characteristics (continued)

 $(V_{CC} = +12V, V_{RS+} = +12V, V_{SENSE} = +100mV, T_A = +25^{\circ}C, unless otherwise noted.)$ 

#### MAX4173H SMALL-SIGNAL TRANSIENT RESPONSE (VSENSE = 95mV to 100mV)



#### START-UP DELAY (V<sub>CC</sub> = 0 to 4V) (V<sub>SENSE</sub> = 100mV)



## **Pin Description**

PIN		NAME	FUNCTION	
SOT23	so	NAME.		
1, 2	3	GND	Ground	
3	1	Vcc	Supply Voltage Input. Bypass to GND with a 0.1µF capacitor.	
4	8	RS+	Power-Side Connection to the External Sense Resistor	
5	6	RS-	Load-Side Connection for the External Sense Resistor	
6	4	OUT	Voltage Output. Vout is proportional to Vsense ( Vrs+ - Vrs- ). Output impedance is approximately $12k\Omega$ .	
_	2, 5, 7	N.C.	No Connection. Not internally connected.	

### **Detailed Description**

The MAX4173 high-side current-sense amplifier features a 0 to +28V input common-mode range that is independent of supply voltage. This feature allows the monitoring of current out of a battery in deep discharge and also enables high-side current sensing at voltages greater than the supply voltage (VCC).

The MAX4173 operates as follows: Current from the source flows through RSENSE to the load (Figure 1). Since the internal-sense amplifier's inverting input has high impedance, negligible current flows through RG2 (neglecting the input bias current). Therefore, the sense amplifier's inverting-input voltage equals VSOURCE - (ILOAD)(RSENSE). The amplifier's open-loop gain forces its noninverting input to the same voltage as the inverting input. Therefore, the drop across RG1 equals (ILOAD)(RSENSE). Since IRG1 flows through RG1, IRG1 = (ILOAD)(RSENSE) / RG1. The internal current mirror multiplies IRG1 by a current gain factor, β, to give  $IRGD = \beta \cdot IRG1$ . Solving  $IRGD = \beta \cdot (ILOAD)(RSENSE) /$ RG1. Assuming infinite output impedance, Vout = (IRGD) (RGD). Substituting in for IRGD and rearranging, Vout =  $\beta \cdot (RGD / RG1)(RSENSE \cdot ILOAD)$ . The parts gain equals  $\beta \cdot RGD / RG1$ . Therefore,  $V_{OUT} = (GAIN) (R_{SENSE})$  $(I_{I} \cap AD)$ , where GAIN = 20 for MAX4173T, GAIN = 50 for MAX4173F. and GAIN = 100 for MAX4173H.

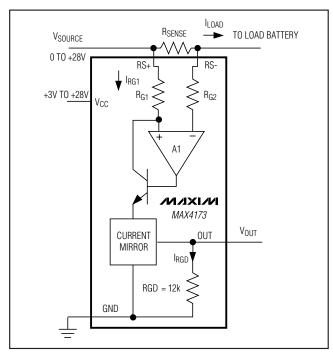


Figure 1. Functional Diagram

Set the full-scale output range by selecting RSENSE and the appropriate gain version of the MAX4173.

## Applications Information

#### **Recommended Component Values**

The MAX4173 senses a wide variety of currents with different sense resistor values. Table 1 lists common resistor values for typical operation of the MAX4173.

#### **Choosing RSENSE**

To measure lower currents more accurately, use a high value for RSENSE. The high value develops a higher sense voltage that reduces offset voltage errors of the internal op amp.

In applications monitoring very high currents, RSENSE must be able to dissipate the  $\rm I^2R$  losses. If the resistor's rated power dissipation is exceeded, its value may drift or it may fail altogether, causing a differential voltage across the terminals in excess of the absolute maximum ratings.

If ISENSE has a large high-frequency component, minimize the inductance of RSENSE. Wire-wound resistors have the highest inductance, metal-film resistors are somewhat better, and low-inductance metal-film resistors are best suited for these applications.

#### Using a PCB Trace as RSENSE

If the cost of RSENSE is an issue and accuracy is not critical, use the alternative solution shown in Figure 2. This solution uses copper PC board traces to create a sense resistor. The resistivity of a 0.1-inch-wide trace of 2-ounce copper is approximately  $30m\Omega/\text{ft}$ . The resistance-temperature coefficient of copper is fairly high (approximately 0.4%/°C), so systems that experience a wide temperature variance must compensate for this effect. In addition, do not exceed the maximum power dissipation of the copper trace.

For example, the MAX4173T (with a maximum load current of 10A and an RSENSE of  $5m\Omega$ ) creates a full-scale VSENSE of 50mV that yields a maximum VOUT of 1V. RSENSE in this case requires about 2 inches of 0.1 inchwide copper trace.

#### **Output Impedance**

The output of the MAX4173 is a current source driving a  $12k\Omega$  resistance. Resistive loading added to OUT reduces the output gain of the MAX4173. To minimize output errors for most applications, connect OUT to a high-impedance input stage. When output buffering is required, choose an op amp with a common-mode input range and an output voltage swing that includes ground when operating with a single supply. The op

Table 1. Recommended Component Values

FULL-SCALE LOAD CURRENT ILOAD (A)	CURRENT-SENSE RESISTOR RSENSE (m $\Omega$ )	GAIN	FULL-SCALE OUTPUT VOLTAGE (FULL-SCALE V <sub>SENSE</sub> = 100mV) V <sub>OUT</sub> (V)
		20	2.0
0.1	1000	50	5.0
		100	10.0
		20	2.0
1	100	50	5.0
		100	10.0
		20	2.0
5	20	50	5.0
		100	10.0
		20	2.0
10	10	50	5.0
		100	10.0

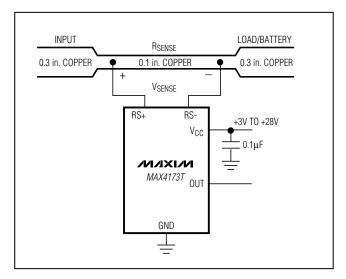


Figure 2. MAX4173 Connections Showing Use of PC Board

amp's supply voltage range should be at least as high as any voltage the system may encounter.

The percent error introduced by output loading is determined with the following formula:

$$\%_{ERROR} = 100 \left( \frac{R_{LOAD}}{12k\Omega + R_{LOAD}} - 1 \right)$$

where RLOAD is the external load applied to OUT.

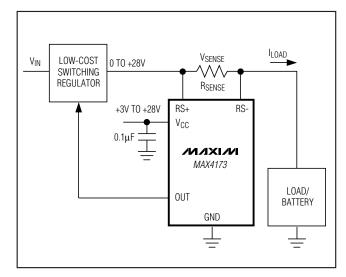
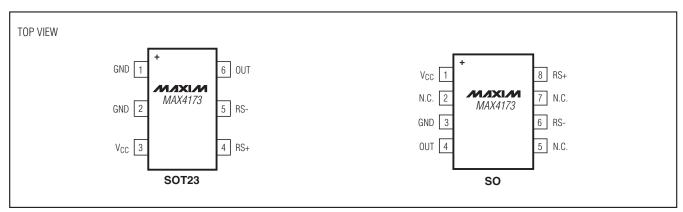


Figure 3. Current Source

#### **Current Source Circuit**

Figure 3 shows a block diagram using the MAX4173 with a switching regulator to make a current source.

## **Pin Configurations**



## **Package Information**

For the latest package outline information and land patterns (footprints), go to <a href="www.maxim-ic.com/packages">www.maxim-ic.com/packages</a>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
8 SO	S8+4	21-0041	90-0096
6 SOT23	U6+2	21-0058	<u>90-0175</u>

### **Revision History**

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
4	6/10	Clarified 0V to 2V is not a high-accuracy range for the device, added lead-free options and soldering temperature	1, 2
5	4/11	Updated V <sub>RS+</sub> conditions to synchronize with tested material	2, 3

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