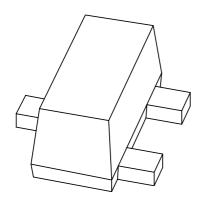
DISCRETE SEMICONDUCTORS

DATA SHEET



2PA1774JPNP general purpose transistor

Product specification Supersedes data of 2000 Dec 12 2001 Aug 03





PNP general purpose transistor

2PA1774J

FEATURES

- Power dissipation comparable to SOT23
- Low output capacitance
- Low saturation voltage V_{CEsat}
- Low current (max. 100 mA)
- Low voltage (max. 50 V).

APPLICATIONS

• General purpose switching and amplification in miniaturized application areas such as telecom and multimedia.

DESCRIPTION

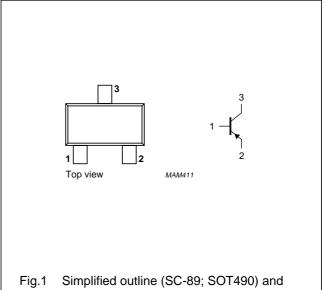
PNP transistor encapsulated in an ultra small plastic SMD SC-89 (SOT490) package. NPN complement: 2PC4617J.

MARKING

TYPE NUMBER	MARKING CODE
2PA1774QJ	YQ
2PA1774RJ	YR
2PA1774SJ	YS

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-50	V
V _{CEO}	collector-emitter voltage	open base	_	-50	V
V _{EBO}	emitter-base voltage	open collector	_	- 5	V
I _C	collector current (DC)		_	-100	mA
I _{CM}	peak collector current		_	-200	mA
I _{BM}	peak base current		_	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Refer to SC-89 (SOT490) standard mounting conditions.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
R _{th j-a}	thermal resistance from junction to ambient	in free air; note 1	500	K/W

Note

1. Refer to SC-89 (SOT490) standard mounting conditions.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = -30 V	_	-100	nA
		$I_E = 0$; $V_{CB} = -30 \text{ V}$; $T_j = 150 ^{\circ}\text{C}$	_	-5	μΑ
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = -4 V	_	-100	nA
h _{FE}	DC current gain	$I_C = -1 \text{ mA}; V_{CE} = -6 \text{ V}; \text{ note 1}$			
	2PA1774QJ		120	270	
	2PA1774RJ		180	390	
	2PA1774SJ		270	560	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -50 \text{ mA}$; $I_B = -5 \text{ mA}$; note 1	-	-200	mV
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = -12 \text{ V}$; $f = 1 \text{ MHz}$	_	2.2	pF
f _T	transition frequency	$I_C = -2 \text{ mA}; V_{CE} = -12 \text{ V};$ f = 100 MHz; note 1	100	_	MHz

Note

1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

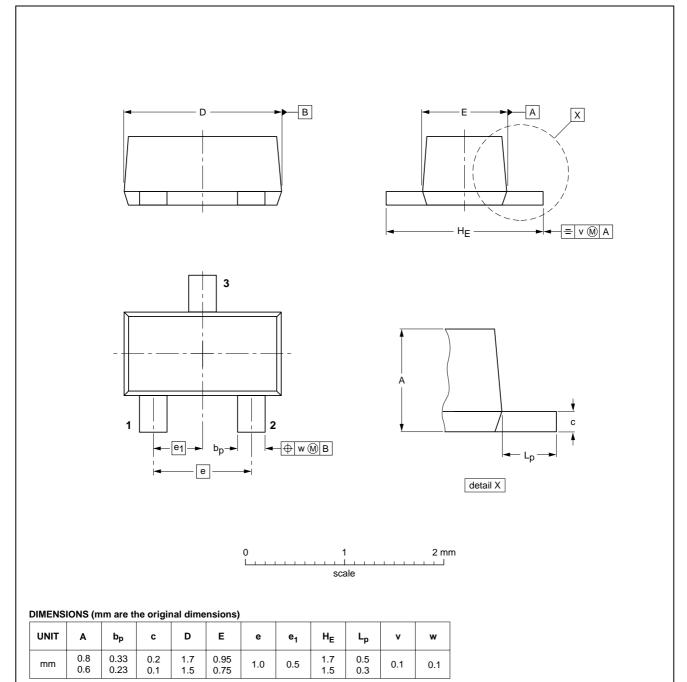
PNP general purpose transistor

2PA1774J

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT490



OUTLINE	REFERENCES		EUROPEAN	ICCUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT490			SC-89			98-10-23

PNP general purpose transistor

2PA1774J

DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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NOTES

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NOTES

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